

Clustering for IBL

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Introduction

- IBL modules have two different geometries
 - 3D modules contain one FE chip
 - Planar modules contain two FE chips

Input for Hardware

- Start from RDO mc14 dataset
- Create Bytestream file
- To create HW input, a python script is used
 - Searches for specific ROD and extracts its data
 - Optionally, strips planar modules
 - Also creates human-readable file for easier understanding

Comparison for IBL

- Python script has been developed
- Accepts HW and SW outputs directly, keeping only the data stream
- Creates identical format for both outputs
- Uses UNIX diff for the comparison

IBL Current Status

- Tests have been performed for 3D modules and for one ROD
- Current agreement between HW and SW is 100%
- Planar modules have not been tested

FTK Resolution Status

- Dataset containing only will be examined
- Scripts for creating the necessary files have been recently modified
 - Testing with new setup is ongoing