

## MINUTES

The following representatives have been invited to the EB:

Mauro Dell'Orso – UNIPi, Project Coordinator, Chair	present in Pisa
Kostas Kordas – AUTH, WP 7 Coordinator, Member	connected by skype from AUTH
Dimitrios Sampsonidis – AUTH, WP 8 Coordinator, Member	connected by skype from AUTH
Paola Garosi – CAEN, WP 2 Coordinator, Member	connected by skype from CAEN
Francesco Crescioli – CNRS, WP 6 Coordinator, Member	connected by skype from LPNHE
Andreas Sakellariou – PRIELE, WP 1 Coordinator, Member	connected by skype from Prisma (Athens)
Marco Piendibene – UNIPi, WP 5 Coordinator, Member	present in Pisa
Chiara Roda – UNIPi, WP 4 Coordinator, Member	present in Pisa
Betty Magnin – CERN, WP 3 Coordinator, Member	connected by skype from CERN

### Other Participants:

C. Gentsos (on skype) – AUTH

A. Iovene (in Pisa) – CAEN

S. Citraro, S. Donati, P. Luciano, C.L. Sotiropoulou (in Pisa) – UNIPi

The slides of the scheduled presentations are available at the site

<https://agenda.infn.it/conferenceDisplay.py?confId=9705>

### ***PRESENTATIONS about the work performed in the WPs, by the seconded, to be seconded and collaborating persons (see the slides at web site):***

**Friday, 29 May 2015**

#### **WP5**

Imaging	30'	Speaker: Calliope-Louisa Sotiropoulou (PI)	10:00 - 10:30
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#### **WP1-2**

Activity at AUTH	30'	Speaker: Naoki Kimura	10:30 - 11:00
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Slp1	30'	Speaker: Christos Gentsos	11:00 - 11:30
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Clustering for IBL - resolution and tests	30'	Speaker: Akis	11:30 - 12:00
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AM boards final changes and Altium training	20'	Speaker: Andreas	12:00 - 12:20
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The CAEN PS	20'	Speaker: Marco Piendibene (PI)	12:20 - 12:40
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AMchip05 and Boards tests	20'	Speaker: Mr. Nicolò Vladi Biesuz (PI)	12:40 - 13:00
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The WP5 activity has started efficiently. The architecture has been chosen and the relative deliverable has been produced, the firmware for the training is under development. A contact with a group of physicists working for medical imaging has been established.

WP1 for board developments and WP2 for integration are advancing without major problems, except the delay on the arrival of AM06 chip (AM06 is not part of our IAPP project but our board final tests and validation time depend on its delivery time). All the developments and so all the presentations are based on the use of the precedent chip version AM05.

Caen PS design is in advanced status. The PS will be delivered to CERN at the end of July to be used for cooling tests in USA15.