



# VIII SuperB General Meeting - Orsay

## Monday, 16 February 2009

### Parallel III - SVT (11:00 - 12:30)

time	[id] title	presenter
11:00	[135] Plans on CMOS MAPS and Pixel with Vertical Integration	RE, Valerio
11:20	[59] Plans for pixel module integration electronics	CITTERIO, Mauro
11:35	[60] DAQ for SVT	VILLA, Mauro
11:50	[61] Low Mass Support and Cooling for Layer0 pixel modules	BOSI, Filippo