

# AM06 chip & board time schedule

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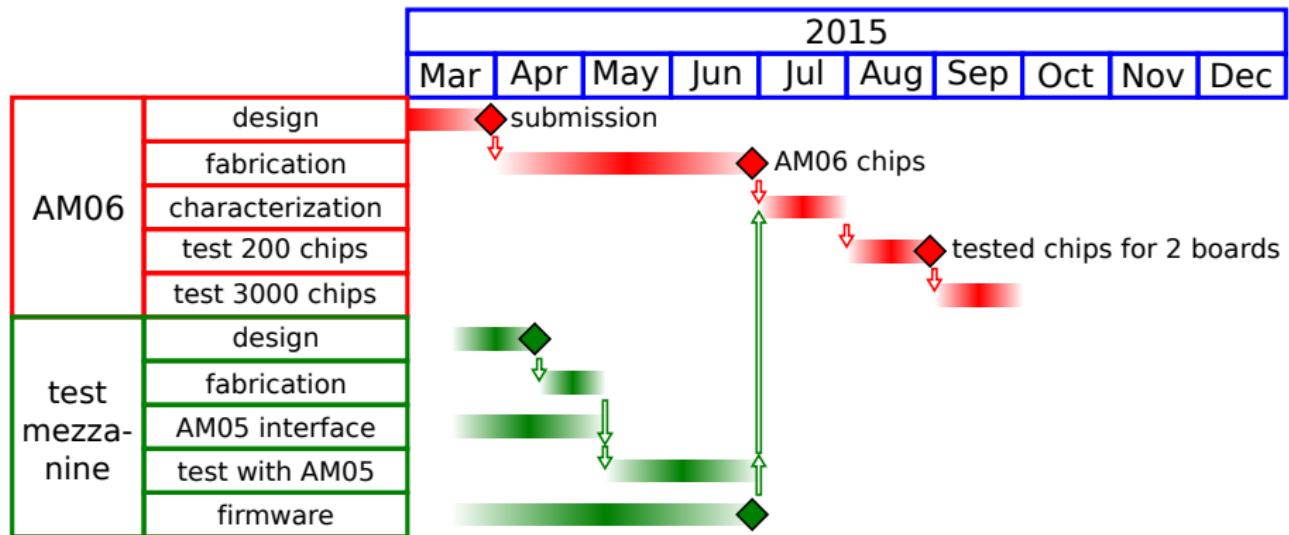
Istituto Nazionale  
di Fisica Nucleare



UNIVERSITÀ  
DEGLI STUDI  
DI MILANO

FTK Workshop, Paris, Mar. 2015

# Time schedule



Target: 2 AM06 boards in October 2015

# Responsibilities

| Task   | Responsible partner(s)                   |
|--|--|
| AM06 chip design<br>chip fabrication<br>chip characterization<br>test 200 chips<br>test 3000 chips                   | Milano + Paris<br>external (IMEC + TSMC) |
|  | Milano + Frascati                        |
|  | Milano + Frascati                        |
|  | external                                 |
|  |  |
| AM06 test board design<br>board fabrication<br>AM05 interface to test board<br>board test with AM05<br>test firmware | Frascati                                 |
|  | external                                 |
|  | ?  |
|  | Frascati                                 |
|  | Milano (+ Paris)                         |