



TEST ACTIVITIES AND LASER SETUP

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HV/HR-CMOS E IBRIDIZZAZIONE MEETING

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STATUS AND UPGRADES

⇒ LASER SETUP IN NEW BOX IS READY AND WORKS PROPERLY!!

- ✓ XYZ Axis with micrometric movements have been implemented

⇒ NEXT LASER SYSTEM UPGRADES:

- ✓ Realization of a Board for DAQ (Usbpix-laser trigger interface) – (In Progress - See next slide)
- ✓ Axis will be implemented in the software to have an automatic scan (e.g. to make a map of the pixel efficiency)

⇒ MEASUREMENTS IN PROGRAM WITH LASER:

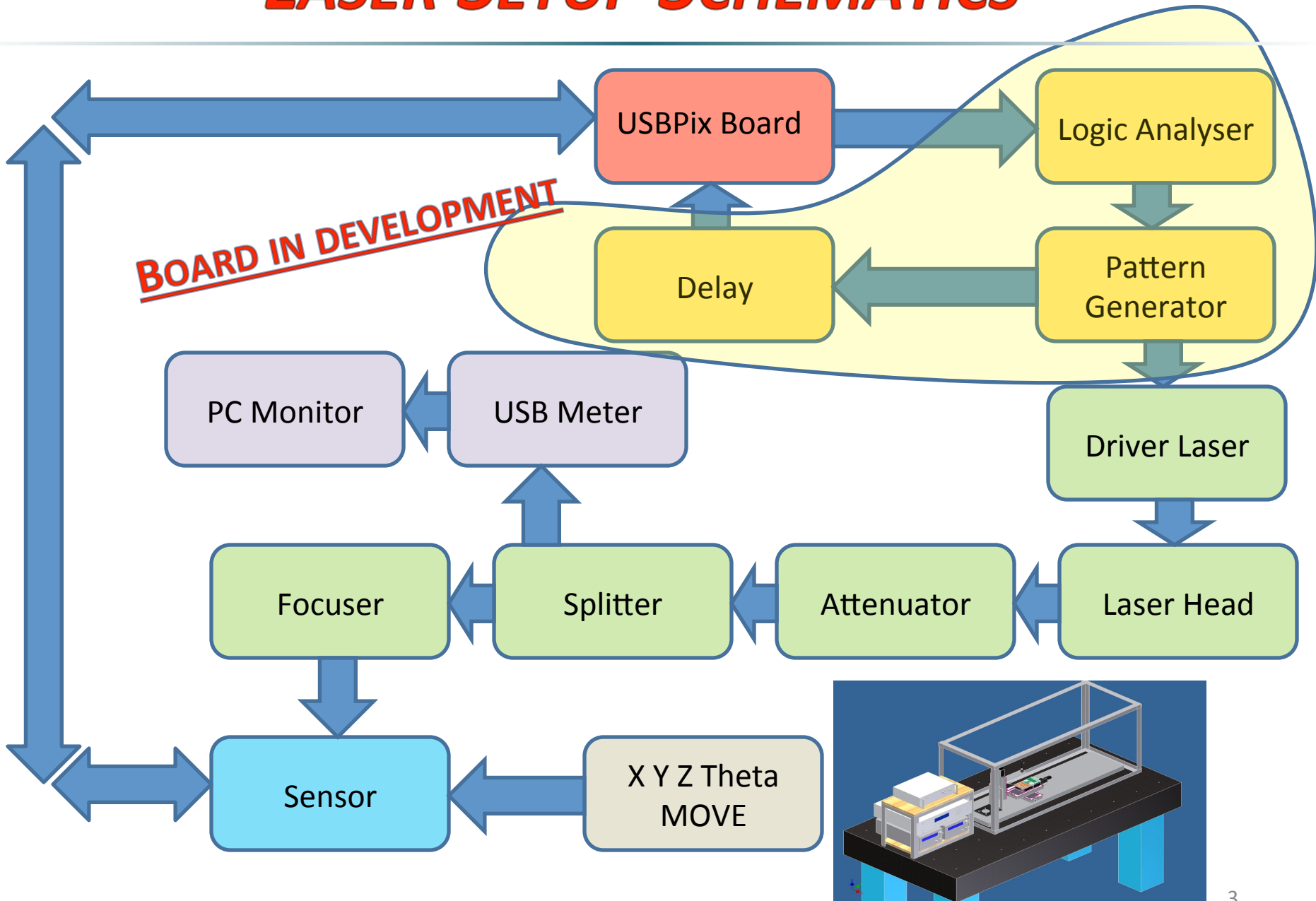
- ✓ Map of Efficiency of a 3D and New Planar pixel
- ✓ Test new chip from STM (when it will be available)

⇒ HV-CMOS SETUP:

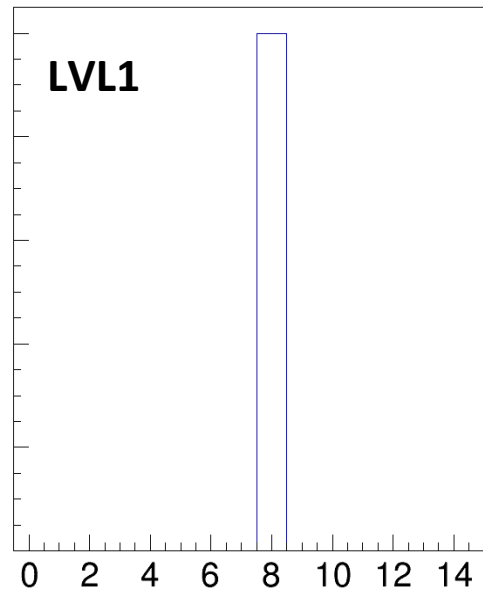
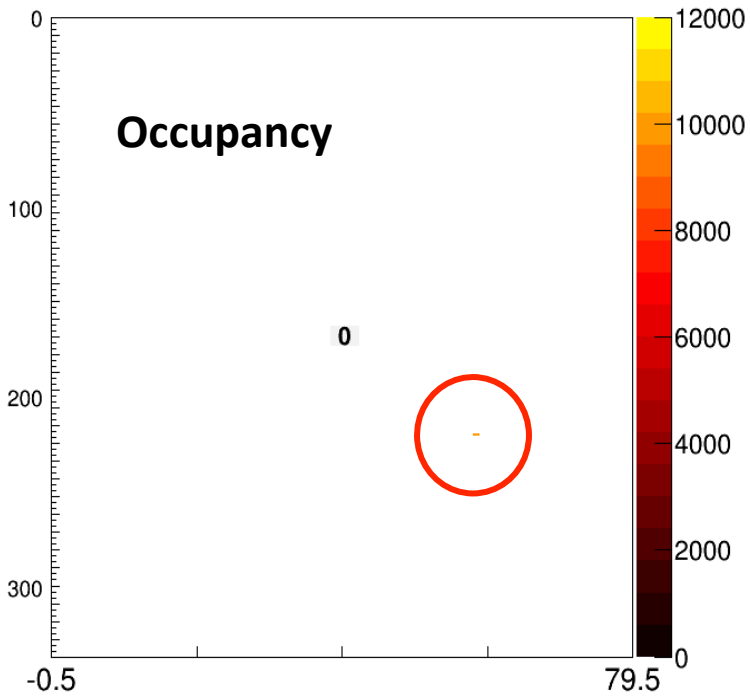
- ✓ Problems with the software (**CCPD tuning not implemented yet**)



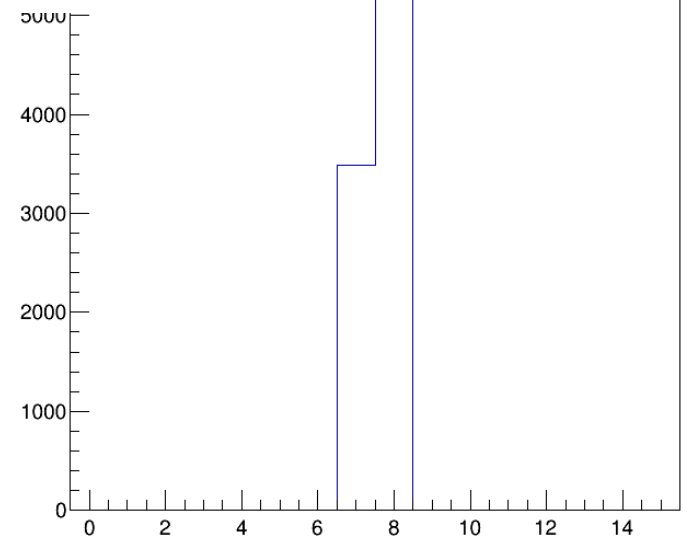
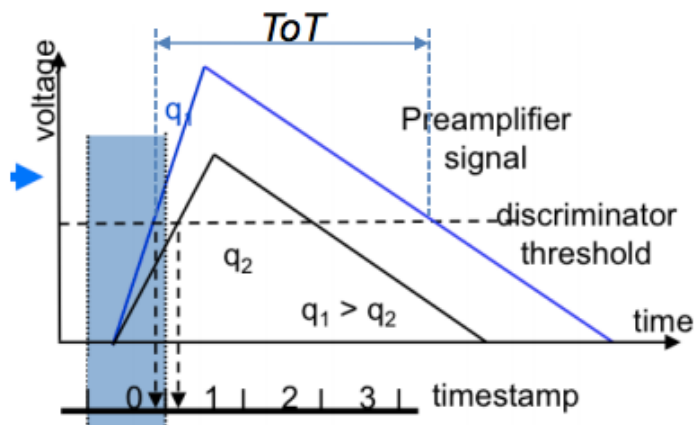
LASER SETUP SCHEMATICS



RUN



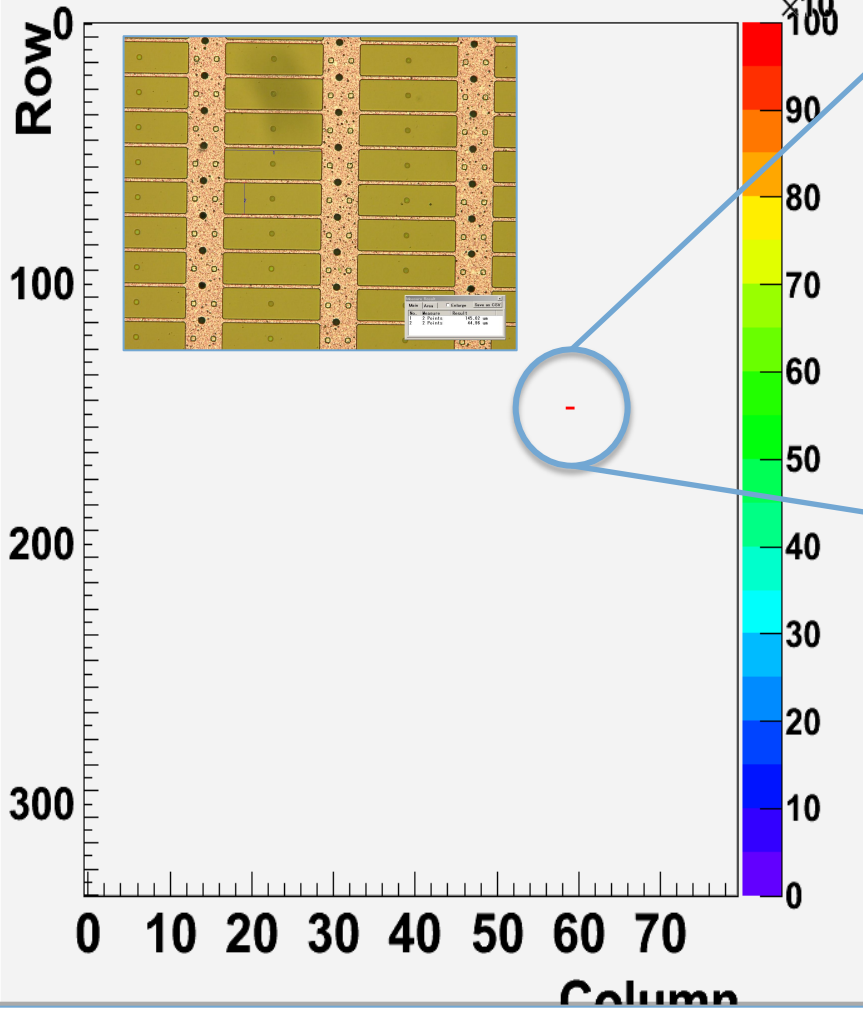
Tuned @ 6 ToT = 20 000 e⁻



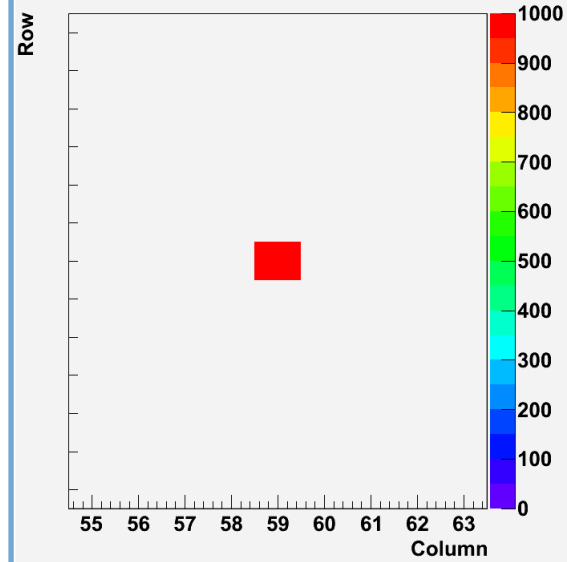
SPARE

FIRST RUN (OLD SETUP)

Occupancy mod 0 bin 0 chip 0



Occupancy mod 0 bin 0 chip 0



ToT mod 0 bin 0

