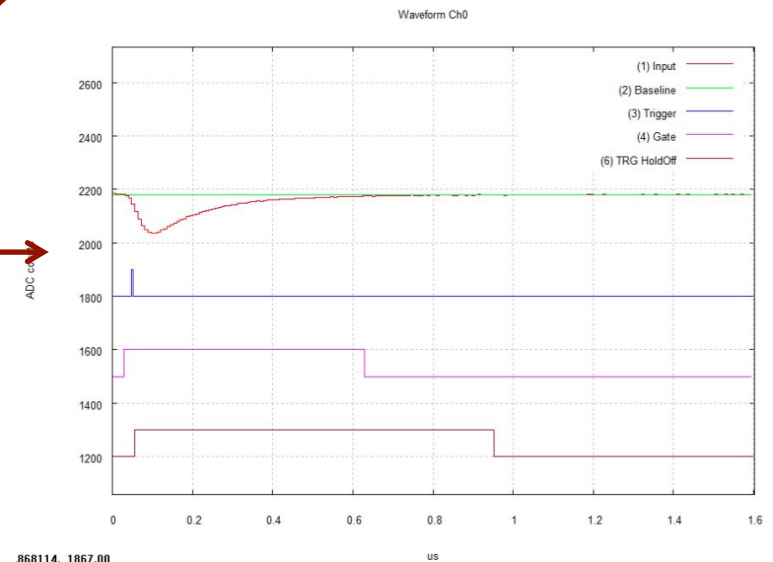
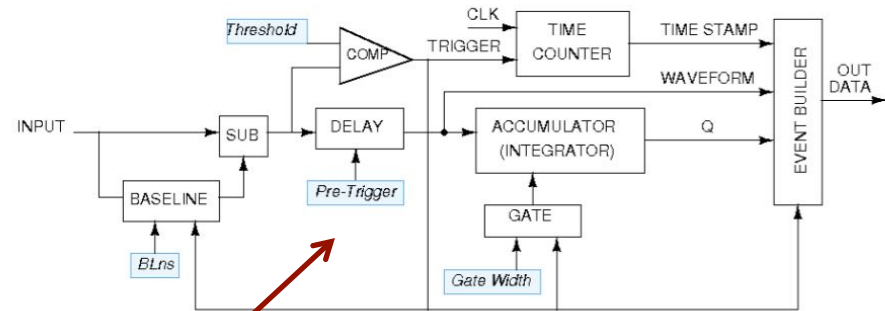


DAQ test

- Target: Test Caen V1720 readout capabilities
- V1720
 - Flash ADC 250MS/s
 - Two firmware possibility
 - “Oscilloscope mode”
 - N sampling for each channel (N can change)
 - “Digital Pulse Processing mode”
 - Only Amplitude and Time are saved
 - Need “ad hoc” configuration
 - To enhance time resolution a “Mixed mode” is possible
 - » Few sampling saved in order to refit online to extract time info



DAQ Test

- Which rate can be tolerated?
 - 2 V1720 (differential input) available
 - Setup 1 V1720 with “oscilloscope mode” and 1 V1720 with “DPP mode”
 - Test with Csl(pure)+PMT and cosmics
 - Test with random trigger at different rate to evaluate the capabilities of each firmware