

# IAPP Risk Assessment and Mitigation

Francesco Crescioli

## Aim of this time slot

- ▶ I am not here to present you anything original or my work, everything I'm going to say it is coming from Paola great work on the Periodic Report
- ▶ We should all be aware of what is written in the Periodic Report about Risk Management
- ▶ We should discuss it and give feedback, this is the time to do so

RISKS ASSOCIATED WITH THE WORKING PLAN (recruitment, delays due to technological reasons, etc) and DEVELOPMENT OF A MITIGATION PLAN. We don't see for the moment motivations for delays due to recruitments and secondments since all of them are going ahead following our schedule or even anticipating the timing expected in the previously approved Gantt Charts. We see instead potential risks of delays in the production phase of boards for the FTK processor, due to the fact that the AM06 is not submitted yet and could delay more. We cannot fully test our boards before production, if the AM06 chip is not available. So delay on our FTK production and commissioning schedule is possible, due to facts external to our project. The ATLAS experiment is conscious and has a backup solution, since the baseline trigger menu planned when FTK was not accepted is still in use, and will be active until FTK will be ready. The delay of AM06 could have also an impact on the deliverable D.5.4, results on movie processing, since we need a substantially large AM memory for that study, while D.5.3 can be executed even with AM05. If movie processing will have problems we will do more research in the area of still image processing, analysing more medical images from the large set of public databases we know to be available.

- ▶ No delays in recruitments and secondments
  - ▶ We are generally on schedule or even ahead
  - ▶ No risks here
- ▶ Delays foreseen in boards production
  - ▶ We blame AMchip06, those guys designing it delayed a lot ...
  - ▶ The ATLAS experiment has a backup plan: it will use the old trigger menu - without FTK - until we are ready
  - ▶ D.5.3 - image processing - can proceed with the smaller but functionally equivalent AMchip05
  - ▶ D.5.4 - movie processing - needs a large AM, we have to wait for the AMchip06
  - ▶ As the AMchip06 is an external product the only mitigation we can and will implement is a rebalance of our activities to prioritize what can be done with AMchip05 and wait 06 for the rest