

Workshop della Commissione Calcolo e Reti dell'INFN - 2015 (LNF)

Wednesday, 27 May 2015

Nuove tecnologie hardware - Aula Touschek (14:45 - 18:45)

-Conveners: Michele Michelotto; Daniele Cesini; Piero Vicini

time	[id] title	presenter
14:45	[25] Introduction	MICHELOTTO, Michele VICINI, Piero
14:55	[26] The INFN COSA project	CESINI, Daniele
15:25	[27] Energy vs time performances of HPC applications on Tegra K1	CALORE, Enrico
15:55	[28] Triggering Events with GPUs at ATLAS	RINALDI, Lorenzo
16:15	[32] Event building for the LHCb Upgrade	GALLI, Domenico
16:45	Pausa caffè	
17:15	[29] NaNet: a Family of FPGA-based Network Interface Cards for Real-Time Trigger and Data Acquisition Systems in HEP Experiments.	LONARDO, Alessandro
17:45	[30] INFN H2020 projects for new hardware technologies	VICINI, Piero
18:15	[31] Some experiences with Power Efficient Architectures in HEP	Mr PANTALEO, Felice