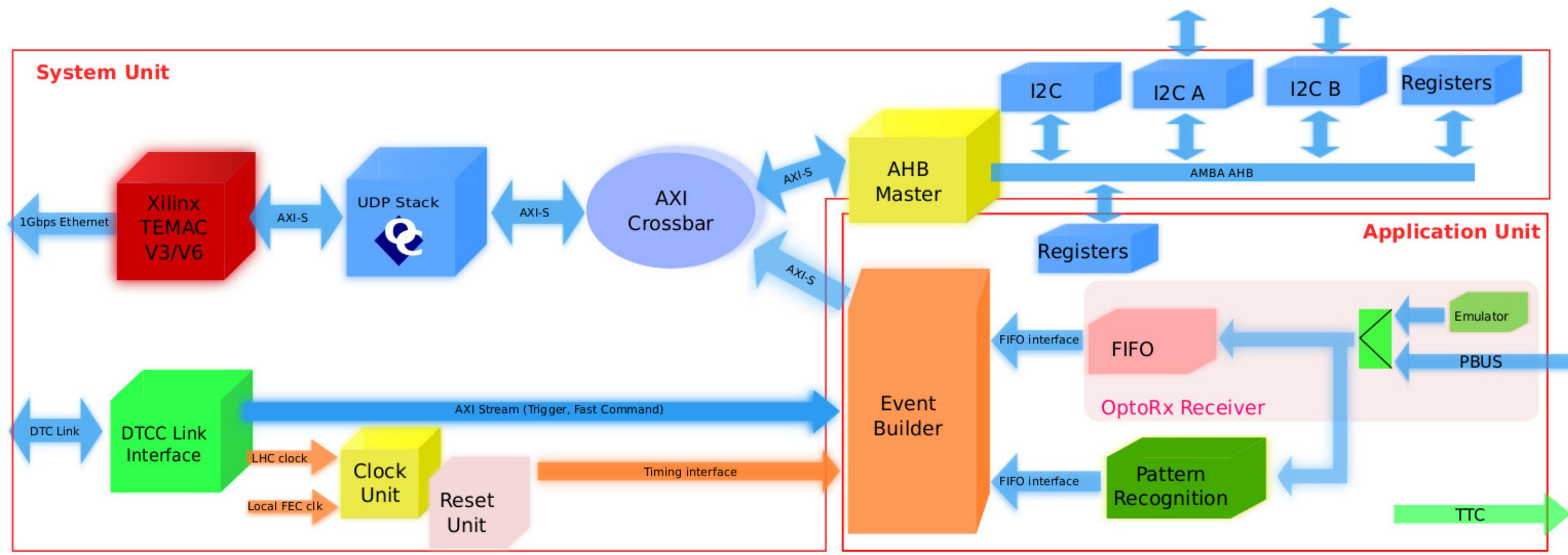


System Unit



System Unit

1Gbps Ethernet

Xilinx
TEMAC
V3/V6

AXI-S

UDP Stack

AXI-S

AXI Crossbar

AXI-S

AHB Master

I2C

I2C A

I2C B

Registers

AMBA AHB

DTC Link

DTCC Link
Interface

LHC clock

Clock
Unit

Local FEC clk

Reset
Unit

Timing interface

Event
Builder

AXI-S

Registers

Application Unit

FIFO interface

FIFO

OptoRx Receiver

FIFO interface

Pattern
Recognition

Emulator

PBUS

TTC