



HV/HR-CMOS e Ibridizzazione MEETING

Update su sistema di test HV-CMOS (Laser System)

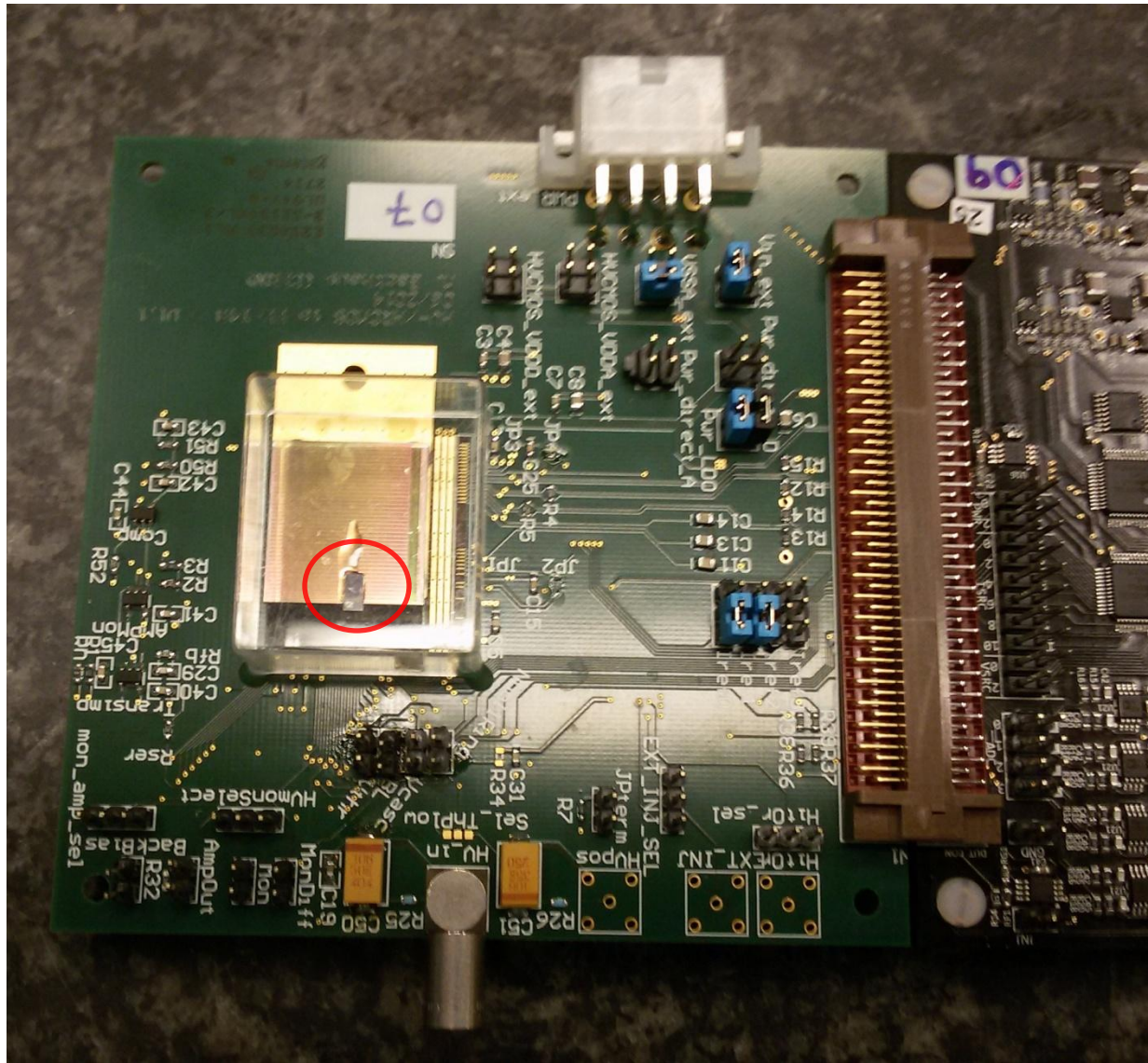
6 ottobre 2014 ore 14.30 L204

G. Gariano, A. Gaudiello

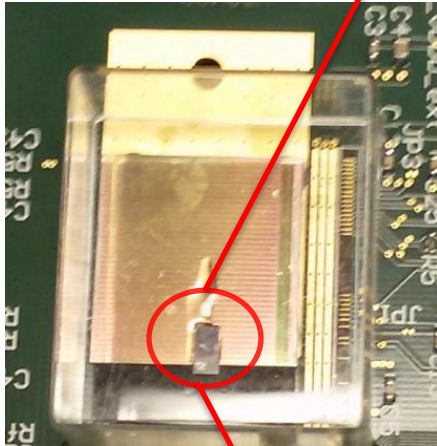
OUTLINE

- ✓ **HV CMOS sensor**
- ✓ **Boards for DAQ**
- ✓ **New setup BOX - Updates**

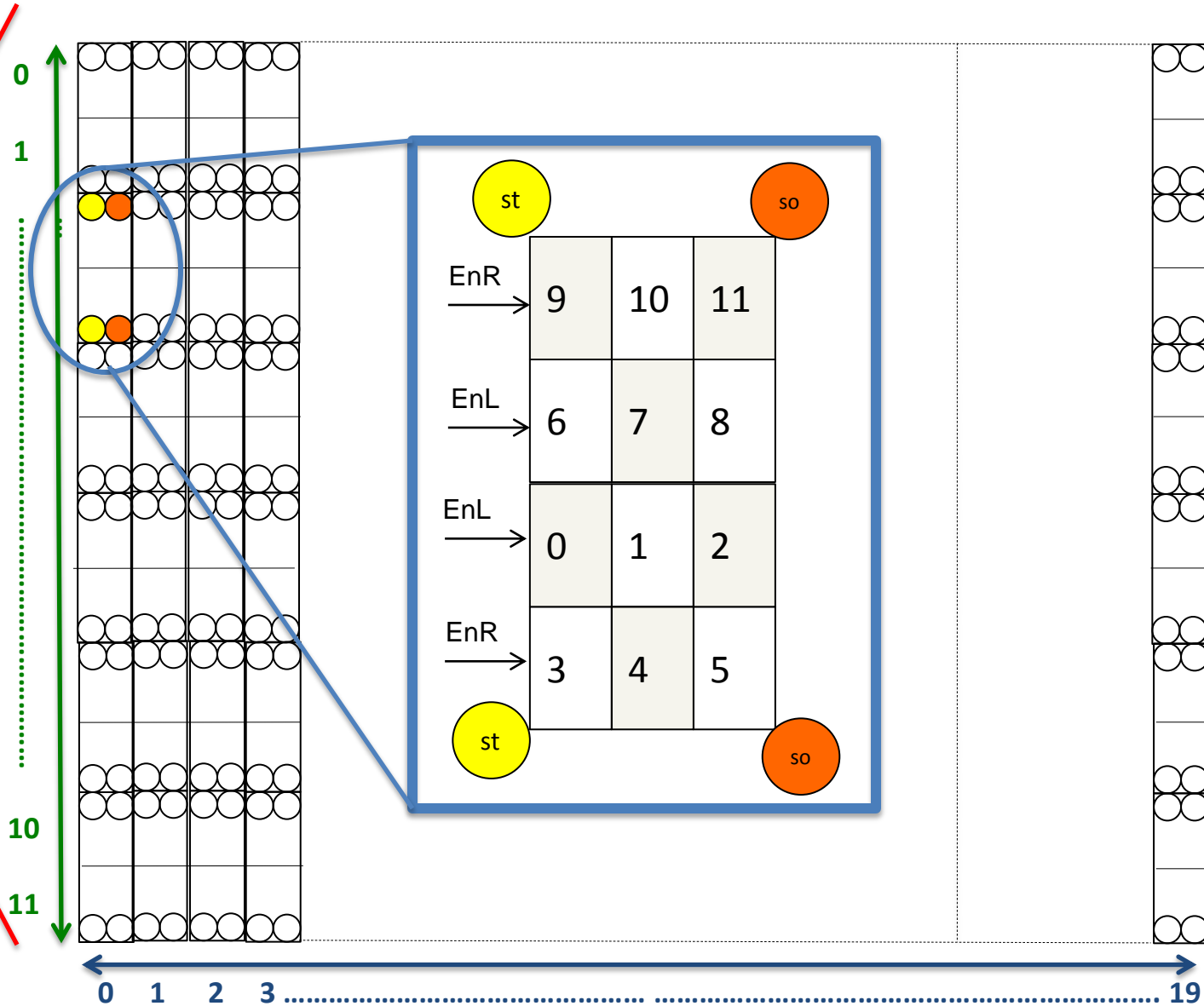
Sensor HV CMOS



Sensor HV CMOS



2.2 x 4.4 mm²
60 columns x 24 rows

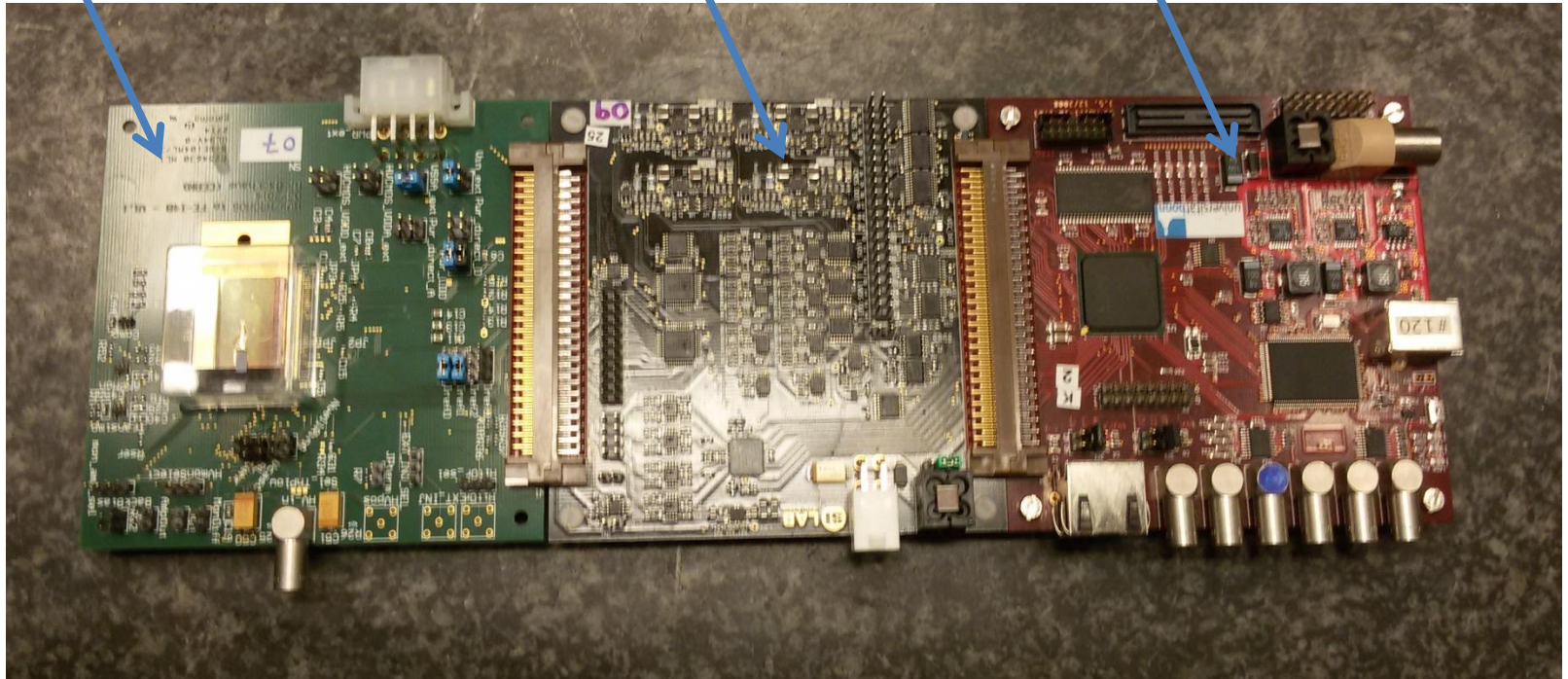


Boards for DAQ

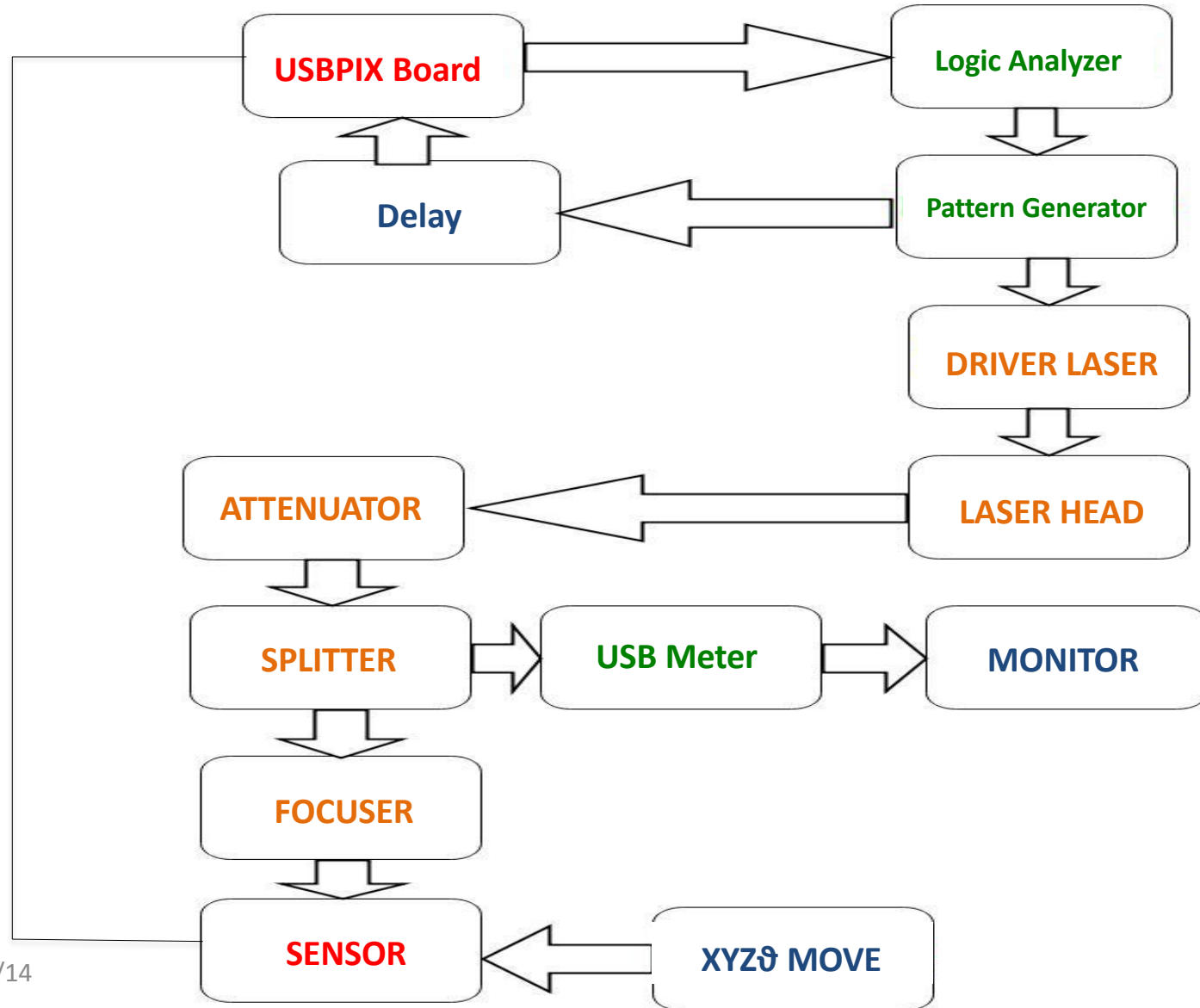
Sensor Board

GPAC

USBpix

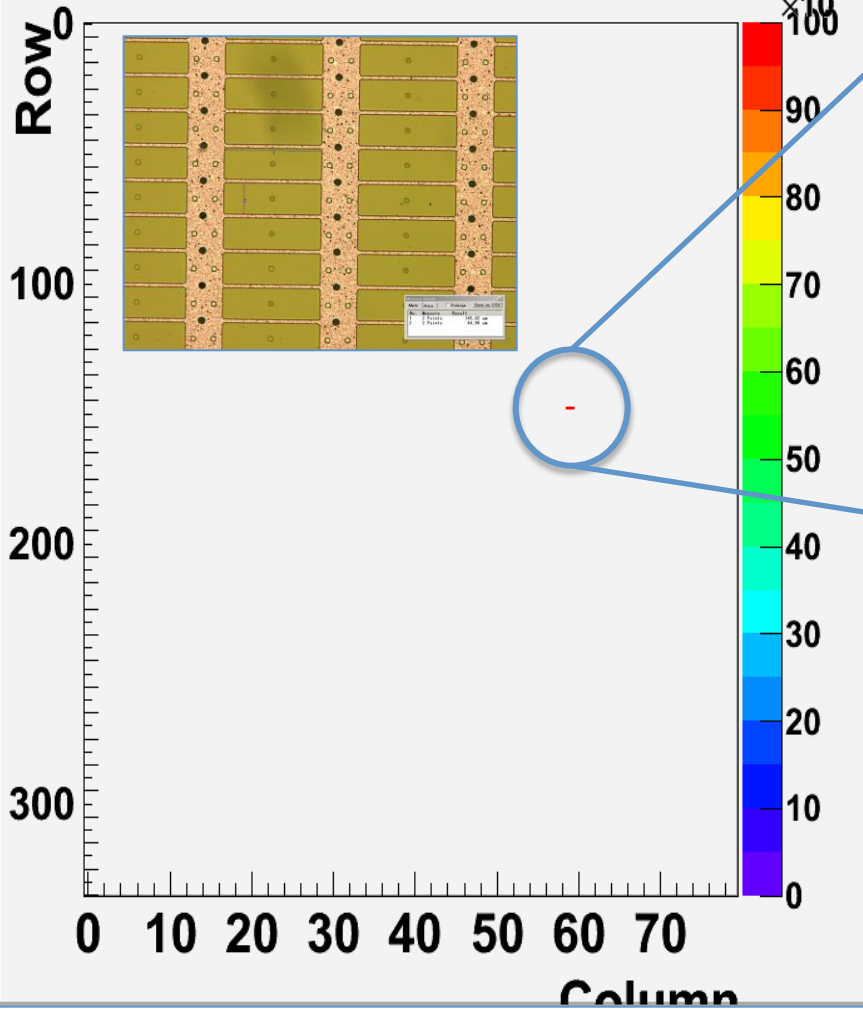


Setup Schematics

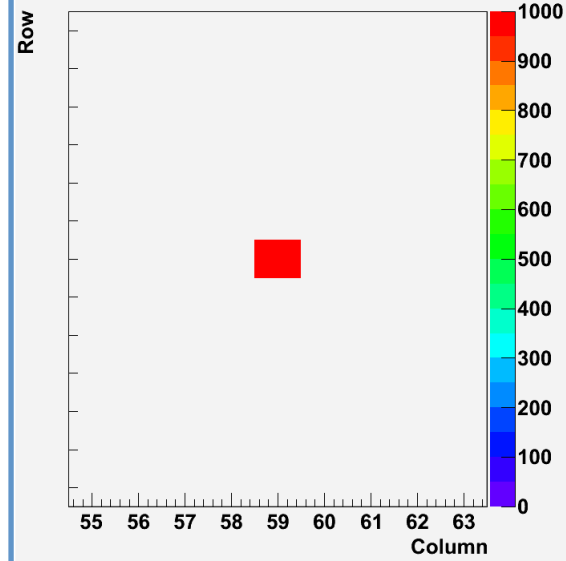


First Run

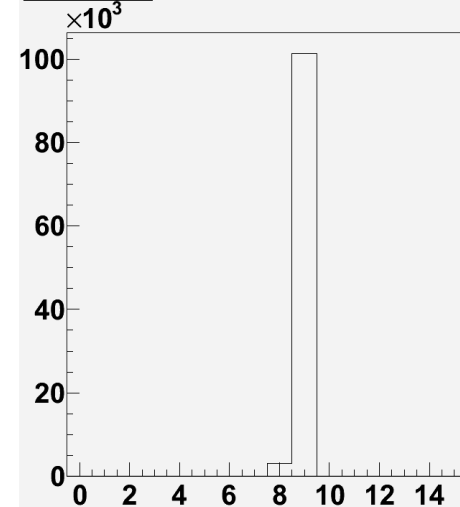
Occupancy mod 0 bin 0 chip 0



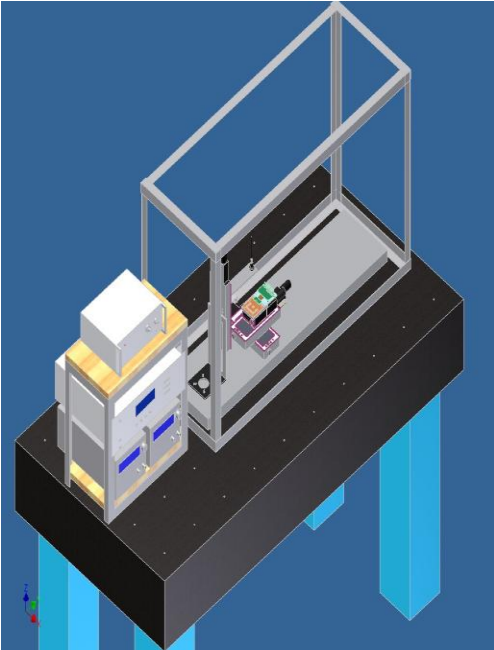
Occupancy mod 0 bin 0 chip 0



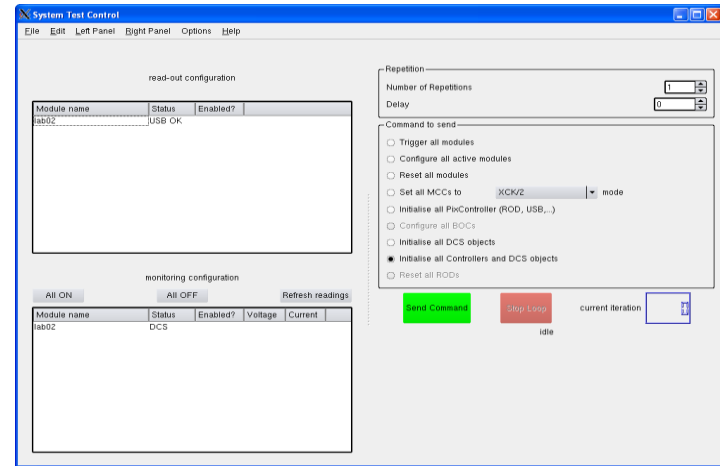
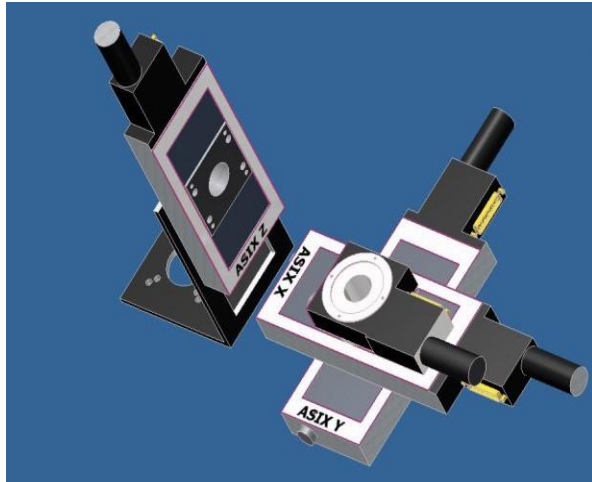
ToT mod 0 bin 0



New Setup Project 1



New Setup Project 2



X, Y, Z, T completely automatized with resolution 1um
They will be integrated into the new ST program for an automatic Laser (source) scan

