SuperB Computing Workshop

Wednesday, 17 December 2008

SuperB DAQ/Electronics (II) - Aula seminari (11:00 - 13:00)

time	[id] title	presenter
11:00	[54] The CMS DAQ system: architecture and first experience	SCHWICK, Christoph
11:45	[57] The LHCb DAQ system	GALLI, Domenico
12:30	[65] Multi-Gigabit/s fixed-latency serial links with FPGAs for SuperB	GIORDANO, Raffaele