FRONTIER DETECTORS FOR FRONTIER PHYSICS
13th Pisa Meeting on Advanced Detectors

Thursday, 28 May 2015

Front end, Trigger, DAQ and Data Management - Poster Session (17:20 - 18:53)

time	[id] title	presenter
17:20	[409] Development of the Quality Control System of the Readout Electronics for the Large Size Telescope of the Cherenkov Telescope Array observatory	PAOLETTI, Riccardo
17:21	[35] The LHCb trigger system and its upgrade	Mrs DZIURDA, Agnieszka
17:22	[411] ATLAS LUCID electronics	LASAGNI MANGHI, Federico
17:23	[383] An FPGA-based trigger for the MEG II experiment	NICOLÒ, Donato
17:24	[161] VME Rear Transition Module with Backplane Data Access Capability for the ATLAS FTK Upgrade	Mr BOGDAN, Mircea
17:25	[177] The timing upgrade project of the TOTEM RP detectors	Dr BERRETTI, Mirko
17:26	[291] The supply voltage apparatus of the CUORE experiment	PESSINA, Gianluigi Ezio
17:27	[154] The first level trigger of JEM-EUSO: concept and tests	Dr BERTAINA, Mario Edoardo
17:28	[167] The WaveCatcher Waveform Digitizers: high-end instrumentation for characterization of advanced fast detectors	Mr BRETON, Dominique
17:29	[18] The Upgrade for the Data Acquisition System of the KOTO Detector	Dr TECCHIO, Monica
17:30	[201] The Trigger and Data Acquisition System for the 8 tower subsystem of the KM3NeT detector	Mr MANZALI, Matteo
17:31	[224] The RD53 effort towards the development of a 65 nm CMOS pixel readout chip for extreme data rates and radiation levels	RE, Valerio
17:32	[241] The Level 0 trigger processor for the NA62 experiment	NERI, Ilaria
17:33	[75] The GANDALF Framework: Readout and Trigger System for the CAMERA detector at COMPASS II	Mr HERRMANN, Florian
17:34	[215] The Central Logic Board for the KM3NeT detector: design and production	MUSICO, Paolo
17:35	[205] The computing and data infrastructure to interconnect EEE stations	NOFERINI, Francesco
17:37	[221] Study of the spatial resolution for binary readout detectors	Dr YONAMINE, Ryo
17:38	[350] Smart Fast-Digitizer system for astro-particle physics detectors	Dr DAVINI, Stefano
17:39	[204] Self-Triggering Readout System for the Neutron Lifetime Experiment PENeLOPE	Mr GAISBAUER, Dominic
17:40	[191] Research and Development for a Free-Running Readout System for the ATLAS LAr Calorimeters at the High Luminosity LHC	Mr HILS, Maximilian
17:41	[362] Real time tracking with a silicon telescope prototype using the “artificial retina” algorithm	Mr PETRUZZO, Marco
17:42	[288] Radiation testing campaign results for understanding the suitability of FPGAs in detector electronics	CAMPLANI, Alessandra
17:43	[180] Performance of pile-up mitigation techniques for jets in pp collisions with the ATLAS detector	Mrs TESTA, Marianna

17:44	[53] PANDA Straw Tube Detectors and Readout	Mr STRZEMPEK, Pawel
17:45	[299] Optical Wireless Communication system for particle detectors in High Energy Physics.	Mr ALI, Wajahat
17:46	[128] New electronics of the spectrometric channel for the SND detector electromagnetic calorimeter	Mr SURIN, Ilya
17:47	[259] New electronics for the surface detectors of the Pierre Auger Observatory	Dr KLEIFGES, Matthias
17:48	[323] Method of Signal Detection from Silicon Photomultipliers Using Fully Differential Charge to Time Converter and Fast Shaper	Mr BASZCZYK, Mateusz
17:49	[152] L1Track: a fast Level 1 track trigger for the ATLAS High Luminosity Upgrade	Dr CERRI, alessandro
17:50	[223] In-pixel conversion with a 10 bit SAR ADC for next generation X-ray FELs	LODOLA, Luca
17:51	[56] Front-end chip for Silicon Photo-Multipliers detector with pico-second Time-of-Flight information	Mrs STANKOVA, Vera
17:52	[235] Electronics Design and Layout Complexity of the ATLAS New Small Wheels	Dr YACOOB, Sahal
17:53	[371] EUSO-BALLOON: readout electronics performances	Mrs BLIN, Sylvie
17:54	[39] Digital signal processing for thermal neutron detectors using ZnS(Ag):6LiF scintillating layers read out with WLS fibers and SiPMs	Dr MOSSET, Jean-Baptiste
17:55	[365] Development of the FoCal-E PAD detector and its electronics for the LHC-ALICE experiment	Dr INABA, MOTOI
17:56	[216] Design and test of clock distribution circuits for the Macro Pixel ASIC	GAIONI, Luigi
17:57	[268] Design and performance of the upgrade of the CMS L1 muon trigger	Dr BORTIGNON, Pierluigi
17:58	[211] Characterization of Bandgap Reference Circuits designed for High Energy Physics Applications	DE CANIO, Francesco
17:59	[104] CITIROC 32channel ASIC for SiPM readout	Dr DE LA TAILLE, Christophe
18:00	[357] An artificial retina processor for track reconstruction at the full LHC crossing rate	CENCI, Riccardo
18:01	[285] ALDO: a radiation-tolerant, low-noise, adjustable low dropout linear regulator in 0.35 micron CMOS technology	CARNITI, Paolo
18:02	[144] A simulation tool for a Silicon Photomultiplier coupled to a scintillating fiber	Dr RIPICCINI, EMANUELE
18:03	[101] A proposed DT-seeded muon track trigger for the CMS experiment at the HL-LHC	POZZOBON, Nicola
18:04	[236] A new highly selective first level ATLAS muon trigger with MDT chamber data for HL-LHC	Dr KROHA, Hubert
18:05	[178] A high performance Front End Electronics for Drift Chamber readout in MEG experiment upgrade	PEPINO, Aurora
18:06	[129] A charge amplifier for VUV photomultiplier operating in cryogenic environment.	Dr DI GIOVANNI, ADRIANO
18:07	[265] A New Front-end ASIC for GEM detectors with Time and Charge Measurement Capabilities	Mr CICIRIELLO, Fabio
18:08	[162] A Fast hardware Tracker for the ATLAS Trigger system	Mr PANDINI, Carlo
18:09	[141] A 12-bit SAR ADC integrated on a multichannel Silicon Drift Detector Readout IC	SCHEMBARI, Filippo

18:10	[404] A New ATLAS Muon CSC Readout System with System on Chip Technology on ATCA Platform	CLAUS, Richard
18:11	[289] A Pattern Recognition Mezzanine based on Associative Memory and FPGA technology for Level 1 Track Triggers for the HL-LHC upgrade.	FEDI, Giacomo
18:32	[421] A Track Finding Algorithm for a Time Multiplexed L1 Track Trigger for the phase II CMS experiment	Mr CIERI, Davide