



# FRONTIER DETECTORS FOR FRONTIER PHYSICS

## 13th Pisa Meeting on Advanced Detectors

### Thursday, May 28, 2015

#### Front end, Trigger, DAQ and Data Management - Poster Session (5:20 PM - 6:53 PM)

time	[id] title	presenter
5:20 PM	[409] Development of the Quality Control System of the Readout Electronics for the Large Size Telescope of the Cherenkov Telescope Array observatory	PAOLETTI, Riccardo
5:21 PM	[35] The LHCb trigger system and its upgrade	Mrs DZIURDA, Agnieszka
5:22 PM	[411] ATLAS LUCID electronics	LASAGNI MANGHI, Federico
5:23 PM	[383] An FPGA-based trigger for the MEG II experiment	NICOLÒ, Donato
5:24 PM	[161] VME Rear Transition Module with Backplane Data Access Capability for the ATLAS FTK Upgrade	Mr BOGDAN, Mircea
5:25 PM	[177] The timing upgrade project of the TOTEM RP detectors	Dr BERRETTI, Mirko
5:26 PM	[291] The supply voltage apparatus of the CUORE experiment	PESSINA, Gianluigi Ezio
5:27 PM	[154] The first level trigger of JEM-EUSO: concept and tests	Dr BERTAINA, Mario Edoardo
5:28 PM	[167] The WaveCatcher Waveform Digitizers: high-end instrumentation for characterization of advanced fast detectors	Mr BRETON, Dominique
5:29 PM	[18] The Upgrade for the Data Acquisition System of the KOTO Detector	Dr TECCHIO, Monica
5:30 PM	[201] The Trigger and Data Acquisition System for the 8 tower subsystem of the KM3NeT detector	Mr MANZALI, Matteo
5:31 PM	[224] The RD53 effort towards the development of a 65 nm CMOS pixel readout chip for extreme data rates and radiation levels	RE, Valerio
5:32 PM	[241] The Level 0 trigger processor for the NA62 experiment	NERI, Ilaria
5:33 PM	[75] The GANDALF Framework: Readout and Trigger System for the CAMERA detector at COMPASS II	Mr HERRMANN, Florian
5:34 PM	[215] The Central Logic Board for the KM3NeT detector: design and production	MUSICO, Paolo
5:35 PM	[205] The computing and data infrastructure to interconnect EEE stations	NOFERINI, Francesco
5:37 PM	[221] Study of the spatial resolution for binary readout detectors	Dr YONAMINE, Ryo
5:38 PM	[350] Smart Fast-Digitizer system for astro-particle physics detectors	Dr DAVINI, Stefano
5:39 PM	[204] Self-Triggering Readout System for the Neutron Lifetime Experiment PENELOPE	Mr GAISBAUER, Dominic
5:40 PM	[191] Research and Development for a Free-Running Readout System for the ATLAS LAr Calorimeters at the High Luminosity LHC	Mr HILS, Maximilian
5:41 PM	[362] Real time tracking with a silicon telescope prototype using the "artificial retina" algorithm	Mr PETRUZZO, Marco
5:42 PM	[288] Radiation testing campaign results for understanding the suitability of FPGAs in detector electronics	CAMPLANI, Alessandra

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5:43 PM	[180] Performance of pile-up mitigation techniques for jets in pp collisions with the ATLAS detector	Mrs TESTA, Marianna
5:44 PM	[53] PANDA Straw Tube Detectors and Readout	Mr STRZEMPEK, Pawel
5:45 PM	[299] Optical Wireless Communication system for particle detectors in High Energy Physics.	Mr ALI, Wajahat
5:46 PM	[128] New electronics of the spectrometric channel for the SND detector electromagnetic calorimeter	Mr SURIN, Ilya
5:47 PM	[259] New electronics for the surface detectors of the Pierre Auger Observatory	Dr KLEIFGES, Matthias
5:48 PM	[323] Method of Signal Detection from Silicon Photomultipliers Using Fully Differential Charge to Time Converter and Fast Shaper	Mr BASZCZYK, Mateusz
5:49 PM	[152] L1Track: a fast Level 1 track trigger for the ATLAS High Luminosity Upgrade	Dr CERRI, alessandro
5:50 PM	[223] In-pixel conversion with a 10 bit SAR ADC for next generation X-ray FELs	LODOLA, Luca
5:51 PM	[56] Front-end chip for Silicon Photo-Multipliers detector with pico-second Time-of-Flight information	Mrs STANKOVA, Vera
5:52 PM	[235] Electronics Design and Layout Complexity of the ATLAS New Small Wheels	Dr YACOOB, Sahal
5:53 PM	[371] EUSO-BALLOON: readout electronics performances	Mrs BLIN, Sylvie
5:54 PM	[39] Digital signal processing for thermal neutron detectors using ZnS(Ag):6LiF scintillating layers read out with WLS fibers and SiPMs	Dr MOSSET, Jean-Baptiste
5:55 PM	[365] Development of the FoCal-E PAD detector and its electronics for the LHC-ALICE experiment	Dr INABA, MOTOI
5:56 PM	[216] Design and test of clock distribution circuits for the Macro Pixel ASIC	GAIONI, Luigi
5:57 PM	[268] Design and performance of the upgrade of the CMS L1 muon trigger	Dr BORTIGNON, Pierluigi
5:58 PM	[211] Characterization of Bandgap Reference Circuits designed for High Energy Physics Applications	DE CANIO, Francesco
5:59 PM	[104] CITIROC 32channel ASIC for SiPM readout	Dr DE LA TAILLE, Christophe
6:00 PM	[357] An artificial retina processor for track reconstruction at the full LHC crossing rate	CENCI, Riccardo
6:01 PM	[285] ALDO: a radiation-tolerant, low-noise, adjustable low dropout linear regulator in 0.35 micron CMOS technology	CARNITI, Paolo
6:02 PM	[144] A simulation tool for a Silicon Photomultiplier coupled to a scintillating fiber	Dr RIPICCINI, EMANUELE
6:03 PM	[101] A proposed DT-seeded muon track trigger for the CMS experiment at the HL-LHC	POZZOBON, Nicola
6:04 PM	[236] A new highly selective first level ATLAS muon trigger with MDT chamber data for HL-LHC	Dr KROHA, Hubert
6:05 PM	[178] A high performance Front End Electronics for Drift Chamber readout in MEG experiment upgrade	PEPINO, Aurora
6:06 PM	[129] A charge amplifier for VUV photomultiplier operating in cryogenic environment.	Dr DI GIOVANNI, ADRIANO
6:07 PM	[265] A New Front-end ASIC for GEM detectors with Time and Charge Measurement Capabilities	Mr CICIRIELLO, Fabio

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6:08 PM	[162] A Fast hardware Tracker for the ATLAS Trigger system	Mr PANDINI, Carlo
6:09 PM	[141] A 12-bit SAR ADC integrated on a multichannel Silicon Drift Detector Readout IC	SCHEMBARI, Filippo
6:10 PM	[404] A New ATLAS Muon CSC Readout System with System on Chip Technology on ATCA Platform	CLAUS, Richard
6:11 PM	[289] A Pattern Recognition Mezzanine based on Associative Memory and FPGA technology for Level 1 Track Triggers for the HL-LHC upgrade.	FEDI, Giacomo
6:32 PM	[421] A Track Finding Algorithm for a Time Multiplexed L1 Track Trigger for the phase II CMS experiment	Mr CIERI, Davide