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## Belle-II SVD ladder assembly procedures

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The Belle-II experiment at the SuperKEKB collider in Japan will operate at a luminosity approximately 50 times greater than its predecessor (Belle). At its heart lies a six-layer vertex detector comprising two layers of pixelated silicon detectors (PXD) and four layers of double-sided silicon microstrip detectors (SVD). One of the key measurements for Belle-II is time-dependent CP violation asymmetry, which hinges on a precise charged-track vertex determination. Towards this goal, a proper assembly of the SVD components with precise alignment ought to be performed and the geometrical tolerances should be checked to fall within the design limits. We present an overview of the assembly procedure that is being followed, which includes the precision gluing of the SVD module components, wire-bonding of the various electrical components, and precision 3D coordinate measurements of the jigs used in assembly as well as of the final SVD modules.

### Collaboration

Belle - II

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