

An “Artificial Retina” Processor for Track Reconstruction

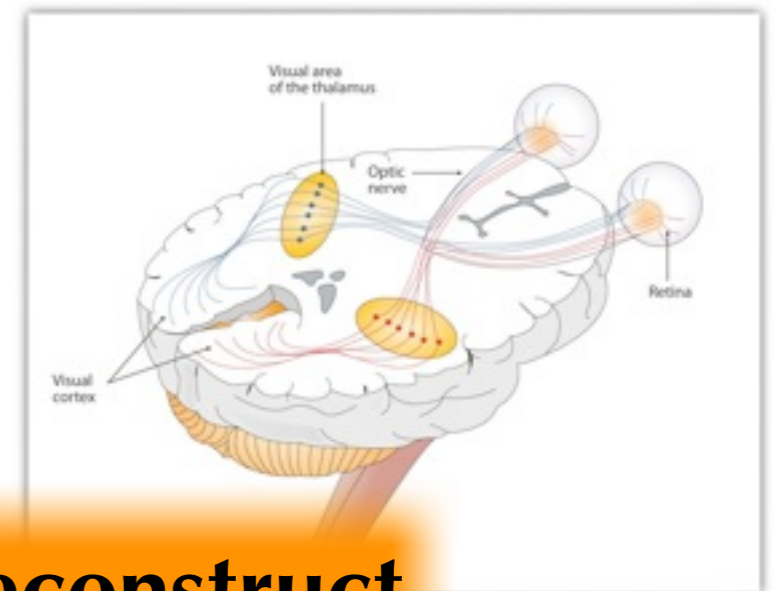
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Problem: trigger efficiently hadronic events at very high luminosity ($>10^{34} \text{ cm}^{-2}\text{s}^{-1}$)

The “Artificial Retina” algorithm

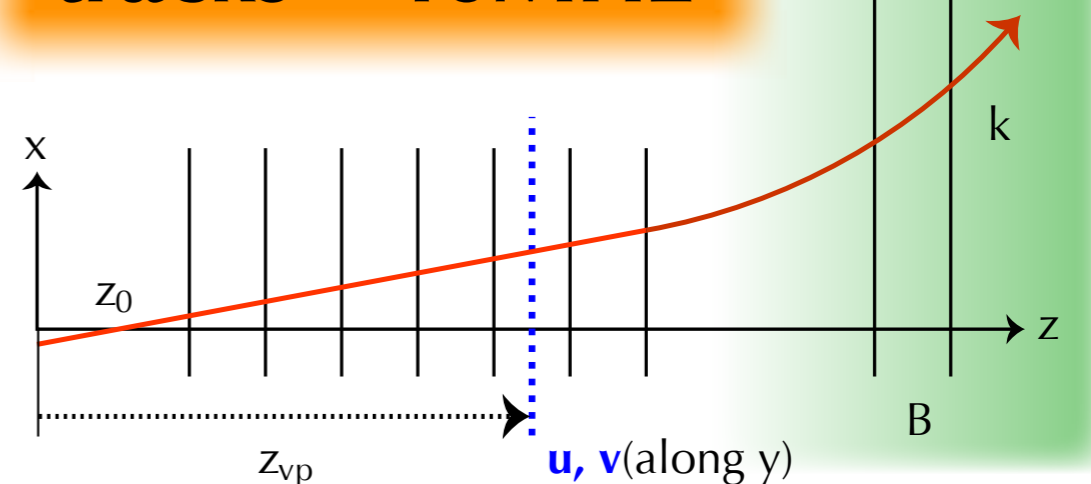
- **Highly-parallel** algorithm inspired to quick detection of edges in mammals visual cortex
- **Continuous response** with limited number of pre-calculated patterns, which allows a coarser grid mapping of parameters
- Reconstruction of charged-particle trajectories (tracks) at LHC collisions frequency with **few μs latency**, which allows to trigger hadronic events



Goal: reconstruct tracks @40MHz

Study for a real-detector application

- Forward spectrometer, **pixel silicon tracker** with fringe magnetic field
- To be implemented in few tenths of common FPGA's, less than 150 clock cycles per event
- **Reconstruction performances equal to offline algorithms**



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First prototype (goal: minimum event rate 1 MHz)

- Simplified tracker with 6 single-coordinate layers (x silicon strips)
- Implemented on **Altera Stratix III** FPGA using the **Tel62** board developed by INFN for NA62
- The system fits on **one crate** (8 boards, 32 FPGA's)
- **More than half of the system has been already designed, simulated and it is running on the real board with an event rate larger than 1 MHz**



Results shown in the poster have been achieved by “Retina”, a 3-year project funded by INFN, Division of technological research experiments (CNS5)

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