A high resolution Timing Counter for the MEG II experiment

M. De Gerone
on behalf of the MEG II Collaboration
Summary

• The MEG and MEG II experiments in a nutshell
• The new Timing Counter R&D
• Basic principles: pixelated structure and multiple hits timing
• Pixel studies and optimization
• Detector design
• Beam test results
• Looks for cLFV $\mu^+ \rightarrow e^+ + \gamma$ decay

• Tiny signal in huge background ($BR(\mu \rightarrow e\gamma) < 5.7 \times 10^{-13}$)

• Needs extremely high resolution on signal kinematic variables:
  
  • Energies & Direction (LXe, Drift Chambers)
  
  • Time (Timing Counter, LXe)

• $E_\gamma = E_{e^+} = 52.8$ MeV

• $\Delta t_{e\gamma} = 0$

• $\Delta \Omega_{e\gamma} = 180^\circ$
The MEG upgrade: MEG II

• Usage of existing infrastructure:
  • cryostat, magnet, beam line, CW accelerator for calibrations

• Full redesign of Drift Chamber system

• Modification in LXe inner face readout devices (PMT ⇒ SiPM)

• **New Timing Counter design**

• A new TDAQ system

• First engineering run this year!

A lot of contribution this week!
(IDs: 1, 10, 136, 142, 143, 144, 172, 174, 178, 208, 343, 383, 393, 403)
Arrays of 15 scintillating bars readout by PMTs

- Time reso: ~65/70ps, possible improvements:
  - PMTs not optimal (B field and He environment)
  - Large scintillators (40x40x800 mm³) imply low granularity, uncertainty in z impact point, large multiple scattering and spread of optical photons paths
The Pixelated Timing Counter: basic ideas

- Arrays of **15 scintillating bars** readout by **PMTs**
- Time reso: ~65/70ps, possible improvements:
  - **PMTs not optimal** (B field and He environment)
  - **Large scintillators** (40x40x800 mm³) imply **low granularity**, uncertainty in z impact point, large multiple scattering and spread of optical photons paths

**Goals:**
- reach the same time resolution with smaller modules
- use more than 1 hit for timing evaluation (reso scales as \(1/\sqrt{N_{\text{hit}}}\))

\[
\sigma^2 = \frac{\sigma_{\text{single}}^2}{N_{\text{hit}}} + \frac{\sigma_{\text{inter pixel}}^2}{N_{\text{hit}}} + \sigma_{\text{MS}}^2 (N_{\text{hit}})
\]
The Pixelated Timing Counter: basic ideas

- Arrays of **15 scintillating bars** readout by **PMTs**
- Time reso: \( \sim 65/70\)ps, possible improvements:
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  - **Large scintillators** (40x40x800 mm\(^3\)) imply **low granularity**, uncertainty in z impact point, large multiple scattering and spread of optical photons paths

- **Higher granularity**: hundreds (2 x 256) of **small scintillator plates** (120x50x5 mm\(^3\)) read-out by **SiPMs**.
- **Good single pixel resolution improved with multiple hits**
- **Thinner** (5mm) **scintillators** for less multiple scattering
- **Less pile-up** also with higher beam intensity

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A 3 years long R&D…

**Single pixel R&D**

1. **2012**
   - Probe single pixel

2. **2013**
   - 1st pixel prototype
   - Comparison tests

3. **2014**
   - 1st multi-pixel prototype
   - Test @BTF, SiPMs choice
   - Test @BTF backplanes readout

4. **2015**
   - 2nd multi-pixel prototype
   - Test @BTF backplanes readout
   - High rate test
   - Test @PSI
   - MEG II RUN

**Multipixel prototypes and test for final design**

- Final TC construction
- NOW!
Single pixel R&D

A lot of items to be tested and compared...here, (some of) the main ones:

- **SiPMs comparison** (noise, PDE, temperature dependence, resolution);
- **SiPMs connection**: series vs parallel;
- **scintillator and counter geometry** (pixel sizes, number of SiPMs);
- implementation in final detector.

<table>
<thead>
<tr>
<th>Manufacturer</th>
<th>Model number</th>
<th>Typea)</th>
<th>Details</th>
</tr>
</thead>
<tbody>
<tr>
<td>HPK</td>
<td>S10362-33-050C</td>
<td>Conventional (Old) MPPC</td>
<td>Ceramic package, Surface mount type (SMT) package</td>
</tr>
<tr>
<td></td>
<td>S10931-050P</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>S12572-050C(X)b)</td>
<td>New (standard-type) MPPC</td>
<td>Metal quench resistor, 25 μm pixel</td>
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<tr>
<td></td>
<td>S12572-025C(X)b)</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>S12652-050C(X)b)</td>
<td>Trench-type MPPC</td>
<td>Metal quench resistor, Improved fill factor</td>
</tr>
<tr>
<td></td>
<td>3X3MM50UMCLT-Bb)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>AdvanSiD</td>
<td>ASD-NUV3S-P-50b)</td>
<td>NUV type</td>
<td>SMT package</td>
</tr>
<tr>
<td>KETEK</td>
<td>PM3350 prototype-Ab)</td>
<td>Trench type</td>
<td>–</td>
</tr>
<tr>
<td>SensL</td>
<td>MicroFB-30050-SMT</td>
<td>B-series</td>
<td>With fast output, SMT package</td>
</tr>
</tbody>
</table>

*a) Sensor size of all the samples is 3 × 3 mm². Pixel pitch is 50 μm unless specified.
*b) Not version of commercial product. Under development.*

All tests led as a function of OV in thermal chamber with controlled temperature (std: 23°C)
Dark count rate: evaluated with random trigger in a fixed time window. **New Hamamatsu** and **trench type** models show best result.

Crosstalk probability: strongly suppressed by **trench type** model & **Advansid** devices.
Single pixel R&D: SiPM comparison

Standard setup for timing measurement:
- 60 x 30 x 5 mm³ scintillator equipped with 4 SiPMs arrays (series).
- Reference counter (RC) for trigger and $T_{\text{ref}}$.
- $^{90}\text{Sr}$ source (E<2.28 MeV).
- Read out: amplifier (G~20, BW~600MHz) + DRS evaluation board v4, dynamic range (-0.1, 0.9)V.

Time resolution is evaluated as the width of: $\Delta t = t_{\text{ref}} - \frac{t_1 + t_2}{2}$

RC reso (28ps) is calculated in dedicated runs and subtracted from overall resolution.

We checked also the stability of $\Delta t$ as a function of overvoltage and temperature.
Single pixel R&D: SiPM comparison

Best time reso with **Hamamatsu** device…but almost all devices work better than previous TC bar.

Advansid and **trench** devices are **more stable** as a function of over voltage and temperature.

<table>
<thead>
<tr>
<th>HPK old</th>
<th>HPK new</th>
<th>HPK new 25um</th>
<th>HPK trench</th>
<th>Ketek</th>
<th>Advansid</th>
<th>SensL</th>
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</thead>
<tbody>
<tr>
<td>2.5</td>
<td>5.5</td>
<td>2.8</td>
<td>0.1</td>
<td>0.1</td>
<td>0.2</td>
<td>0.8</td>
</tr>
</tbody>
</table>

Temperature stability is a crucial parameter for our detector: $\Delta T \rightarrow \Delta V_{BD} \rightarrow \Delta V_{OV}$

Temperature coefficient: ps/°C
Series vs parallel

Series connection:
- OV adjustment between SiPMs with different $V_{BD}$
- reduced capacitance $\rightarrow$ narrower pulse
- better time resolution
- higher bias voltage

Parallel connection:
- lower bias voltage
- OV not adjusted
- increased capacitance $\rightarrow$ wider pulse
- poor time resolution
Series vs parallel

Series connection:
- OV adjustment between SiPMs with different $V_{BD}$
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Parallel connection:
- lower bias voltage
- OV not adjusted
- increased capacitance $\rightarrow$ wider pulse
- poor time resolution

Final array design:
6 SiPM ASD-NUV3S-50P (series connection)
best compromise between intrinsic SiPMs properties
- time resolution
- thermal stability
Single pixel R&D: scintillator

Measurements with 4 SiPMs array in series connection (HAMAMATSU S10362-33-050C).

All the configurations showed a resolution better than 70ps (same as old TC reso). The final choice is done taking into account many factors:

- intrinsic single pixel resolution
- expected number of hits
- efficiency
- number of channel
- costs

<table>
<thead>
<tr>
<th>Properties</th>
<th>BC418</th>
<th>BC420</th>
<th>BC422</th>
<th>BC422Q</th>
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</thead>
<tbody>
<tr>
<td>LY(%Anthracene)</td>
<td>67</td>
<td>64</td>
<td>55</td>
<td>19</td>
</tr>
<tr>
<td>Rise time (ns)</td>
<td>0.5</td>
<td>0.5</td>
<td>0.35</td>
<td>0.11</td>
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<tr>
<td>Decay time (ns)</td>
<td>1.4</td>
<td>1.5</td>
<td>1.6</td>
<td>0.7</td>
</tr>
<tr>
<td>Wavelength (peak, nm)</td>
<td>391</td>
<td>391</td>
<td>370</td>
<td>370</td>
</tr>
<tr>
<td>Attenuation length (cm)</td>
<td>100</td>
<td>110</td>
<td>8</td>
<td>8</td>
</tr>
<tr>
<td>Resolution (ps)</td>
<td>48±2</td>
<td>51±2</td>
<td>43±2</td>
<td>66±3</td>
</tr>
</tbody>
</table>

Numbers to be evaluated on Monte Carlo simulation…

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(few) MonteCarlo results

120mm length pixel is the best compromise between hit multiplicity, time resolution and number of channels.

different pixel height (40 or 50 mm) increases efficiency

tracks envelope in a gradient magnetic field (MEG COBRA magnet)
Final pixel design

- Double side read-out with **6 SiPMs (ASD-NUV3S-P) array** mounted in series connection on a PCB:
  - increase sensor coverage;
  - small material budget along positron trajectories: **PCBs act also as frame**.
- **120 x 50 (40) x 5 mm$^3$ fast plastic scintillator (BC422)** coupled with optical cement, wrapped with reflector (3M mirror).
- Impact time and position reconstructed with sum / difference of single array time.
- **MCX connector for backplane plugging** (no cables on TC).
Proving multi hit @BTF

Resolution improves combining more hits:  
\[ \sigma^2 = \frac{\sigma_{\text{single}}^2}{N_{\text{hit}}} + \frac{\sigma_{\text{inter pixel}}^2}{N_{\text{hit}}} + \sigma_{\text{MS}}^2 (N_{\text{hit}}) \]

Tested under beam @BTF (Frascati, IT) in 2013 and 2014 with different setups.

- e+ beam
- 48 MeV
- multiplicity: ~1.5

2014 configuration
- 6 SiPMs series + PCB
- 120 x 50 x 5mm³ pixels
- backplanes
Beam tests @BTF: results

Time resolution evaluated as the width of

\[ \Delta T(N) = T_{ref} - \frac{1}{N} \sum_{i=1}^{N} T_i \]

\[ \Delta T(N) = \frac{1}{\sqrt{2}} \left[ \frac{1}{N/2} \sum_{j=1}^{N/2} T_{a_j} - \frac{1}{N/2} \sum_{i=1}^{N/2} T_{b_i} \right] \]

- **RC analysis**
- **Odd/even analysis**

Reconstructed charge

Differences due to:
- Longer pixel (120mm vs 90mm)
- Different SiPM model

Resolution < 30ps @8hits

Resolution ~32ps @9hits
Beam test @Paul Scherrer Institute

- $e^+$ from Michel decay
- Hit rate as expected in MEG II

$\mu$-beam ($\pi e5@PSI$), $\sim 10^8$Hz DC beam
- 8 counters mounted on backplanes
  - 6 120x40x5mm$^3$ + 2 120x50x5mm$^3$
- Final pixel layout
- 2 RC counters (trigger/selection)
- BGO calorimeter (beam monitoring)
Resolution does not depend from beam rate in the range ~20-300 kHz.

~35ps resolution was found at the expected MEG II rate (~150 kHz).
Final detector layout

- 256 **pixels** connected to **backplanes**
- **different heights** in different TC regions.
- optimized layout (fit mechanical constraints from magnet and DCH).
- expected reso ~30/35ps

**Work is ongoing!**
Conclusion and next steps…

- We upgraded the MEG Timing Counter experiment, developing a new detector based on hundreds of scintillator tiles with SiPMs readout.
- An improvement in time reso is expected thanks to good single pixel resolution and multiple hits events.
- A 3 years long R&D led to a 120x50(40)x5 mm\(^3\) pixel, equipped with 6-SiPMs arrays in series connection.
- Prototype were tested under beam, proving a time resolution of \(~30-35\text{ps} @8\text{hits}\) (x2 better than previous TC)
- The final detector is currently under construction.
- Next step: MEG engineering run 2015.
Back up
A. Stoykov (Paul Scherrer Institute)
SiPM comparison: PDE, rise time

**PDE**: light from a LED ($\lambda \sim 400\text{nm}$). Relative PDE is calculated from $P(N=0)$ with a correction for accidental coincidences of dark counts.

**Rise Time**: 10-90% time from waveform analysis
Series vs parallel

Series connection:
- OV adjustment between SiPMs with different $V_{BD}$
- reduced capacitance $\rightarrow$ narrower pulse
- **better time resolution**
- higher bias voltage

Parallel connection:
- lower bias voltage
- OV not adjusted
- increased capacitance $\rightarrow$ wider pulse
- **poor time resolution**

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SPICE simulation by D. Kaneko
Probing the multi hit scheme

Time resolution improves combining time measured by many pixels:

\[
\sigma^2 = \frac{\sigma_{\text{single}}^2}{N_{\text{hit}}} + \frac{\sigma_{\text{inter pixel}}^2}{N_{\text{hit}}} + \sigma_{\text{MS}}^2 (N_{\text{hit}})
\]

Tested under beam with prototypes at BTF (Frascati, IT), PSI (Villigen, CH)

BTF (2013, 2014):
- 50MeV positron,
- very low rate (~50Hz),
- provisional pixel setup

PSI:
- MEG II environment,
- positrons from \(\mu\) decay,
- high beam rate (MEG II expected: ~100kHz/pixel)

Confirm multiple hit scheme
Test of backplane

Final pixel layout
High rate environment
Proving multi hit @BTF

Resolution improves combining more hits: \[ \sigma^2 = \frac{\sigma_{\text{single}}^2}{N_{\text{hit}}} + \frac{\sigma_{\text{inter pixel}}^2}{N_{\text{hit}}} + \sigma_{\text{MS}}^2 (N_{\text{hit}}) \]

Tested under beam @BTF (Frascati, IT) in 2013 and 2014 with different setups.

- 2013
  - 4 SiPMs series + PCB
  - 90 x 50 x 5mm\(^3\) pixels
  - cables, no backplanes
  - check multi-hit

- 2014
  - 6 SiPMs series + PCB
  - 120 x 50 x 5mm\(^3\) pixels
  - backplanes
Calibration

We are developing 2 independent ways for pixel inter-calibration based on Michel data and laser calibration.

**Michel**

Developed on MC:
- Analysis with clustering and tracking;
- TOF between counters calculated from tracking;
- Time offset is calculated by minimizing $\chi^2$;
- Tested adding a random time offset (5ns RMS)

$$\chi^2 = \sum_i \sum_j \left( \frac{(T_{ij} - (T_{0i} + TOF_{ij} + \Delta T_j))}{\sigma} \right)^2$$

**Laser**

- Hamamatsu ps laser (MODEL??)
- A chain of optical splitters is used to divide the light signal then
- light is injected in the pixel by means of an optical fiber coupled to the scintillator
- independent from other detector

Sending a synchronous light pulse to all the pixels.

\[
\Delta T = T_{\text{true}} - T_{\text{calib}}
\]
Backplanes

Pixels are connected to the BPs by means of MCX connectors and fixed to the main structure through the thermal connection. BPs usage (940x25x5.5mm³) avoid large number of cables on TC (>500). In the inner region there is not enough room: pixels are mechanically connected to the structure using thermal blocks, while a coaxial cable connect them to the BP. We need 4 different BP layout (for pixel positioning)

Successfully tested at BTF and PSI in 2013 and 2014.
New Timing Counter overview

Pixels have different sizes and frame in different detector region

Zoom of the inner region

40mm height, PCB “L” shaped to fit inner COBRA step

40 or 50mm height, “standard” pixel design

All configurations have been built and tested
The pictures show an example of pixel mounting

- MCX connector
- Copper block for thermalization and mechanical support