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Modeling of Radiation Damage Effects in Silicon Detectors at High Fluences HL LHC with Sentaurus TCAD

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In this work we propose the application of an enhanced radiation damage model based on the introduction of deep level traps / recombination centers suitable for device level numerical simulation of silicon detectors at very high fluences (e.g. $1 \div 2 \times 10E16$ 1 MeV equivalent neutrons).

The model is based on a past modeling scheme featuring three levels with donor removal and slightly increased introduction rate which was successfully adopted for the optimization of the silicon detectors operating at LHC.

The new fluences expected at HL LHC impose new challenges and the extension of the model (valid up to $1\times10E15$ n/cm2) is not straightforward. New effects have to be taken into account (e.g. avalanche multiplications and deep-level capture cross section dependencies on electric field, temperature and fluences), at the same time keeping the solid physically based approach of the modeling (e.g. by using no fitting parameters). This will preserve the generality of the approach, allowing its application to the optimization of different kind of detectors.

We present the comparison between simulation results and experimental data for p-type substrate structures in different operating conditions (temperature and biasing voltages) for fluences up to $2.2 \times 10E16$ n/cm2. The good agreement between simulation findings and experimental measurements fosters the application of this modeling scheme to the optimization of the next silicon detectors to be used at HL LHC.

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