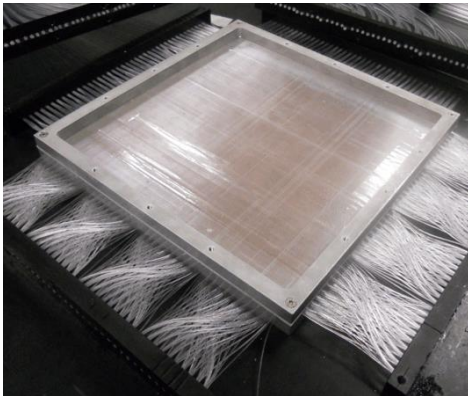


# SE<sup>2</sup>D

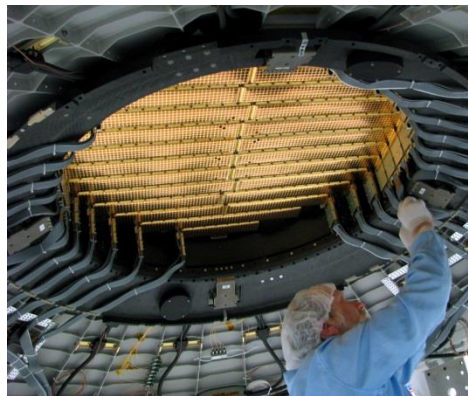
Sensor with Embedded Electronic Development

# SE<sup>2</sup>D

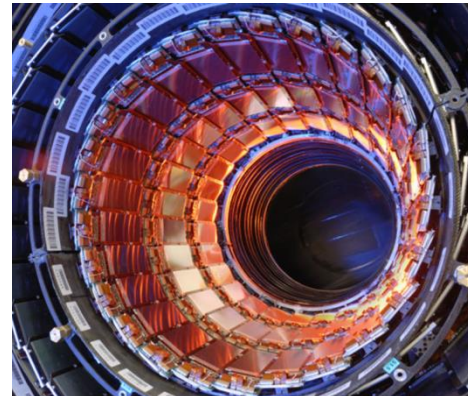
Employ monolithic sensors in experiments to extend their scientific performance.



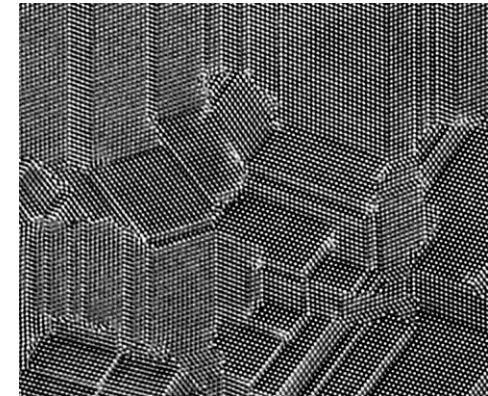
Solid state detectors for medical applications



Space-born trackers and telescopes



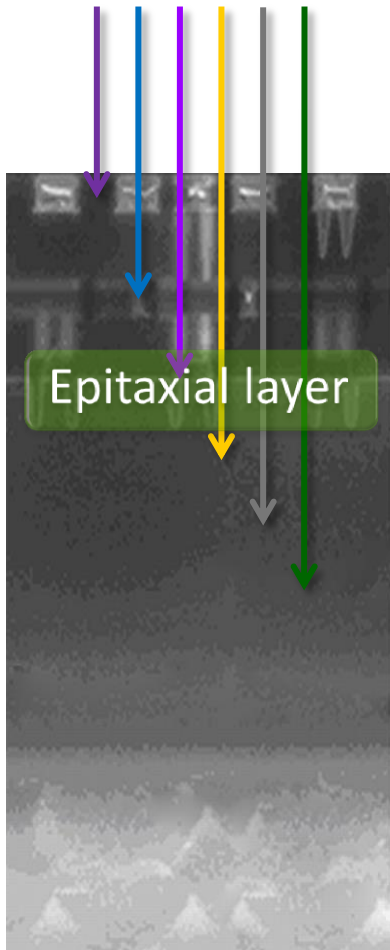
HEP trackers and calorimeters



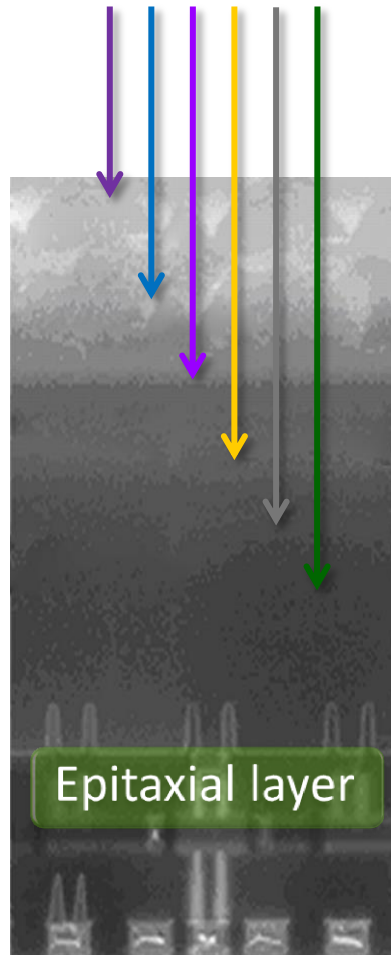
Electron microscopy sensors

Less power consumption, higher granularity, lower assembly cost. Cannot address every issue in present solid state pixel physics detectors, but definitely most of them.

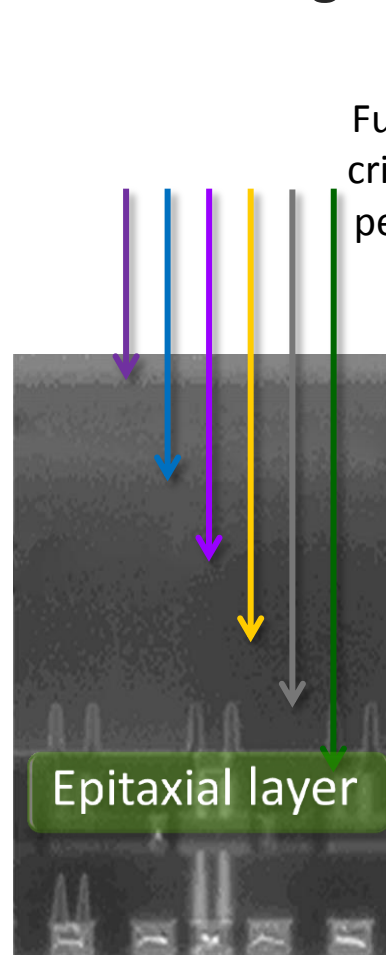
Standard



Back-lit

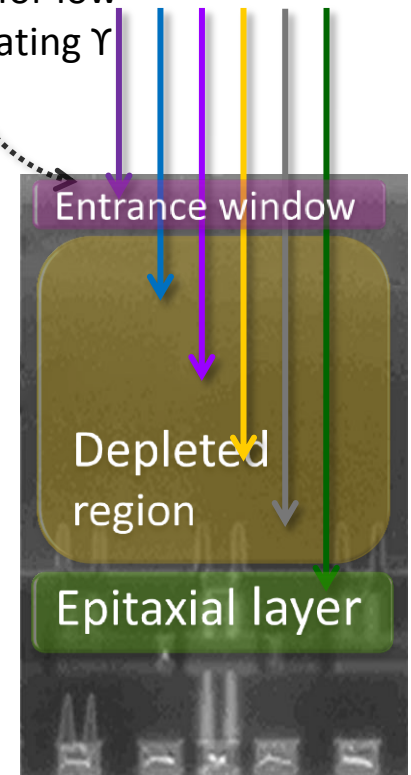


Back-lit  
+ Thinning

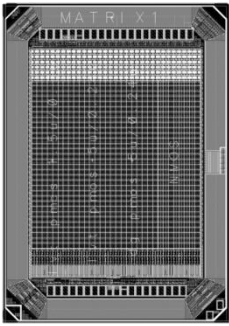
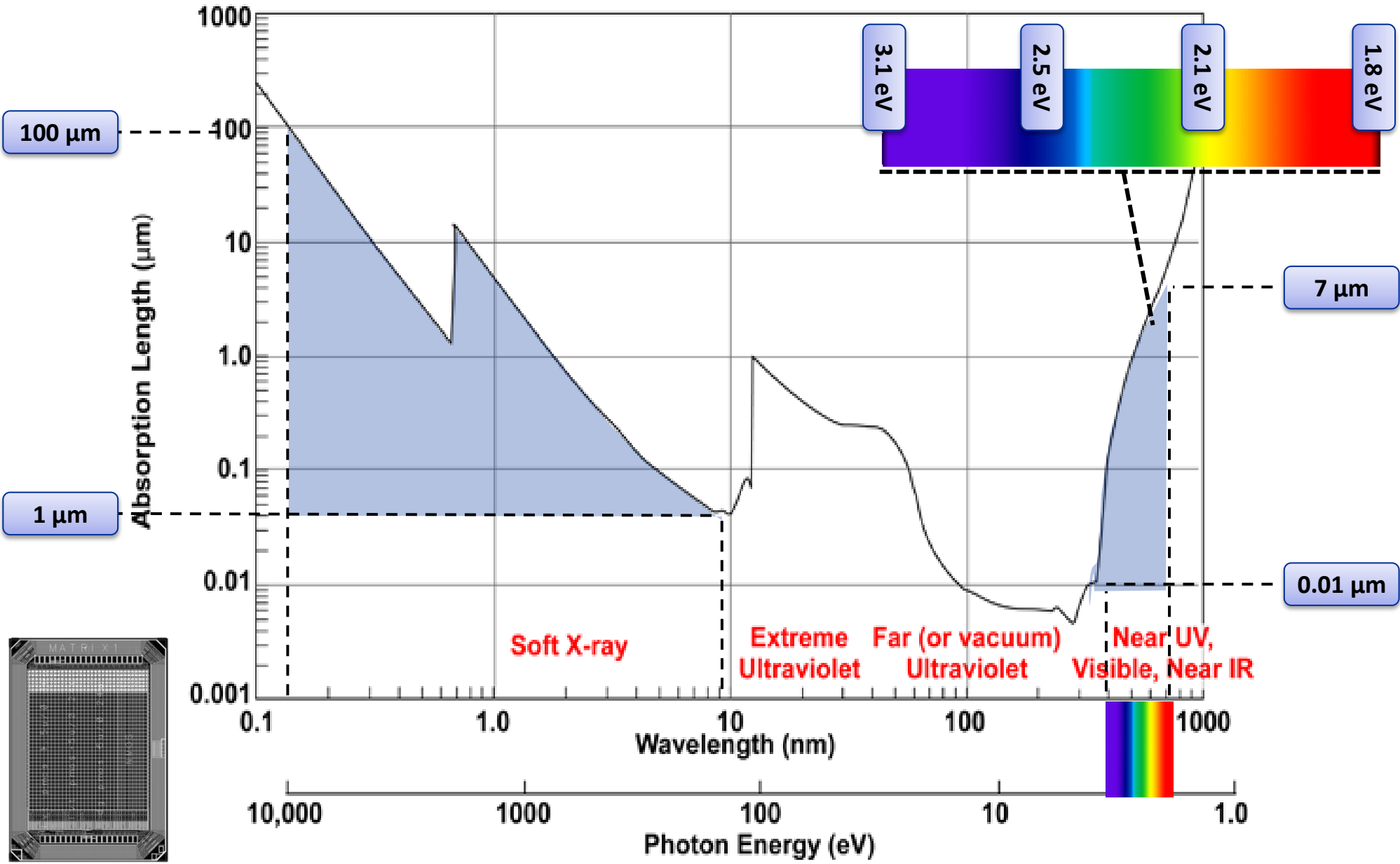


Back-lit  
+ Thinning  
+ Fully depleted

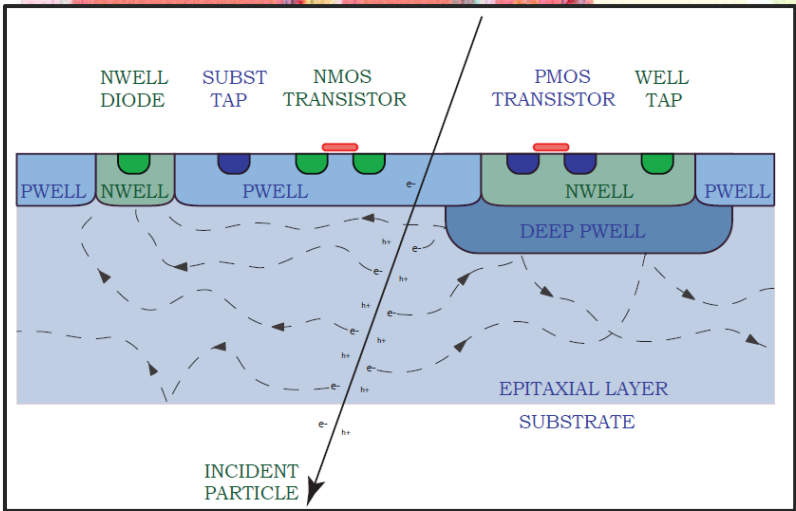
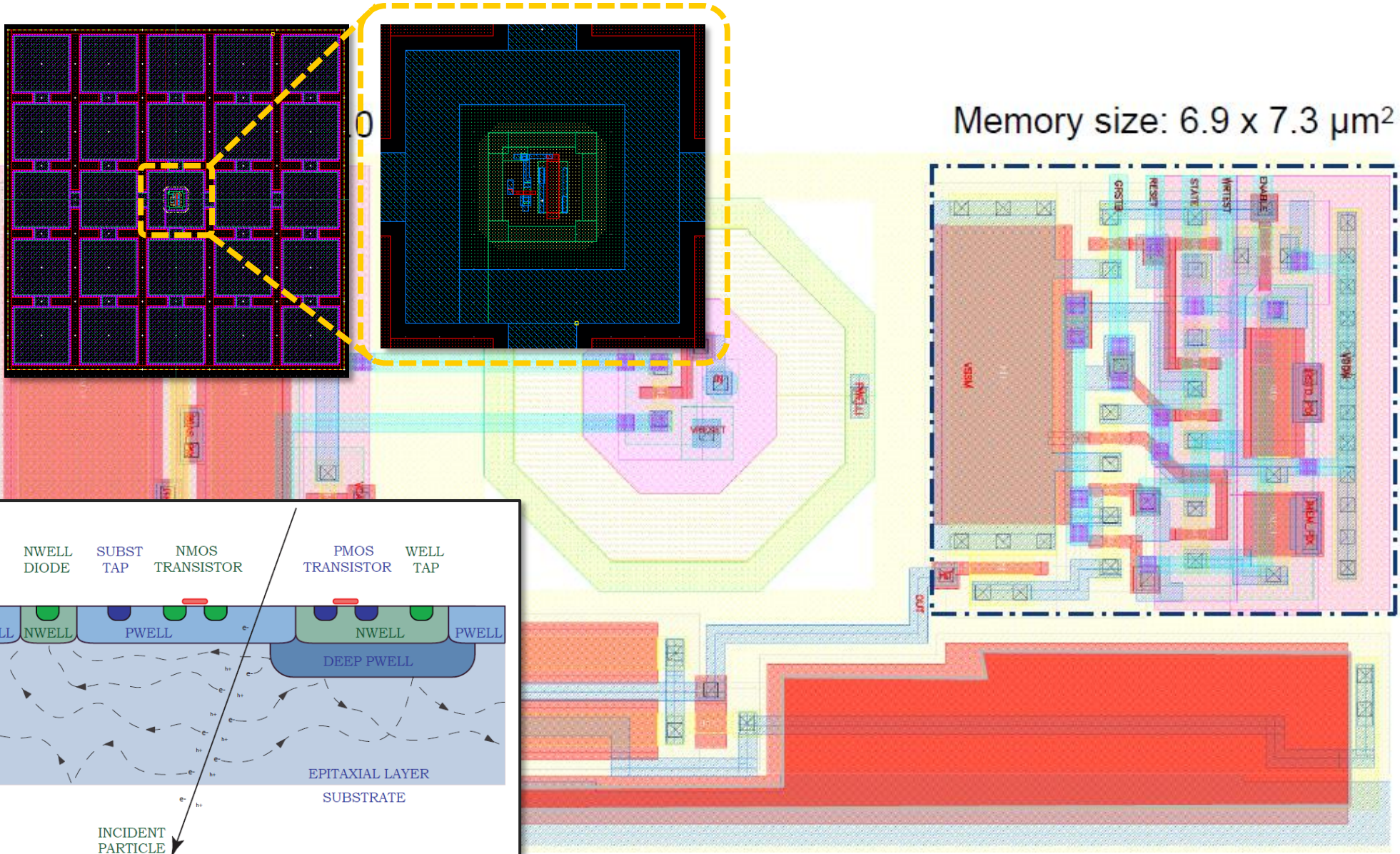
Full depletion  
critical for low  
penetrating  $\gamma$



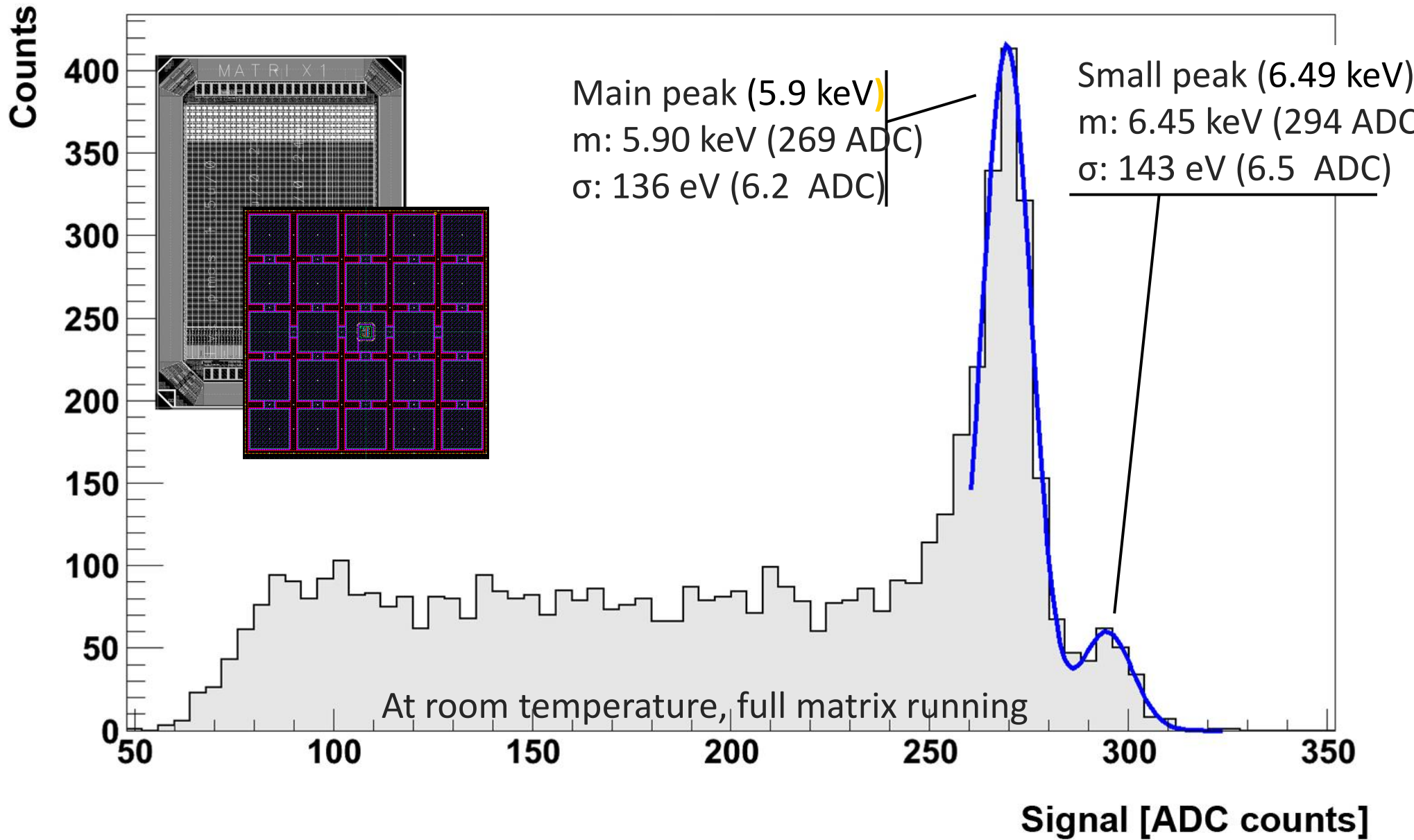
# SE<sup>2</sup>D – full spectrum (1 eV – 10 keV) with a single device



# SE<sup>2</sup>D – electronic in depleted sensor for low power and noise

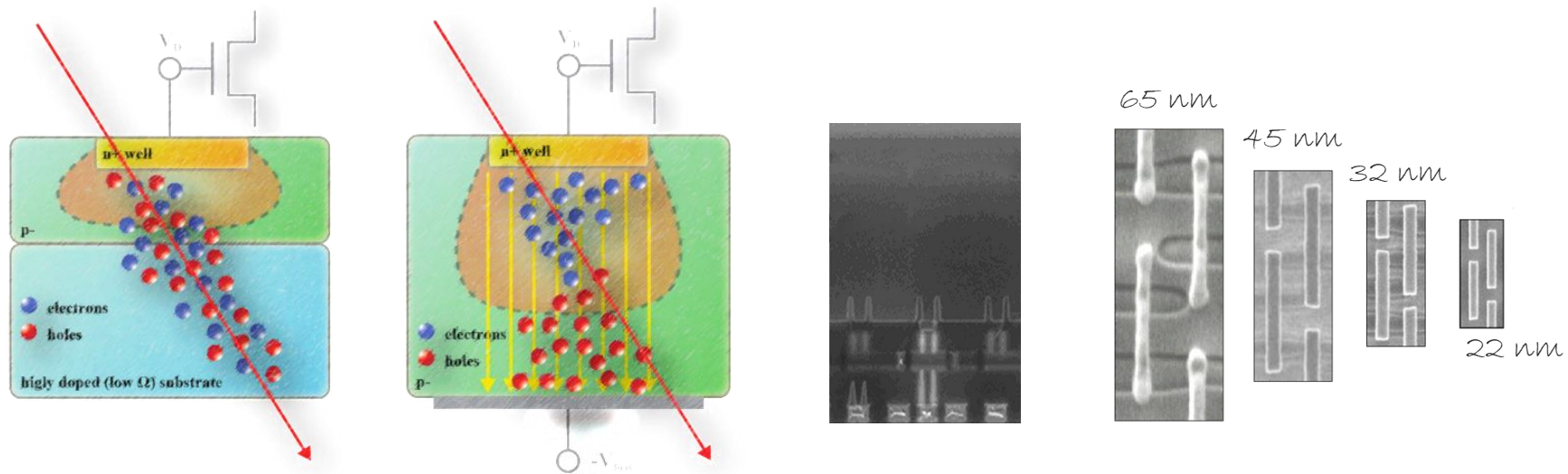


# SE<sup>2</sup>D – low noise, integrated electronic performance



# SE<sup>2</sup>D

Monolithic sensors are mass-produced: **advanced technology at cheap prize.**



The scientific community lacks both the **funding** and **know-how** to get where SE<sup>2</sup>D aims to go. The big HEP experiments (the only with substantial funding) are ineffective in focusing the resources for any very innovative R&D in that sector.

## Reason for a **Technology Transfer** driven project – 2

Trying to address the previous slide issues, a technology transfer driven project could set the base for a different future modus operandi (hence the name SE<sup>2</sup>D).



Collaborate with **industrial/private partners** who can benefit from direct or indirect outcomes of our research to obtain “special prices” in exchange for know-how.



Find among our own scientific targets what part could be shared in market-oriented applications as a mean of financing ourselves.




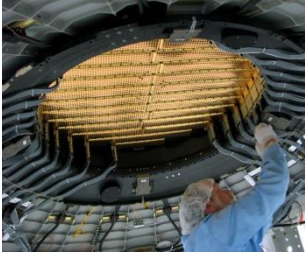
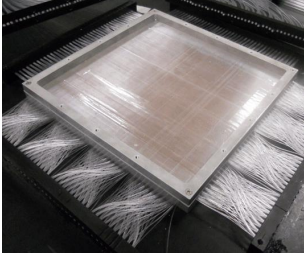
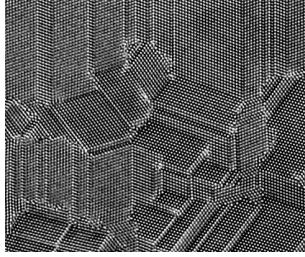
Take the challenge of a different “metric” for success measurement and competition drive: **beating other team supporting different industrial/private partners could be way more challenging than being cheered at the usual conference where everyone knows everybody!**



# LFfoundry

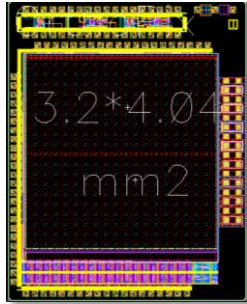


In Avezzano there is an European medium-sized specialty foundry dedicated to microelectronic circuits production (11.000 m<sup>2</sup> clean room, down to 90 nm processes), employing about **1600 people**.

<p>Our scientific interest</p>	 <p>GHz tracking capability, ns timing.</p>	 <p>&lt; 20 mW cm<sup>-2</sup> power, fine pitch (&lt; 10 μm).</p>	 <p>Sparsification and digital conversion.</p>	 <p>Ultra-thin, very large area sensors</p>
<p>Enabling technology (INFN has the know-how)</p>	<p>Medium/high resistive epi layer (≥ 500 Ω cm), quadruple well technology.</p>	<p>Low capacitance node, high routing density, minimal in-pixel cell electronics.</p>	<p>IPs blocks embedded to perform timing, sparsification and digital conversion.</p>	<p>Back illuminated, fully depleted and back-processed device.</p>
<p>Commercial interest</p>	<p>Better signal for IR applications, low light vision. Low-energy monolithic panels for X-rays.</p>	<p>Movie and medium large format camera market. Niche space-born camera market.</p>	<p>Fully digital output sensor for prosumer and consumer imaging applications.</p>	<p>Better signal on all visible wavelengths. <u>Blue/violet S/N much better</u> than current state of the art.</p>

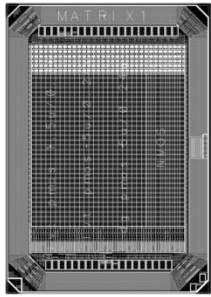
National responsible: **A. Rivetti** (TO) & **P. Giubilato** (PD)

## WP1 Torino



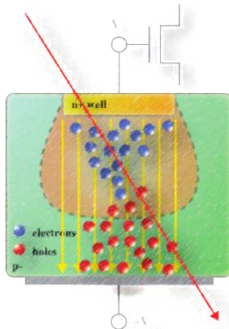
Chip and IPs design, production cycle management, interface with the foundry. Long experience and expertise in pixel ICs design.

## WP2 Padova



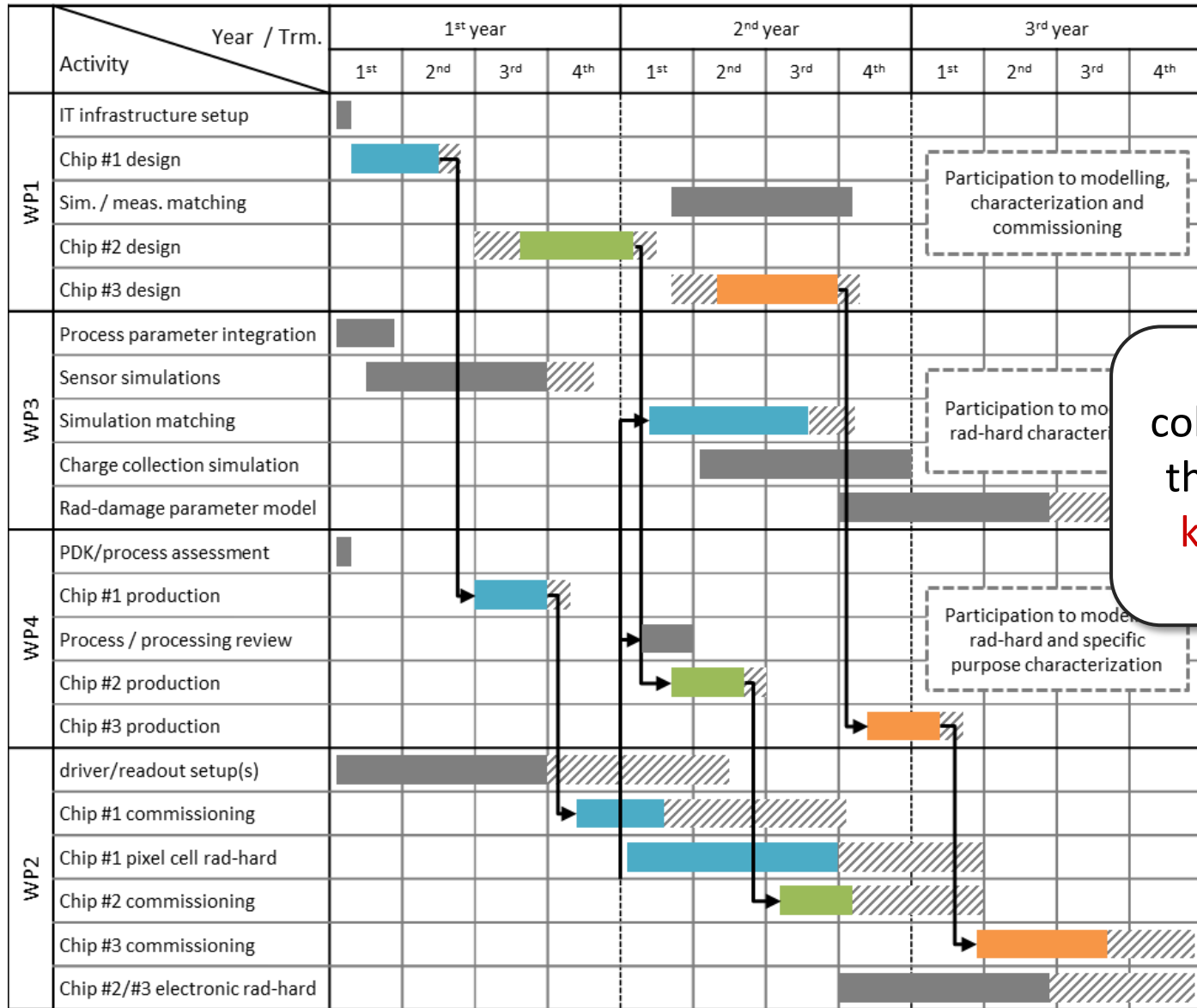
Commissioning, measurement and radiation hardness studies, plus help in design and simulation in collaboration with other working groups.

## WP3 FBK







Device sensor simulation and process development in close collaboration with the foundry (a quite unique opportunity in the ICs world).

# SE<sup>2</sup>D – timeline of the 3 years project



Thanks to collaboration with the foundry, 150 keuro/year will suffice

	1 <sup>st</sup> year	2 <sup>nd</sup> year	3 <sup>rd</sup> year
Goals	<ul style="list-style-type: none"> <li>• Chip readout development (mostly reuse of previous projects equipment).</li> <li>• First chip commissioning, table top measurements.</li> <li>• Some IP blocks design (under Torino guidance).</li> </ul>	<ul style="list-style-type: none"> <li>• Chip rad-hard assessment for both ionizing and non ionizing radiation.</li> <li>• Single Event Effect characterization.</li> <li>• Efficiency measurements at test beams.</li> </ul>	<ul style="list-style-type: none"> <li>• Multi-layer odoscope for tracking efficiency assessment.</li> <li>• Multi-chip systems for high-bandwidth data acquisition and sparsification assessment.</li> </ul>
Resources	<ul style="list-style-type: none"> <li>• 1 assegnista/ricercatore 12 mesi (setup/measurements).</li> <li>• 1 tecnologo eqv. 6 mesi (design).</li> <li>• 1 tecnico elettronico 6 mesi</li> <li>• 6 mesi uomo di officina elettronica.</li> <li>• IT support for design tools installation/maintenance.</li> </ul>	<ul style="list-style-type: none"> <li>• 1 assegnista/ricercatore 12 mesi (setup/measurements).</li> <li>• 1 tecnologo eqv. 6 mesi (design).</li> <li>• 1 tecnico elettronico 6 mesi</li> <li>• 3 mesi uomo di officina elettronica.</li> <li>• IT support for design tools installation/maintenance.</li> </ul>	<ul style="list-style-type: none"> <li>• 1 assegnista/ricercatore 12 mesi (setup/measurements).</li> <li>• 1 tecnologo eqv. 6 mesi (design).</li> <li>• 1 tecnico elettronico 6 mesi</li> <li>• 3 mesi uomo di officina elettronica.</li> </ul>

-  Push state-of-the-art in detection for **future experiments needs**.
-  Consolidate INFN position in **sensors & microelectronic** technology.
-  Develop an effective **collaboration model** between INFN and the industry.
-  Use INFN know-how to support **national strategic infrastructures** (i.e. microelectronics companies) into the worldwide market competition.