

Deliverable 8.1 FTK workshop at PRIELE - 2014

Author: Paola Giannetti (FTK collaboration, INFN Pisa)

FTK Workshop at **PRIELE** (D8.1) March 10-11 2014

Workshop Goal: Summary of all activities performed on the first year of the project (2013).

HOW IT HAS BEEN ORGANIZED:

Participants:

General Assembly members:

Mauro Dell'Orso – UNIP, Chair
Kostas Kordas – AUTH, Member
Alessandro Iovene – CAEN, Member
Betty Magnin – CERN, Member
Giovanni Calderini – CNRS, Member
Petros Soukoulis – PRIELE, Member
Simone Donati – UNIP, Member

connected by skype from Pisa
present in Alexandroupolis
connected by skype from Viareggio
connected by skype from CERN
connected by skype from Paris
present in Alexandroupolis
present in Alexandroupolis

Additional Executive Board members:

Dimitrios Sampsonidis – AUTH, WP 8 Coordinator, Member
Paola Garosi – CAEN, WP 2 Coordinator, Member
Francesco Crescioli – CNRS, WP 6 Coordinator, Member
Andreas Sakellariou – PRIELE, WP 1 Coordinator, Member
Marco Piendibene – UNIP, WP 5 Coordinator, Member
Chiara Roda – UNIP, WP 4 Coordinator, Member

present in Alexandroupolis
connected by skype from Viareggio
present in Alexandroupolis
present in Alexandroupolis
connected by skype from Pisa
connected by skype from Pisa

Other Participants:

P. Giannetti – INFN Pisa
K. Mermikly and Panagiotis Kalaitzis - Prisma Electronics
S. Nikolaidis, C.L. Sotiropoulou, Stamatios Gkaitatzis, & Panagiotis Neroutos – AUTH

C. Gentsos – AUTH
S. Citraro, P. Luciano – UNIP
D. Magalotti - University of Modena
G. Volpi – UNIP

present in Alexandroupolis
present in Alexandroupolis
present in Alexandroupolis
connected by skype from Salonicco
present in Alexandroupolis
connected by skype from CERN
connected by skype from Lisbon

The scheduled presentations are available at the site

<https://agenda.infn.it/conferenceDisplay.py?confId=7686>



It was a very successful final workshop with a lot of scientific high level presentations. The Prisma organization was excellent, with a very good network connection, and a wonderful conference room as can be observed by the photo above. We had also the Delta Tv the 11th of March, you can see here the video:

http://www.youtube.com/watch?feature=player_embedded&v=lyRPXxa7_bs

We had also an interesting visit to the Prisma Electronics production line (see photos below):



The FTK workshop agenda: *here is the list of PRESENTATIONS*

Petros Soukoulis presented the Prisma Electronics company, the story of the company, the main projects, with a special space to the very successful project LAROS (see slides).

PRESENTATIONS about the work performed by the FTK researchers (see the slides at web site) followed:

WP1 talks

- "Design and test of the MiniLamb with the Evaluation Board"
- "Design and test of the AMBSLP+MiniLamb"
- "Design and Simulation of SLP1"

by P. Luciano
by S. Citraro
by C. Gentsos

WP2 talks

- "AUX Board and AMBoard Integration at CAEN"
- "The CAEN PS - cooling tests @ USA15"
- "FTK_IM: design and integration in FTK"
- "Error Detection and HANDling in FTK"
- "lost sincronization"

by M. Piendibene
by A. Iovene
by L.-C. Sotiropoulou
by L.-C. Sotiropoulou
by P. Neroutsos

WP4 talks

- "Simulation for the AMB-AUX combined tests"
- "Simulation for the FTK_IM test"

by G. Volpi
by S. Gkaitatzis

WP6 talks

“Silicon Detector R&D for Phase II”

by G. Calderini

WP7-8 talks

“The Web site today”

by K. Kordas

“Training and MOCAST workshop at AUTH”

by L.-C. Sotiropoulou

Social occasions were the lunches, in the conference site with an external catering, and wonderful evening dinners all together. All was offered by Prisma.

It was a great opportunity to reinforce each other knowledge and to discuss results of the first FTK year. We came to the conclusion that this first year was really successful.

Compared to the first year workshop, the information was correctly delivered and received, showing that collaborators have already acquired a good expertise in the project, that is the transfer of knowledge was really successful.

No negative remark for this first year of work.