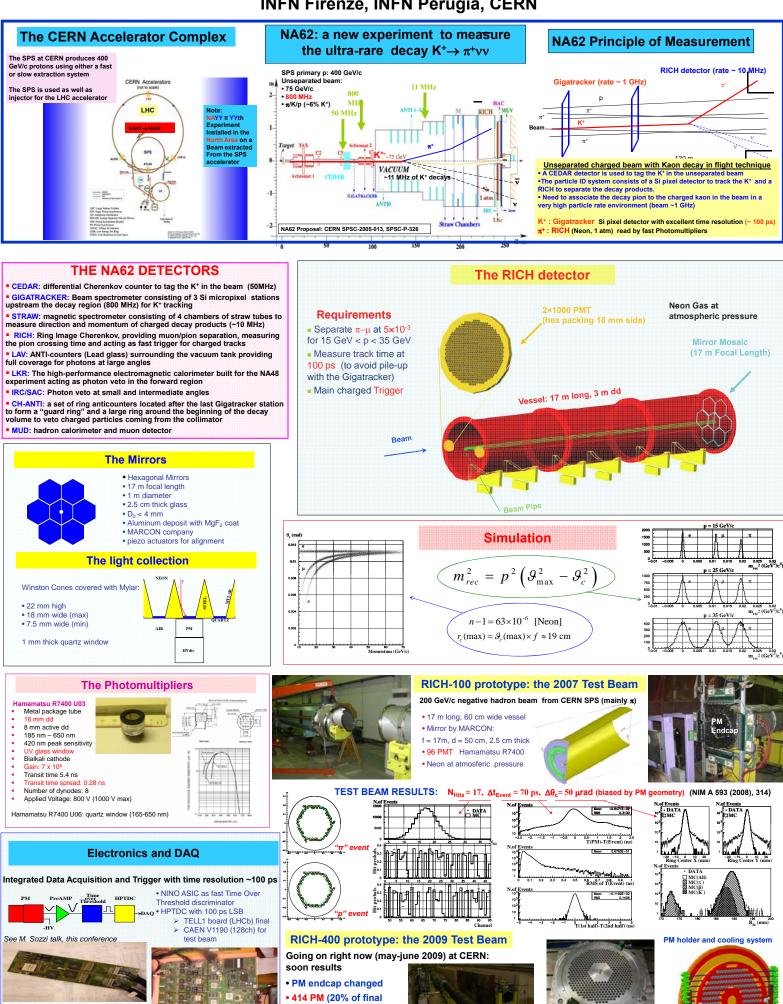
The RICH detector of the NA62 experiment at CERN

INFN Firenze, INFN Perugia, CERN



static memory on-board.
10 layers PCB (16xLVDS connectors, individual shielding) Full Motherboard (512 channels) with PC on-board and 4 x1 Gb/s links

detector)

Validate π-μ separation

in 15<p<35 GeV/c

Improve PM cooling

Electronic board:

128 TDC channels, 100 ps resolution (HPTDC, CERN) control FPGA and

P. Cenci and M. Pepe, INFN Perugia – 11° Pisa Meeting on Advanced Detectors, La Biodola, Isola d'Elba, May 24-30, 2009