Application of the 5 GS/s Waveform Digitizing Chip DRS4

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The DRS4 Chip

Evaluation Board

Four Channel 5 GS/s Oscilloscope with USB power and readout

PC Oscilloscope Application (Windows and Linux)

Basic Parameters

Technology: UMC 0.25µm
Number of Chn.: 9 with 1024 cells
cascadable up to 8192
Power: 17.5 mW/chann.
Sample Speed: 200 MSPS to 5 GS/s

Functional Block Diagram

Experiments using DRS Chips

Ethernet Board (planned)

- 8 Channel board to be mounted directly on detector electronics 50 mm x 100 mm
- Preamplifier for PMTs, APDs, SiPMTs
- Read out through Gigabit

http://drs.web.psi.ch