

# The construction and space qualification of the control electronics for the tracker detector cooling system of the AMS-02 experiment



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### The tracker cooling system and its EGSE

The AMS-02 Tracker Thermal Control System (TTCS) is a carbon dioxide two-phase cooling system developed by NIKHEF (The Netherlands), Geneva University (Suisse), INFN Perugia (Itality), Sun Yat Sen University Guangzhou (China) and NLR (The Netherlands). Its objective is to provide accurate temperature control of AMS Tracker from-tend electronics. The two-phase loop incorporates a long evaporator, picking up the heat from the front end electronics cards in the six silicon planes located inside and just outside the AMS-02 superconducting magnet. The heat is transported to a condenser connected to a heat pipe radiator. The liquid is transported back to the evaporator by means of a mechanical pump. The temperature on the fluid is stabilized by an accumulator. A total of 144 Watt is produced at 192 front-end cards and an additional electronics. The start of 144 Watt is produced at 192 front-end cards and an additional 6-10 Watt cooling capacity is required for additional electronics.

- The thermal design challenges of the TTCS for ASM-02 are Compatibility with the existing Tracker Hardware.
- Limited volume
- Limited volume. Multiple and widely distributed heat inputs up to 160 W. Minimal temperature gradients of less then 1°C Low mass budget < 72.9 kg, low power budget < 80 watt. High reliability ie. fully redundant system design. Two radiators thermally out of phase.

Those challenges are meet in the design shown schematically in the cooling system general schematics (Fig.1).

The tracker cooling system may be viewed as a set of different sensors and actuators which are necessary for bringing the tracker detector to a uniform temperature at which it can operate properly. The sensors include: P1000 themistors, semiconductor thermal sensors, differential and abolute pressure sensors, and pump rotational speed sensors. The actuators are: resistive heaters, Peltier heat pumps and liquid pumps. In order to test the TTCS control electronics (TTCE) and to emulate the TTCS behavior we developed an Electronic The ECSE (fig.2 and 3) simulate the behaviour of all sensors and actuators in the TTCS. This means that it must generate the same signals from the electrical point of view but also its global behaviour, namely the response to any internal and external condition change should be the same as the actual tracker cooling system. In order to achieve this performance appropriate software that includes the simulated response of the tracker cooling system have been developed.



Fig.1 The TTCS



Fig.2 EGSE crate 1

Fig.3 EGSE crate 2

The tracker cooling control electronics

The tracker cooling system described above includes various sensors and actuators. The sensors include: P11000 thermistors, semiconductor thermal sensors, differential and absolute pressure sensors, and pump rotational speed sensors. The actuators are resistive heaters, petiter heat pumps, and liquid pumps. Since the pumps are essential elements of the loops they are dual redundant in each loop. The electronic control system TTCE (Tracker Thermal Control Electronics) is included in a crate that contains two redundant independent contri-systems, A and B. Each system is connected to both the primary and secondary loops, so each individual control system can control each loop

or particular, a tank be called system is connected to been the primary and eccounting topys, so each mandatal counter system call counter been The crate contains 5 beards: The TTEC A board which contains the interface electronics for the slow control system of the experiment, the thermal sensor readout (both semiconductor sensors and the PH1000 sensors), the basic logical functions, and the safeguard logic (fig.4).

The TTEC B board (same as A) The TTEP A board contains circuits for generating local supplies from the primary 28V and the power conversion and switch circuits for the

heaters and petiter heat pump (fig.5). The TTEP B board (same as A) The TTPP board (same as A)

# The Qualification tests

The boards will be produced in three phases: EM (Engineering Model): will be used for functional tests and preliminary tests of the loop CM (Qualification Model): will be used for qualification tests, namely Environmental Stress Screening, Thermo-mechanical testing, Thermo-vacuum test and EMI/EMC tests

FM/FS (Flight/Spare Model): will be used for flight after some acceptance test.

In order not to stress the Flight Model Board QM test specifications are more severe and complete compared to the same tests that the FM should undergo. The tests for QM boards are the following:

### Board level ESS Thermo mechanical test

Thermo Vacuum test EMI/EMC test

For FM we make the first three test (not the EMI/EMC) with reduced stress levels. Board level ESS is performed cycling boards between -45 °C and 85°C for 10 cycles one hour each temperature at power off(For FM/FS temperatures are

Board level ESo is performed opung device Section 2. The first phase is a thermal cycling test in air with the scheme presented in fig. 7 with 10 intermediate thermal cyclical stress it is composed of three phases. The first phase is a thermal cycling test in air with the scheme presented in fig. 7 with 10 intermediate thermal cyclical stress it is composed of three phases. The first phase is a thermal cycling test in air with the scheme presented in fig. 7 with 10 intermediate thermal cyclical stress is shown in fig. 9 for OM (For FMF's temperatures are the same as in the thermo mechanical test) EMI/EMC tests include conducted susceptibility: CS01, CS02, CS06, conducted emissions: CE01, CE03, Radiated susceptibility: RS02: RS03: and radiated emissions: RE02. Figure 10 shows the outoput of a RE02 test performed using QM.







Fig. 4 The TTEC

Fig. 5 The TTEP

Fig. 6 The TTPF



Fig. 7 Thermal cycling scheme of the ESS the table shows the values both for the QM phase and for the FM/FS



Fig. 9 Thermo-vacuum TEST cycles



Fig. 8 Random vibration spectrum at which the TTCE crate will be tested



Fig.10 Low frequency RE-02 EMI test

