

GPU Computing in High Energy Physics

Friday, 12 September 2014

GPU in Low Level Trigger (2/2) (11:30 - 13:15)

-Conveners: Silvia Arezzini

time	[id] title	presenter
11:30	[47] INTEL HPC portfolio	POLITANO, Emiliano
12:15	[0] The GAP project: GPU for online processing in low-level trigger	FIORINI, Massimiliano
12:45	[33] A FPGA-based Network Interface Card with GPUDirect enabling real-time GPU computing in HEP experiments.	LONARDO, Alessandro