

## INTEL HPC portfolio

*Friday, 12 September 2014 11:30 (45 minutes)*

The use of heterogeneous architectures in HPC at the large scale has become increasingly common over the past few years. One new technology for HPC is the Intel Xeon Phi co-processor that is x86 based, hosts its own Linux OS, and is capable of running most codes with little porting effort. However, the Xeon Phi architecture has significant features that are different from that of Xeon CPUs. Attaining optimal performance requires an understanding of possible execution models and the architecture. This talk highlights various options in Intel HPC portfolio.

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**Session Classification:** GPU in Low Level Trigger (2/2)