

# **Teldes status report**



B. Checcucci<sup>1</sup> A. Papi<sup>1</sup> M. Piccini<sup>1</sup> M. Bizzarri<sup>1</sup> C. Santoni<sup>1</sup> V. Duk<sup>1</sup> M. Lupi<sup>3</sup> M. Barbanera<sup>3</sup> A. Salamon<sup>2</sup> V. Bonaiuto<sup>2</sup> N. De Simone<sup>2</sup> L. Federici<sup>2</sup>

- 1 INFN Perugia Italy
- 2 INFN Roma Tor Vergata Italy
- 3 Università degli Studi di Perugia Italy

- Teldes production status
- Intertel pre production status
- Conclusions



## **TELDES** production



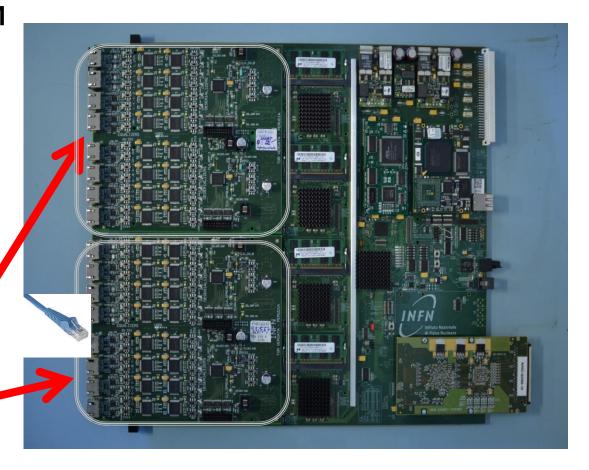
#### "TELDES"

To receive data from the CREAM



8 ethernet cables per board RJ45 terminated

#### 2 mezzanine cards per TEL62





# Teldes production status



# All the boards 52 (including 20%spares) have been sent to CERN on August 28 th



### **Outlook**



#### TELDES PRODUCTION GANTT with tests @ Perugia

#### HERE WE ARE

#### Bruno Checcucci INFN Perugia

ID	Nome attività	Inizio	Fine	Durata -	mag		giu 2014			lug 2014			ago 2014			T	5	set 2014			ott 2014			
					4/5 11/5	5 18/5 25/5	1/6	8/6 1	i/6 22/6	6 29/6	6/7	13/7 20	0/7 27/7	3/8	10/8	17/8 24/	31/8	7/9	14/9	21/9 2	28/9 5	ī/10   12/1	19/10	26/10
1	Final Electrical Scheme Definition	05/05/2014	28/05/2014	3s 3g																				
2	Components procurement	02/05/2014	10/07/2014	10s																				
3	PCB production enginering study	29/05/2014	13/06/2014	2s 2g				_																
4	PCB production (N.70)	16/06/2014	03/07/2014	2s 4g				_			_		]											
5	PCB assemly	21/07/2014	29/08/2014	6s								_					<b>y</b>							
6	TELDES delivery	01/09/2014	05/09/2014	1s																				
7	TELDES test @ Perugia	08/09/2014	09/10/2014	4s 4g																		<b>y</b>		
8	TELDES delivery @ CERN	22/09/2014	23/10/2014	4s 4g															-					<u>'</u>



## **Production TEST Setup**



**ALTERA DE2-115 Used to handle PRBS** 

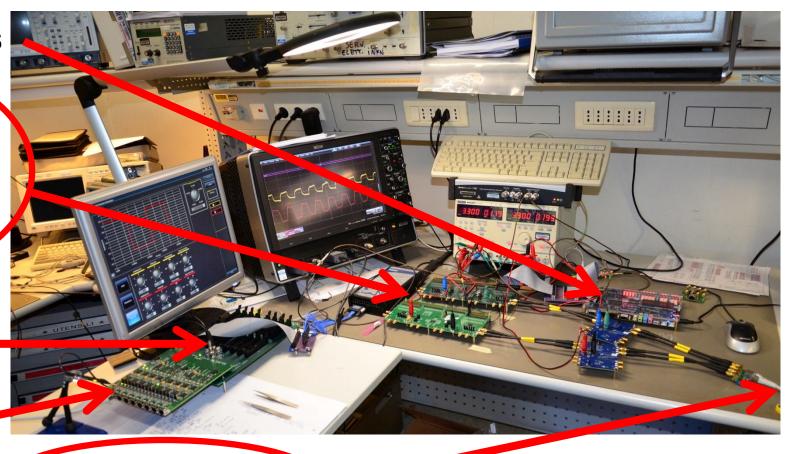
<u>CREAM</u>

double channel emulator

(serializer + BA)

TELDES TEST CARD. (TEL62 partial emulator)

**TELDES** 



Ethernet cable, CAT 6A



# Teldes production status



An equal test set up is foreseen to be installed at CERN. We basically have all the materials. We just need a work space of about 2m<sup>2</sup> outside the radiation controlled area. (i.e. electronic laboratory)



# Itertel preproduction status



#### InterTEL:

> Is a board which Interconnet deserialized data between 2

## **TEL**62;

- ➤ Only RICH & LAV will use this card;
- ➤ We are going to have 2 slightly different versions for the 2 sub systems;
- > A preproduction run of 2 boards per sub detectors is on going;
- ➤ While a final production run of 14 boards (10 LAV + 2 RICH + 1 RICH and 1 LAV as spare) will start after pre production tests;
- ➤ This means a very final production of 18 boards ready for test in November and delivered to the collaboration hopefully by December.



# Itertel preproduction status





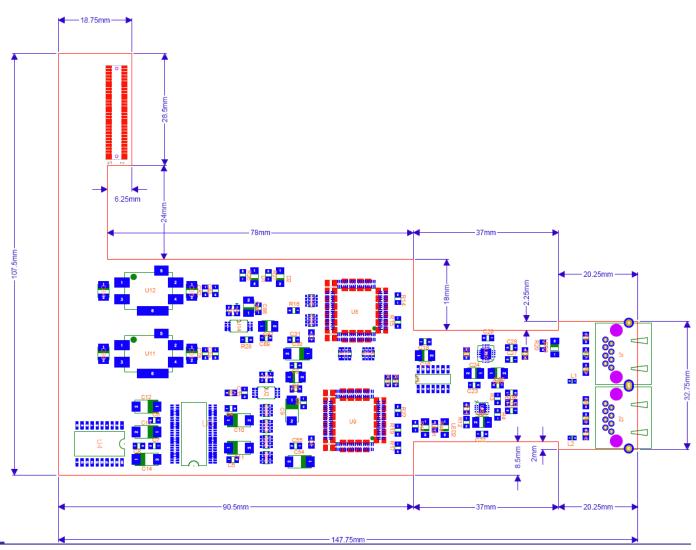


# Itertel preproduction status



Components view and mechanical layout

**Board 147x107** mm





## Conclusions 1 Teldes



- So far all the project planning actions have been respected;
- It is wise to implement at CERN a test set up equal to the one used for test in Perugia;
- The final production phase should be completed by the end of October.



## Conclusions 2 InterTEL



- So far all the project planning actions have been respected;
- The InterTEL Pre production run has been just launched;
- According to the schedule the first 4 boards have to be tested in Perugia after the TELDES (start in November);
- Report on them will be done during the next meeting in October.