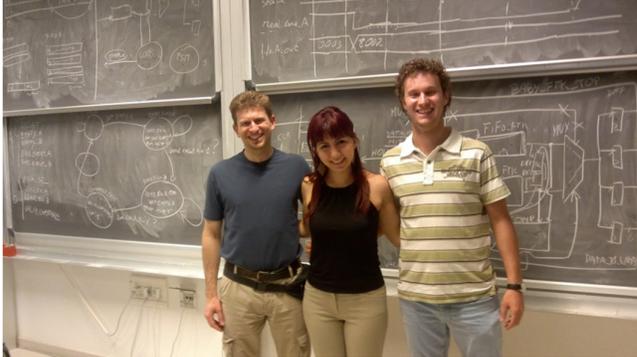


Summer School in Pisa July 1-4 2013

We have organized in July 1-4, a IAPP summer school in Pisa whose title was "VHDL design". Lessons were provided by the teacher Calliope-Louisa Sotiropoulou (seconded to CAEN). Exercises related to the argument of the lesson were executed immediately after with the help of the teacher assistants Marco Piendibene and Daniel Magalotti.



The teacher Calliope-Louisa Sotiropoulou and the teacher assistants Marco Piendibene and Daniel Magalotti.

The goal was to learn how to complete by steps a simple project (see figure 1) that required simple object VHDL descriptions (like a register, a MUX, a counter and a Finite State Machine), the addition of Xilinx modules, like FIFOs and RAMs and to simulate them by steps first, and finally all together. Each day a piece of the logic was added.

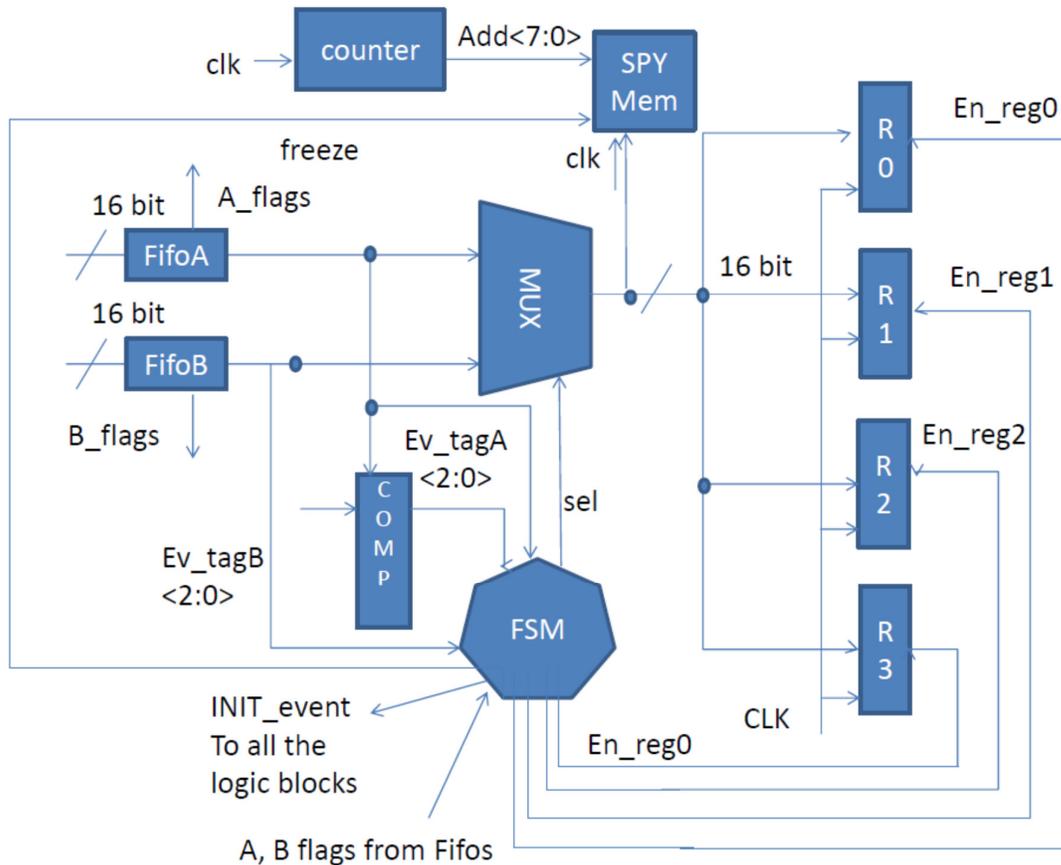


Figure 1: the logic built by pieces during the week.



Each student had a CAD station to be able to write his code and implement and simulate it with the Xilinx CAD.

Here is the school program: <https://agenda.infn.it/conferenceDisplay.py?confid=6549>
 Each day we had a session during the morning and a session during the afternoon.



The lessons were at the opening of each session, followed by the relative exercises.

What we indicate as "Exercise", it means that it was executed on the Xilinx (ISE) CAD by each one of the students independently on his own CAD station).

The last day we had a Lab session. There we had only a test stand, so it was more a demo than "exercises". Demo were executed using the Xilinx chipscope tool on FPGAs of the AMBFTK board and using a board provided by CAEN : <http://www.caen.it/csite/CaenProd.jsp?parent=1&idmod=704>

The students were 10 persons:



- (1) Kostas Kordas seconded from AUTH to CAEN;
- (2) Dimitrios Dimas seconded from Priele to Pisa;
- (3) Guido Volpi (CERN): interested to FTK recruitments;
- (4) Seyedruhollah Shojaii, PHD student from Milan;
- (5) 2 University students from Milan and two from Pisa;
- (6) Takashi Kubota from University of Melbourne, a Japanese FTK collaborator;
- (7) Mariano Falcitelli, a CNR researcher in Pisa.

CAD station were made available also on Friday 5 for people that wanted to continue to exercise.