

SuperB @ Frascati June 2012 - SVT parallel sessions			
title	presenter	time	
Friday June 1st 16-17:30			
Introduction & status of TDR	G. Rizzo (PI)	15	5
Update on activities in Strasbourg & tracking in high occupancy	I.Ripp-Baudot (IN2P3)	15	5
Update on background simulation	R. Cenci (Maryland)	20	5
Fastsim performance studies	N. Neri (MI)	20	5
subtotal		70	20
Friday June 1st 18-19:30			
INMAPS chip tests	S. Bettarini (PI)	20	5
Update on FE chip performance inner layers & INMAPS chip tests	PV	20	5
Update on FE chip performance outer layers	L. Bombelli (MI)	15	5
Update on sensor and fanout design in Trieste	TS	15	5
subtotal		70	20
Saturday June 2nd 8:30-10:30			
Update on HDI design and peripheral electronics	M. Citterio (MI)	20	5
Update on activities in Bologna	M. Villa (BO)	20	5
Update on activities in UK	A.Bevan (QMUL)	15	5
SVT Mechanics	F. Bosi (PI)	15	5
Update on SVT Mechanics in Milano	S. Coelli (MI)	15	5
subtotal		85	25
total		225	65