



Ongoing activities in WP2 at INFN-MIB

Alessandro Cattaneo

alessandro.cattaneo@mib.infn.it

Outline

□Neutrino simulations

☐Shared SQUID device

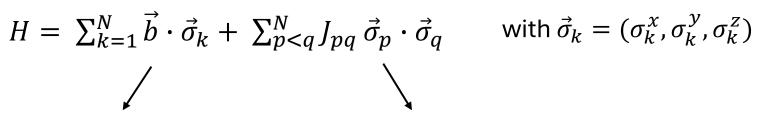
Outline

□Neutrino simulations ←←←

☐ Shared SQUID device

Ideas for QUART&T device

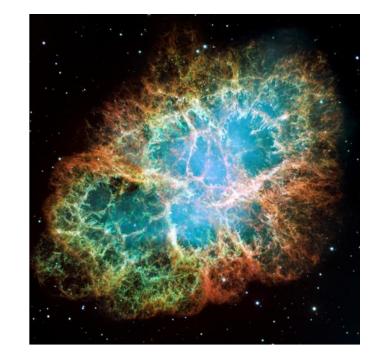
- **High density neutrino environments** where neutrino flavor oscillations are affected by neutrino neutrino quantum interaction.
- The Hamiltonian for a **two flavors oscillation** can be written as:



- 1. Vacuum mixing
- 2. MSW effect

3. Neutrino-neutrino interaction

Quantum architecture to simulate this specific problem.



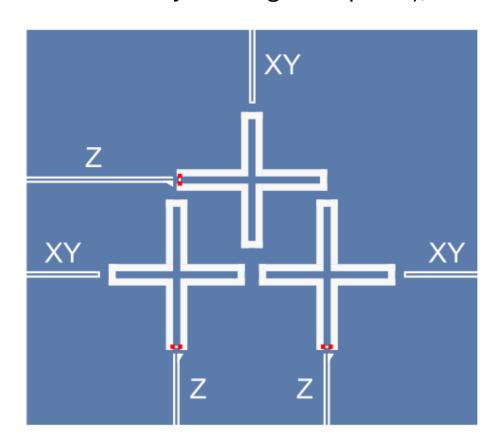
The Crab Nebula, a remnant of a supernovae.

[Phys.Rev.D 104 (2021) 6, 063009]

Analog device

Key idea:

1. Realize an **analog block** which realize an all-to-all connectivity using YY (capacitive) interaction (we are currently working on 3 qubits);



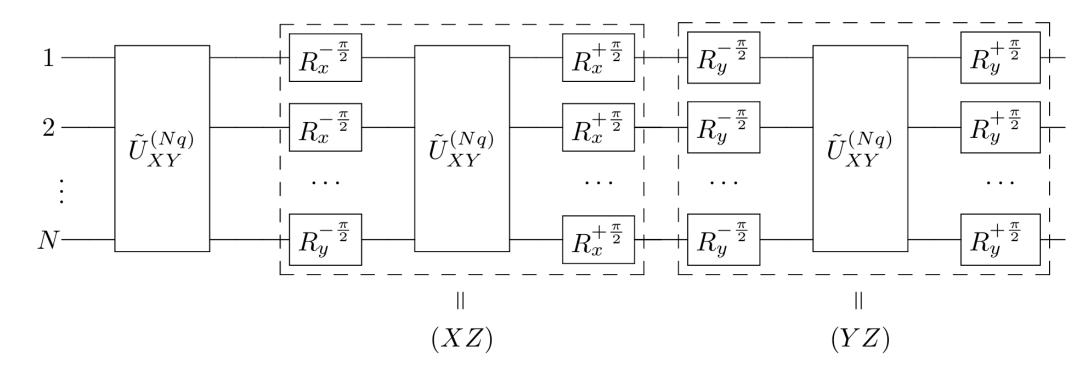
$$H^{(Nq)} = -\sum_{k=1}^{N} \frac{\omega_k}{2} \sigma_z^{(k)} + \sum_{p < q} g_{pq} \sigma_y^{(p)} \sigma_y^{(q)}$$

Hamiltonian of N qubits capacitively coupled

Analog device

Key idea:

2. Use a sequence of single qubit gate and analog block to simulate the neutrinos hamiltonian.



- 3 analog blocks separated by $4 \cdot N$ single qubit gates;
- Each analog block has the same time length which correspond to the desired simulation time;

Digital Vs Analog

Simulating quantum objects has two important limitations:

- **Trotter error** requires splitting the simulation into many small time steps, setting a lower bound on the number of steps needed for a given interaction time.
- **Gate fidelity** limits the number of gates, imposing an upper bound on the steps possible for a given interaction time.

Digital

High number of gates per simulation step



High fidelity 2 qubit gates (99.9%)



Analog

 Hardware optimization: low number of gates per simulation step



Uncertain analog block fidelity



 Reduced trotter error due to less noncommuting gates



Analog device – simulation results

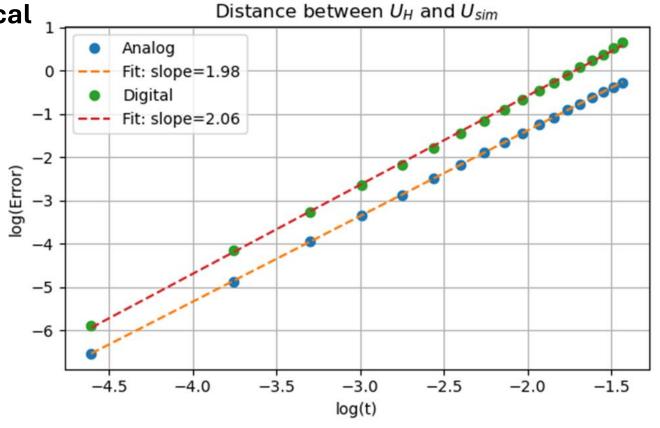
3 qubit-device **qutip simulations** and **analytical analysis** to quantify the analog advantage.



This will determine the analog block fidelity required to overcome a digital quantum computer.

$$\|\mathscr{A}(t)\| = \mathcal{O}(\widetilde{\alpha}_{comm}t^{p+1})$$

Theoretically additive error (trotter error). Quantifying $\tilde{\alpha}_{comm}$ is fundamental to quantify the analog advantage.



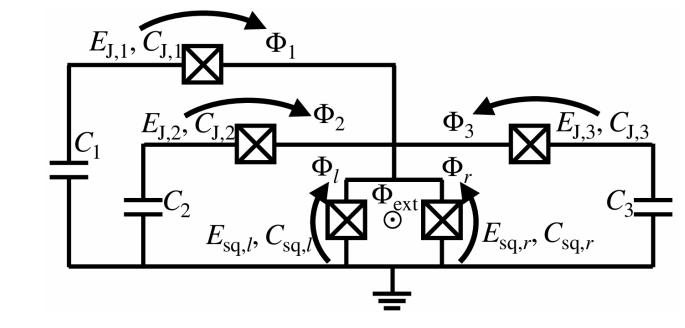
Outline

■ Neutrino simulations

□Shared SQUID device ←←←

Device schematic

- 3 transmon qubits device;
- Each qubit superconducting current pass through the SQUID realizing the coupling – current coupling;
- Coupling enabled by AC flux signals sended to the SQUID – parametric coupling;



Device analytics

A change of variables allow us to obtain the simplest Hamiltonian. The coupling term is all
contained in the charging energy.

The off-diagonal terms are large \rightarrow strong coupling

 The eigenstates of the systems are delocalized between the transmon islands due to strong coupling.

$$<\Psi_{uncoupled}|\Psi_{coupled}>\sim 0.6$$

Parametric coupling

normal modes

Turned on by driving the SQUID at a specific frequency

https://doi.org/10.1103/6prx-zmdz

Three-body interaction

$$\widehat{H}_{int} pprox -J(A_p)\widehat{a}_1^{\dagger}\widehat{a}_2^{\dagger}\widehat{a}_3$$
 + h.c.

Two-body interaction

$$\widehat{H}_{int} pprox -J(A_p)\widehat{a}_1^{\dagger}\widehat{a}_2$$
 + h.c.

SQUID asymmetric junction needed to turn on three-body interaction

Parametric coupling

Interaction strength:

Two-body

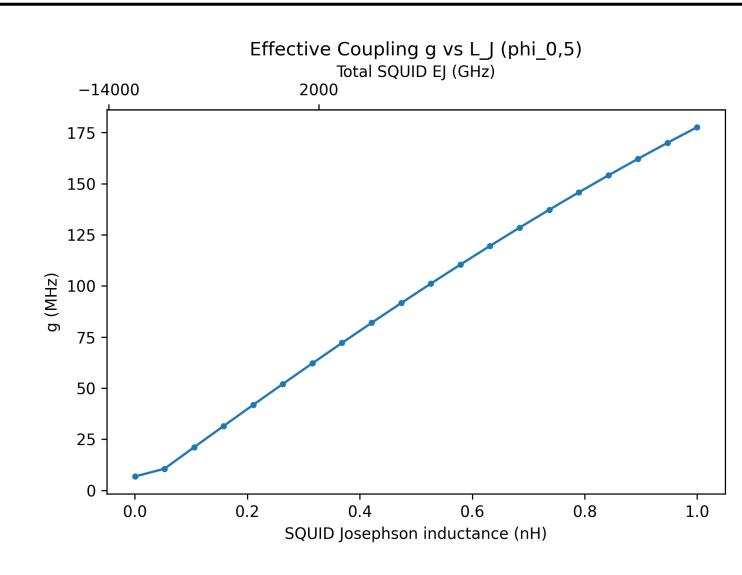
$$g = \frac{E_J}{2} sin\left(\frac{\pi}{\Phi_0} \Phi_b\right) \beta_{sq,1} \beta_{sq,2} \tilde{A}_p$$

$$g \sim 75 MHz$$

Three-body

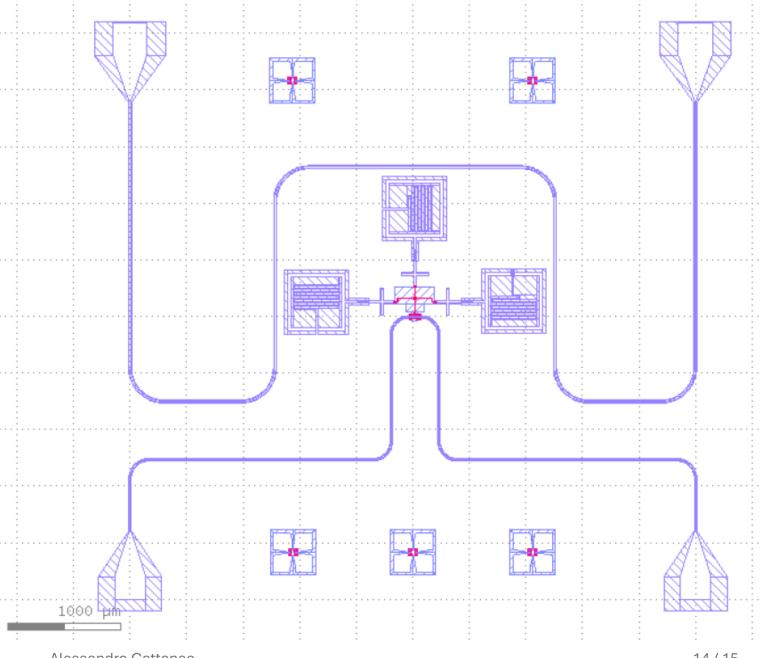
$$g = \frac{\delta E_J}{2\sqrt{2}} \cos\left(\frac{\pi}{\Phi_0} \Phi_b\right) \beta_{sq,1} \beta_{sq,2} \beta_{sq,3} \tilde{A}_p$$

$$g \sim 15 \, MHz$$



Device design

- 2 lines:
 - Feedline
 - Flux bias
- 3 readout resonators capacitively coupled;
- Metal island to connect the qubits JJs;
- Device fabricated @ Institute for Quantum Computing, Waterloo CA



Use of the device

- Analog block easily implementable applying a flux pulse made of 2 signal frequencies;
- Three body interaction achievable;
- Optimized GHZ state preparation through the three body interaction;