

From: Markus Friedl markus.friedl@oeaw.ac.at

Subject: VTRx+stacking height

Date: 13 October 2025 at 14:57

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MF

Dear all,

following up on the discussion we had at KEK, I found the following drawing in the "VTRx+ Application Note":

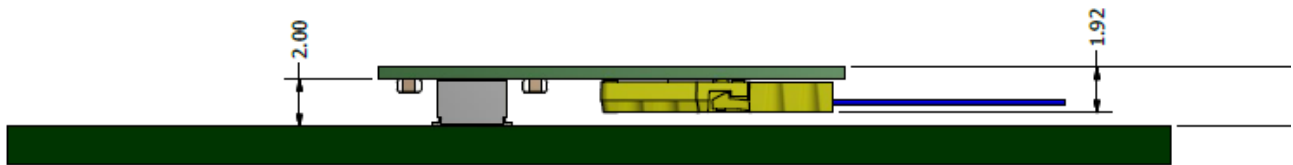


Figure 6: VTRx⁺ module plugged onto its host board.

The same document says that the host board connector is Hirose DF40C-(2.0)-40DS-0.4V.

The related datasheet https://www.hirose.com/en/product/document?clcode=&productname=&series=DF40&documenttype=Catalog&lang=en&documentid=en_DF40_CAT

already shows that there are different host connector heights available, and apparently the 3D CAD model had the wrong one.

(In Vienna, we have also equipped a higher one (4.0mm stacking height) onto our test board as well as the Dual-IpGBT board, because that was written in the CERN BOM for the test board.)

Even though in principle we can use any height, I think it is smarter to stick to the compact 2mm version. In the part number, the stacking height is the one in brackets.

Those connectors (2.0mm stacking height) are in stock at both Mouser and Digikey.

Cheers, Markus

On 13.10.2025 10:34, Markus Friedl wrote:

Dear all,

it turned out that the best 2 days are

Tue+Wed 11+12 November 2025

The meeting will take place in the (old) MBI location (formerly HEPHY) at Nikolsdorfer Gasse 18, 1050 Vienna, Austria. Google Maps: <https://maps.app.goo.gl/g58SJa449Cxe3Sa9>

Booking.com shows several affordable rooms in the vicinity of the institute. The "traditional" place would be Hotel Attache in Wiedner Hauptstraße 71, 1040 Vienna, Austria (~82€/night); but there are also several apartment options starting at just 28€/night (bedroom in a shared apartment).

Looking forward to seeing you in Vienna!

Cheers, Markus

On 29.09.2025 09:28, Markus Friedl wrote:

Dear all,

there is the idea of having a 2-day technical meeting in Vienna in November with mechanical and electrical engineers. Discussion items will be ladder design, support structure and electrical issues (mainly around IpGBT/VTRx+). The target is to prepare something reasonable for presentation in the December meeting in Pisa.

I prepared a Doodle poll to find the proper date. Please note that each option means the first day, so the meeting will take place on the day written + the next one.

<https://nuudel.digitalcourage.de/vEmF2ZlAdFsjpU5N>

Please fill in the table until 12 October.

Cheers, Markus

