

Research and Technological Development		k€	overheads	
WP2	AMchip05 MPW submission (10x5 mm ²) 32 kpatterns	250		IMEC
	manpower AMchip05 design - test vectors production-tests	130		35 LNF, 30 Milan, 25 Pisa, 40 LPNHE
	feasibility study 1 month	15		
	Multi Packaging cost 2D	80		IMEC included manpower
	test vector conversion - test of 500 (1000) chips @Microtest	130		Microtest included manpower
	500 (1000) chips production for 10 (20) EVBs	30	[60]	IMEC
WP3	LAMB Development & small production (PCB & chip procurement)	50		Geneva included manpower
	Single chip mezzanine for Amchip tests -EVB's firmware	50		INFN Milan-LNF
	Test setup for LAMB- firmware development	60		INFN Pavia
	LAMB assembly and quality tests	50		Prisma included manpower
	Acquisition of Xilinx evaluation board for users-basic firmware	80		Tessaloniki-Prisma included manpower
WP4	movement reconstruction	80	128	Florence
	CMS tracking ?	120		Florence-INFN Pisa
	Atlas tracking	80		UCL
	Tessaloniki?	80		help Florence - Imaging
	Prisma?	80		help Florence - tracking
	CAEN	80		security CAEN
	University in Portugal	80		security CAEN-associated
WP5	Multi-packaging 2.5D	100		IMEC? included manpower & FPGA cost
	3 D activity?	80		Heidelberg manpower
	AMchip06 design 64 or 128 kpatterns	100		30 LNF, 20 Milan, 20 Pisa, 30 LPNHE
Super TOT (without overheads, management, dissemination)		1805		3249 80% added