

WP	EU fund	TASK	Parteners involved in the task	WP leader
WP1	MGT 100%	Project management and coordination		
1.1	MGT	Project Coordination	INFN	INFN
1.2	MGT	Project Management	INFN	
WP2	RTD 75%	ASICs Development		
2.1	RTD	Full custom design and floorplanning of 2D AM and simulation	INFN	LPNHE
2.2	RTD	Standard cell design, placement & routing of 2D AM and simulation	LPNHE	
2.3	RTD	Test of prototypes	LPNHE -INFN	
2.4	RTD	2D Multipackaging: 2 Amchips on the same plane	IMEC	
2.4	RTD	"Translation of test vectors & tests of Amchip for the demonstrator	Microtest	
WP3	RTD 75%	Board Development		
3.1	RTD	Firmware and software development for the embedded system	PRISMA-AUTH	AUTH
3.2	RTD	Acquisition and commissioning of embedded systems for demonstration WP	PRISMA-AUTH	
3.3	RTD	LAMB design and development	UGENEVA	
3.4	RTD	Mezzanines assembly and first quality test of boards	PRISMA	
3.5	RTD	Single AMchip mezzanine for AMchip tests design and test vector design	INFN	
3.6	RTD	Firmware for array of Amchip tests and LAMB tests	INFN	
3.7	rtd	Board for tests at Microtest machine	Microtest	
WP 4	DEM 50%	Test Bench Applications Demonstrations		
4.1	DEM	CMS simulation	UNIFI - INFN -PRISMA	UNIFI
4.2	DEM	Atlas simulation	UCL - AUTH-PRISMA	
4.3	DEM	Movie reconstruction	UNIFI-AUTH	
4.4	DEM	Tomography	CAEN-IPFN	
WP5	RTD 75%	Design for further integrated system		
5.1	RTD	Concept design for a 3D AMchip	UHEID	UHEID
5.2	RTD	2.5D Multipackaging: 2 AMchips one over the other	IMEC	
5.3	RTD	Amchip06 design 128 Kpatterns	INFN-LPNHE	
5.4	RTD			
WP6	MGT 100%	Dissemination and exploitation		
6.1	MGT	Dissemination	INFN	PRISMA
6.2	MGT	Exploitation	PRISMA	
6.3	MGT	Project Exit Plan	Microtest	