Year	k€	Quarter		WP 0 AMCH	IIP - costs	s in column G		WP 1 Boards -	costs in	K€
2011	145	1st Q 2nd Q 3rd Q 4th Q	design Am4 design Am4 design Am4 design Am4-submission	Annovi, Crescioli, Stabile, Beretta			70 k\$	design AMBFTK/LAMB design AMBFTK/LAMB design AMBFTK/LAMB design AMBFTK/LAMB		
2012	40		miniasic/serdesIP/Am5 miniasic/serdesIP/Am5	Beretta, Stabile, Crescioli	test stand setup test stand setup test AMchip4 test AMchip4	Beretta, Annovi,Stabile Beretta, Annovi, Becherle	110 k\$	design AMBFTK/LAMB design AMBFTK/LAMB test AMBFTK/LAMB test AMBFTK/LAMB		
2013	260	2nd Q	sub. miniasic 20/3/13 design Am5 design Am5-submission	Stabile, Crescioli, Becherle	tests miniasic	BerettaStabileBecherle	200+5 keuro	designSLP2(AMB+LAMB) designSLP2(AMB+LAMB) test SLP2 test SLP2	50	
2014	260	1st Q 2nd Q 3rd Q 4th Q	design Am6 design Am6 design Am6 design Am6	Stabile, Crescioli	test AMchip5 1500 AM5 - 8 PUs test production commissioning	Becherle Beretta	93 keuro	prod. 10 SLP2-AMB prod. 40 SLP2-LAMB prod FTK_IM		32 135
2015	263	1st Q 2nd Q 3rd Q	Full mask submiss.		test AM6 2.7 kAM6 6 wafers-16 PL		244 k\$ + 256 k\$ 44 k\$	prod. 16 SLP2-AMB		50
2016	273	4th Q 1st Q 2nd Q 3rd Q 4th Q			prod 24 wafers 64 PU test production commissioning		<b>88 k\$ +</b> 88 k\$	prod. 64 SLP2-LAMB prod. 64 SLP2-AMB prod. 256 SLP2-LAMB		200
2017	350	1st Q 2nd Q 3rd Q 4th Q			production 64 PU test production commissioning	all all	180 k\$	prod. 64 SLP2-AMB prod. 256 SLP2-LAMB		200
2018		1st Q 2nd Q 3rd Q 4th Q						•	DATA TAKING -	MAINTANAMCE

tot 1602
TOT not-USA keuro
TOT not-USA keuro
TOT not-USA keuro
827
EXTRA FUNDS
992

100 keuros Germany for Amchip

50-100 keuros from Japan for Amchip SUPERTOT ITALIA 1609

University of Geneva: participation to VS, commissioning, simulation... can we add LAMB design and production ~200-300 keuro?

## Institutions that could sign the IMOU and bring EXTRA FUNDS

LPHNE: participation to Amchip design/test with Crescioli? Funds?

Tessaloniki: ?? How can Tessaloniki participate?

Academia Sinica: can help to get better quotations for Amchip prototypes and productions?

## Extra info:

FTK is selected for negotiation for the IAPP last call (FP7)-> good probability to get EU funds for manpower and collaboration with two CAEN and Prisma electronics. We plan to integrate FTK@CAEN

50

46

16

6,1

7,04

9,5 10

144,64

WP2 Integration	WP 3 commissioning	WP4 Simulation
	VS @ CERN Lab32	
	VS @ CERN Lab32	
	VS @ CERN Lab4	
	VS @ POINT 1	
AMBFTK @CAEN		
AMBFTK @CAEN	ANADETIC ODOINT 4	
AMBETK @CAEN	AMBETIC @POINT 1	
AMBFTK @CAEN	AMBFTK @POINT 1	
SLP2 @CAEN	commissioning Domo	
SLP2 @CAEN	commissioning Demo commissioning Demo	
SLP2 @CAEN SLP2 @CAEN	commissioning Demo	
JU Z & CALIN	commissioning Demo	
	Data taking with Demo	
	Butu tuking with Bemo	
	commissioning FTK	
	commissioning FTK	
	running FTk	
	running FTk	
	commissioning FTK	
	commissioning FTK	
	running FTk	
	running FTk	
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