## FCC-ee vertex detector R&D workshop Thursday, 30<sup>th</sup> October 2025, INFN Pisa





 Our team
 Current involvement
 FCC-ee vertex R&D
 Infrastructure

# FCC-ee vertex R&D team



Rainer Wallny Group Head





Malte Backhaus Senior scientist (tenured)



Thea Årrestad Senior scientist



**Branislav Ristic** Senior scientist



Matthew Franks
Postdoc Researcher



Paul Sander PhD Student (3<sup>rd</sup> year)



**Chin Zhe Tee** PhD Student (2<sup>nd</sup> year)



Theresa Heinz PhD Student (starts Dec 2025)



Lorenzo Asfour Master's Student

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## Current involvement



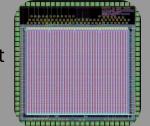
#### **CMS**

- ← Extensive involvement in Phase 0 and 1 upgrade
- ← Development of diamond sensors
- ↓ Phase 2 Inner Tracker Module R&D
  - Development of passive planar CMOS pixel sensors
  - HDI and cooling plate design, production, testing
  - Definition and implementation of module QA and QC software
- → Assembly of prototype modules and production of ~500 modules

#### DMAPS R&D

(In collaboration with PSI)

- ← MoTiC development &
- → PANTHER development
  - Chip design
  - DAQ development
  - Characterisation



- → (at ETH) OCTOPUS involvement
  - WP4: Characterisation & Testing (M. Franks co-convenor)



#### Mu3e

- ← Commissioning of HV-MAPS vertex detector
- ← SciFi R&D construction, commissioning
- ↓ Full detector DAQ and readout (SciFi + HV-MAPS pixels)
- → Online event reconstruction with GPUs

## Machine learning

- → Aim: address increased data rate problem future vertex detectors (layer 1)
- → Either: on-detector compression, frontend filtering
- → "Smart pixels"
  - In-pixel intelligence to reduce data
  - Extract particle features from single layer

...and much more! <u>ipa.phys.ethz.ch</u>



M. Franks et al.

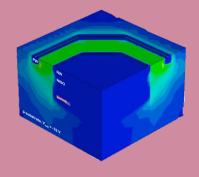
# FCC-ee vertex R&D: Monolithic Active Pixel Sensors (MAPS)

### Design

- Cadence virtuoso licenses available through ETH
- ETH maintains contacts to (most) MPW providers
- Access to many CMOS Process Design Kits (PDKs)
  - o TSMC 65 nm
  - o IHP 130 nm
  - GlobalFoundries (various)
  - TPSCo 65 nm (NDA signed)
  - 0 ...

#### **Simulation**

- Sentaurus Synopsys TCAD licenses available through ETH
  - Processsimulations
  - Structure simulations
  - Device simulations
- Large computing clusters available



#### Characterisation

- Wafer-level testing
- I-V measurements
- TCT (and e-TCT)
- Irradiation campaigns
- Telescope construction and test beam



### Integration

- Lightweight system mechanics (carbon fibre)
- Experience designing different system aspects e.g. powering
- Cooling systems (CO2, air, helium)
- Component production





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# Available infrastructure

#### Cleanroom

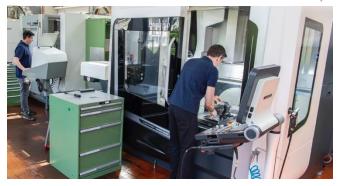
- 50 m<sup>2</sup>
- Up to ISO class 5
- Detector production
- Probe station
- Wirebonder (+ Pull tester)
- TCT setup (Particulars)

#### **Electronics**

- 3 electronics engineers for electronics design
  - Analog and digital electronics
  - FPGA & microcontroller firmware
  - Full readout systems

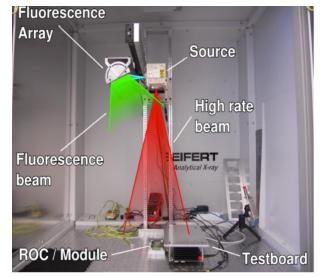
#### IPA Cleanroom







Wirebonder and probe station



M. Franks et al.

X-ray chamber at ETH

#### ETH Zurich workshop

#### **Mechanics**

- 3 mechanical engineers for mechanics design
- In-house
  - CNC/conventional milling, turning
  - Laser cutting, welding, engraving

#### Laboratories

- 6 MV accelerator
- X-ray chamber
- Radioactive sources (Am, Fe, Sr, ...)
- Climate chamber
- Time-Domain Reflectometer



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