







MPGD ENDCAP Trackers (ECT) for ePIC

GEM $-\mu$ RWELL technology

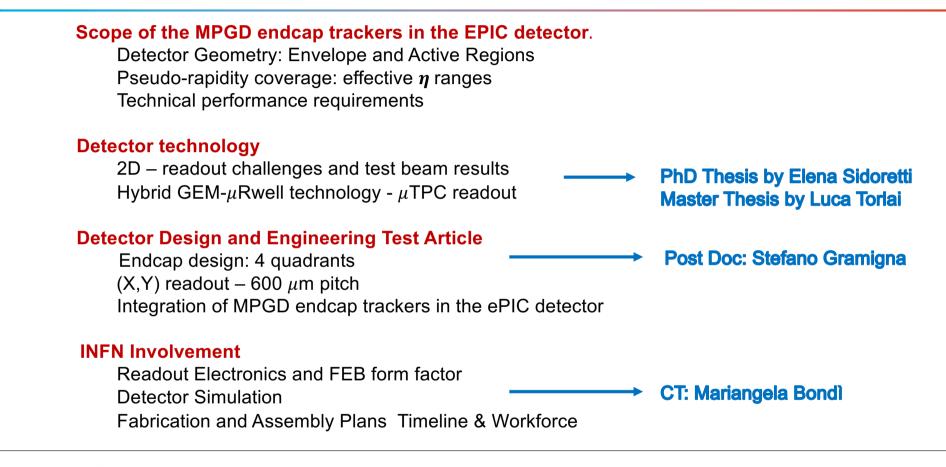
Annalisa D'Angelo – ePIC ECT project coordinator

On behalf of the **ECT project group**: C. Ammendola, R. Ammendola, M. Bondì, R. Di Salvo, A. Fantini, S. Gramigna, L. Lanza, G. Nobili, L. Torlai, E. Tusi In collaboration with: G. Bencivenni, M. Giovannetti, M. Poli Lener, G. Morello

INFN ePIC Referee Meeting Torino, July 35, 2025

Electron-Ion Collider

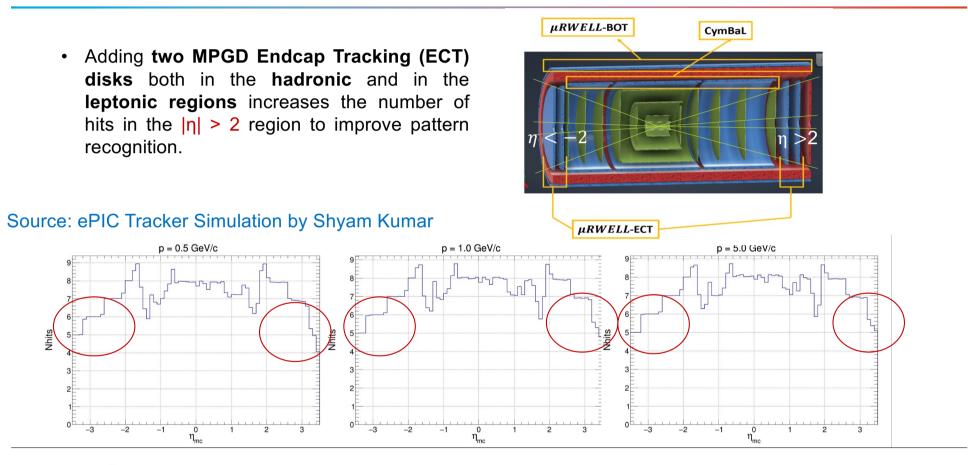
GEM-µRWELL ECT Project Update: OUTLINE



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MPGD-ECT: the scope of the endcaps in ePIC detector tracking

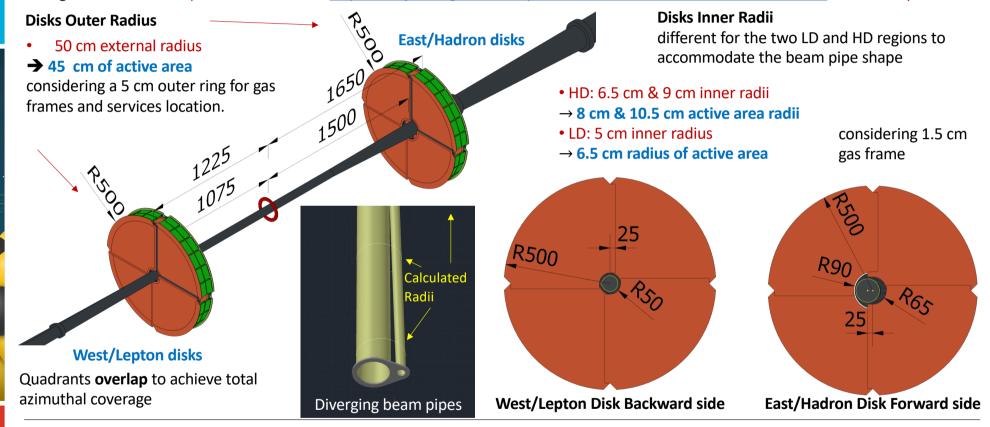


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The ePIC MPGD End Cap Tracker Envelope and Active Regions

The geometrical envelopes are available at: <u>https://eic.jlab.org/Geometry/Detector/Detector-20240117135224.html</u> \rightarrow will be updated

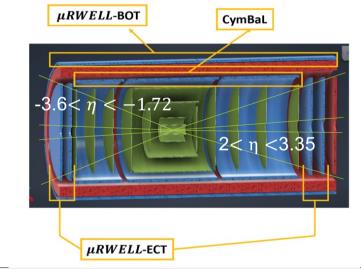


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Component	Z (cm)	Inner Active Reg. Radius (cm)	<i>θ</i> min (deg)	$ \eta $ max	Outer Active Reg. Radius (cm)	θ max	$ \eta $ min
HD MPGD 2	166	8.0/10.5	2.76/3.62	3.73/3.45	45	15.17	2.0
HD MPGD 1	150	8.0/10.5	3.05/4.00	3.62/ <mark>3.35</mark>	45	16.70	1.9
LD MPGD 1	-107	6.5	3.47	3.49	45	22.80	1.6
LD MPGD 2	-122	6.5	3.04	3.62	45	20.24	1.72

Pseudo-rapidity coverage: effective η ranges



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• The minimum $|\eta|$ value is not larger than 2 it is limited by the outer HD disk location/dimensions

• The maximum $|\eta|$ value is not less than 3.35 it is limited by the inner HD disk location/dimensions

The η range covered by the MPGD Endcap tracking disks is **compliant** with requirements.

MPGD-ECT Technical Performance Requirements

Time resolution ~10 ns or less to provide tracking timing

- Time resolution~ 10 \div 20 ns
- Sampling faster than 50 MHz

Low material budget

- <1 % X₀ - it will be the minimum compatible with the chosen technology

Spatial resolution: 150 μ m or better

- <150 μ m intrinsic spatial resolution for perpendicular tracks
- Technological optimizations to retain 150 μ m resolution for inclined/curved tracks $\rightarrow \mu$ TPC mode

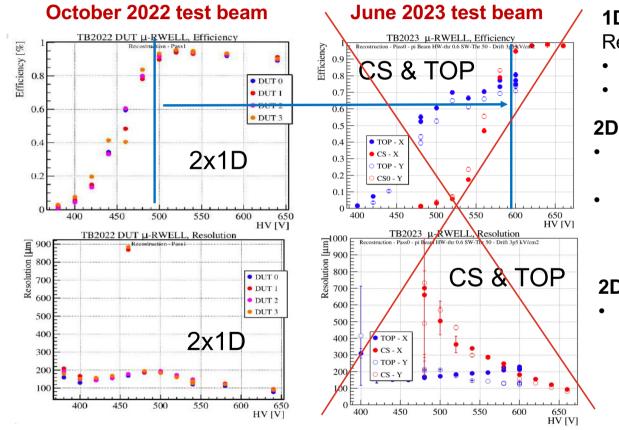
High Efficiency

- Single detector efficiency ~ 96 – 97 % \rightarrow 92 – 94 % combined efficiency for two disks

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MPGD Technology: µRWELL from 1D to 2 D



1D pitch 0.78 mm Reference performances:

- 96% efficiency
- 120 μm resolution

2D CS pitch 1.2mm

- Due to the charge spread the working point is shifted to high voltage/gain
- Spatial resolution improves at high gain reaching 150 μm with a strip pitch of 1.2 mm

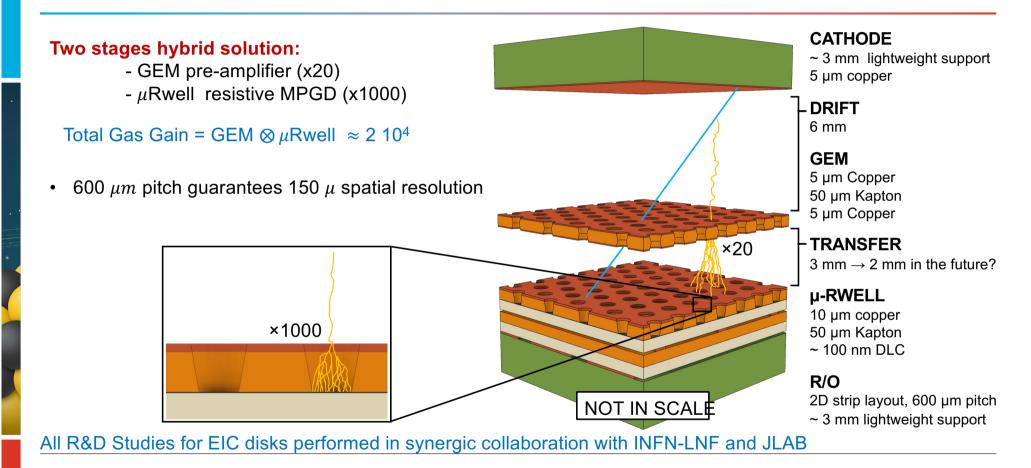
2D Top-r/out pitch 0.78 mm

low-voltage/gain operation but low efficiency level (80%) due to the geometrical dead zone on the segmented amplification stage

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New MPGD Technology: - GEM-µRWELL Hybrid Detector

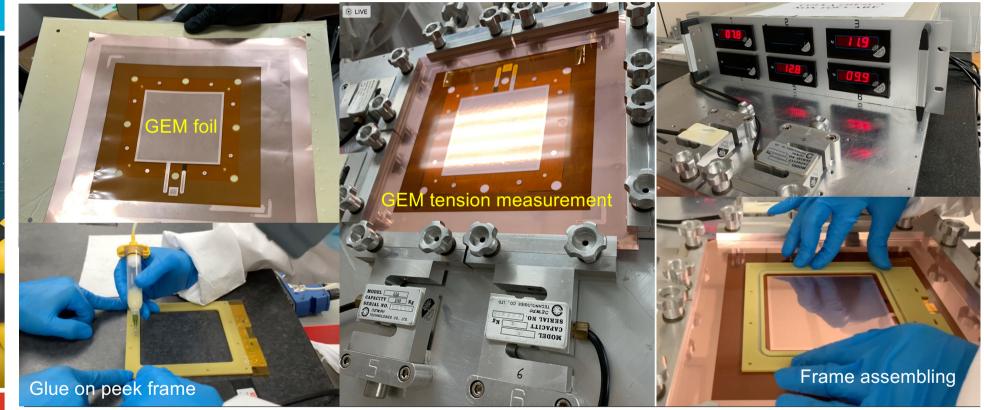


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New MPGD Technology: - GEM-µRWELL Hybrid Detector

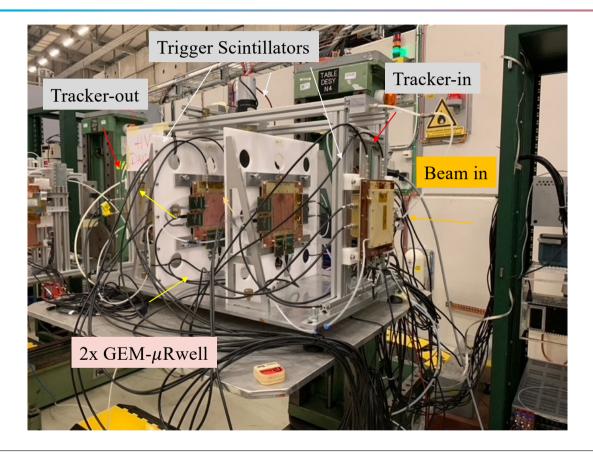
GEM 10x10 cm² layer assembly



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GEM-µRWELL Hybrid Technology 2024 Test Beam



All detectors :

2D COMPASS-like readout 400 μm pitch

Tracker-In : μ Rwell – 3 mm drift gap

Tracker-Out : GEM- μ Rwell 6 mm drift + 3 mm transfer gaps

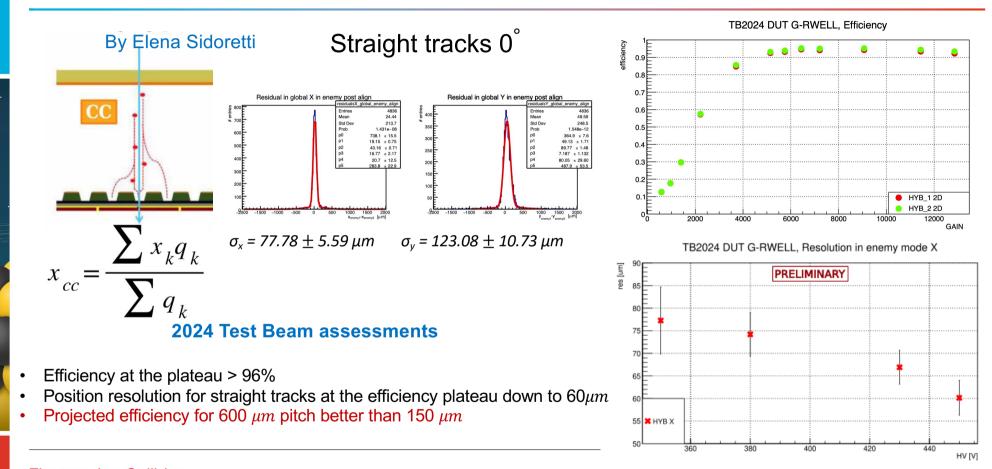
Detectors Under Study (DUT)

- 2 x GEM-µRwell prototypes
- 6 mm drift + 3 mm transfer gaps

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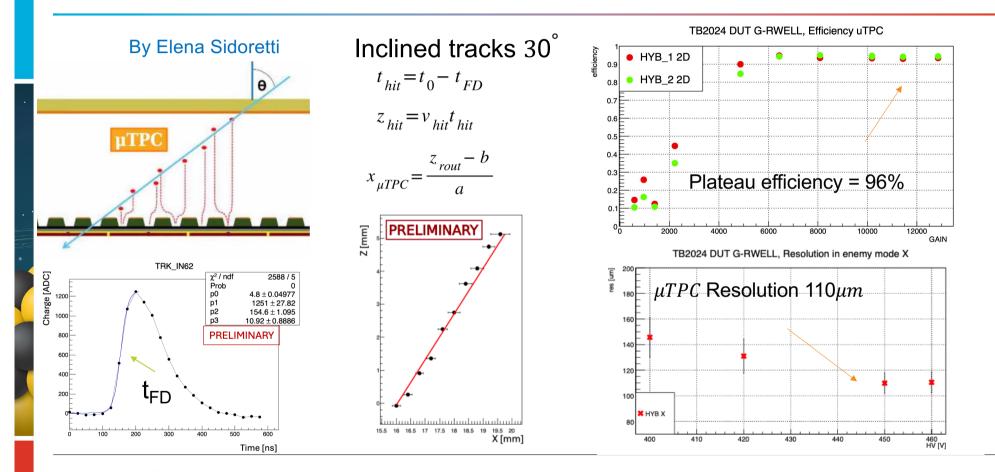
GEM-µRWELL-ECT: Hybrid Technology 2024 Test Beam



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GEM-µRWELL-ECT: Hybrid Technology 2024 Test Beam

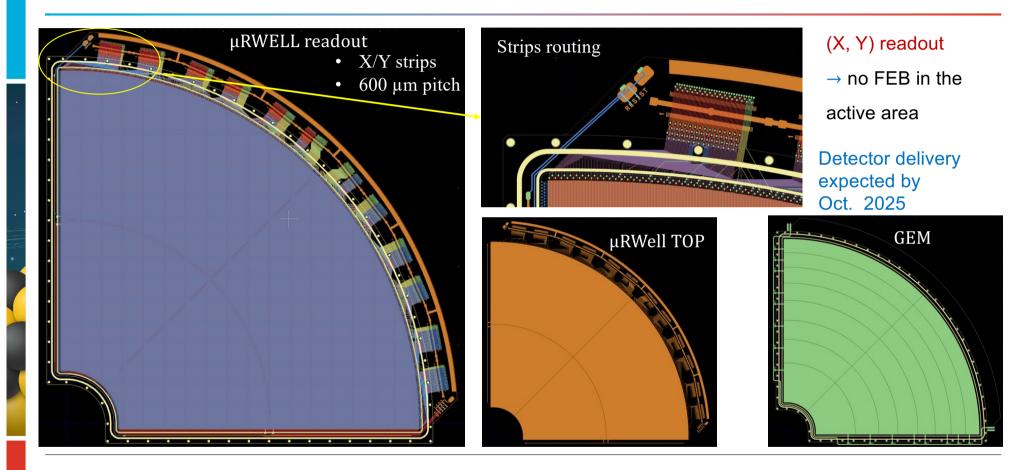


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MPGD-ECT: PED Test Article Module

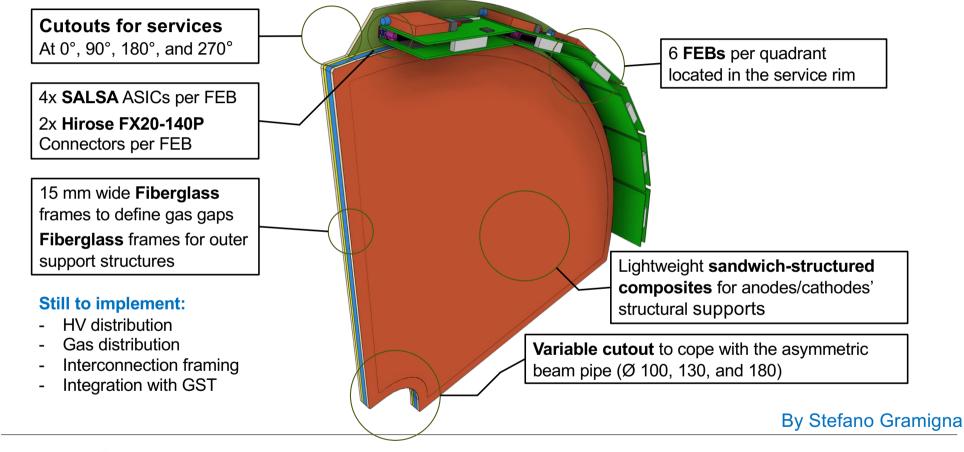
By Stefano Gramigna



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MPGD-ECT: PED Test Article Module

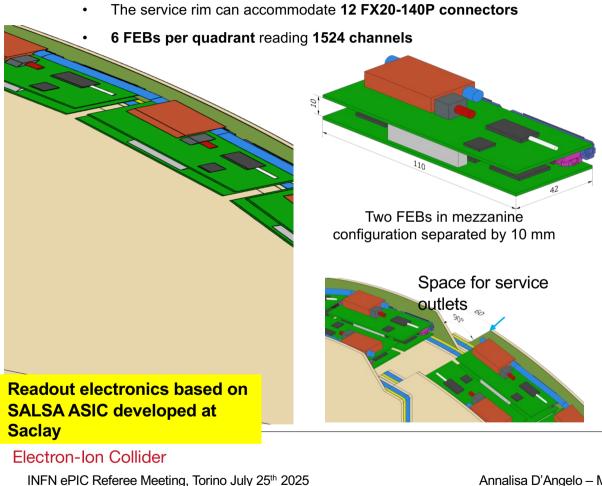
Refer to Stefano's presentation for more details



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GEM-µRWELL-ECT: Front End Boards (FEB) & Services



All the service requirements have been communicated to the Integration group

For each endcap disk (4 disks in total):

- 16 HV cables
- 4 gas inlets and 4 gas outlets •
- 24 data cables
- 24 low voltage cables
- 2 temperature sensors cables •
- 2 humidity sensors cables •
- 2 inlet and 2 outlet cooling hoses • (H₂O) 210 W cooling dissipation

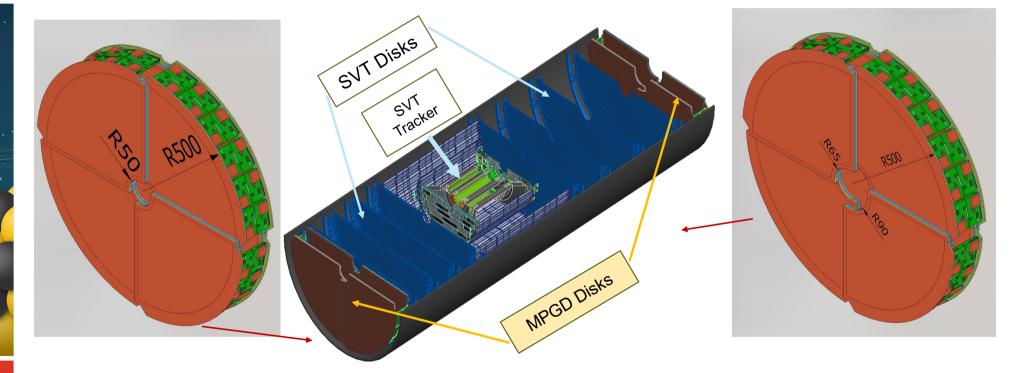
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GEM-µRWELL-ECT: Integration in ePIC Detector

The assigned envelope will include the detectors and the FEB electronics.

The disks will be attached to the support frame under design.

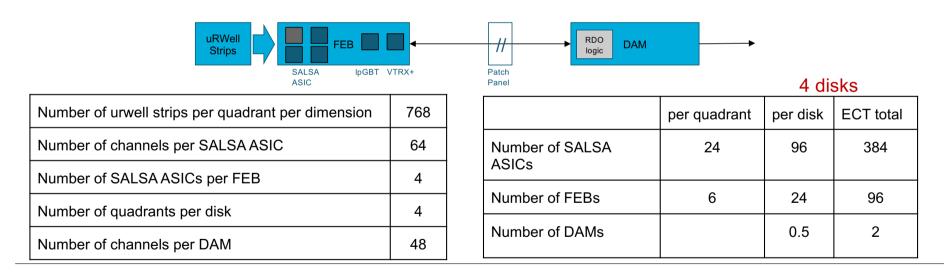


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GEM-µRWELL-ECT: DAQ Scheme and Figures

- Readout based on SALSA ASIC developed at Saclay (CEA Irfu)
- Front End boards will host 4 64 channel SALSA chips
- FE Boards modular development in common with other MPGD detectors
- Each detector will work in a specific FEB form factor
- Actual DAQ scheme foresees IpGBT and VTRX+ on FEB and no RDO stage



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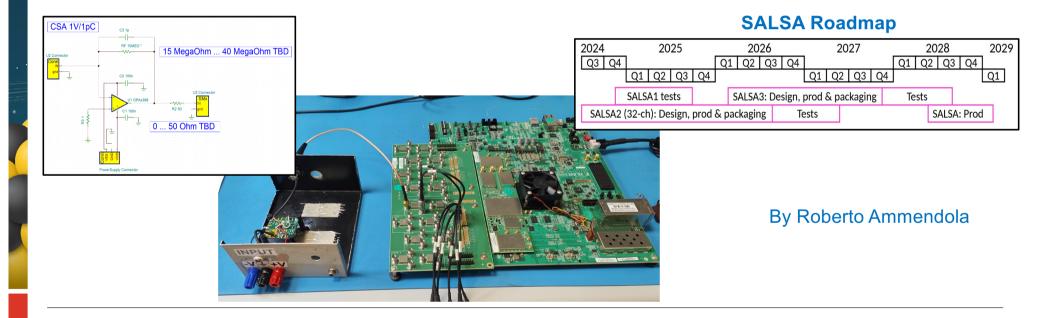
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By Roberto Ammendola

GEM-µRWELL-ECT: Front End and Readout Chain Emulation

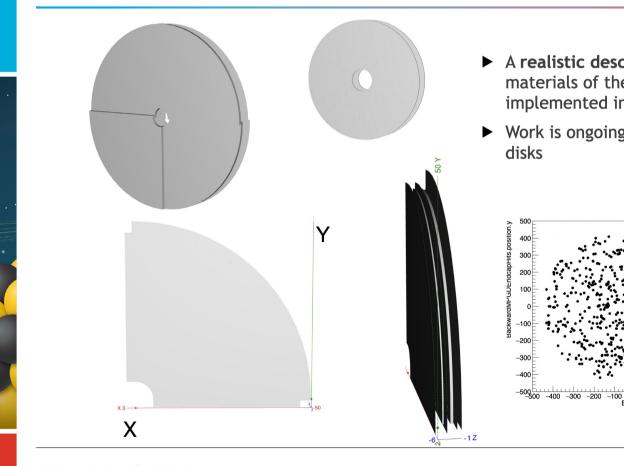
- SALSA chip could be available in 2027 for first integration in Front-End boards
- In the meanwhile, we are preparing a testbed to exercise the complete readout chain interfacing with real detector
- Using development board with multi-channel, high sampling rate integrated ADCs FPGAs (AMD Xilinx ZCU216)
- Developing custom charge amplifier (CSA) mimicking SALSA analog stage
- Adding more SALSA features in firmware and coupling with VLDB+ board to implement IpGBT + VTRX+ layer



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GEM-µRWELL-ECT: Detector Simulation



A realistic description of the geometry and materials of the MPGD disks has been implemented in the detector simulation Work is ongoing to enable ACTS tracking with the disks

300 400 500

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100 200

Fabrication / Assembly Plans & Timeline / Workforce

MPGD ECT Timeline			neline	CD-3 Q1/2028 CD-3 Q1/2028 CD-3 Q1/2028 CD-3 Q1/2028 CD-3 Q1/2029 Q1/2031 CD-3 Q1/2029 Q1/2031 CD-3 Q1/2032 CD-3 Q1/2032 CD-3 Q1/2032 CD-3 Q1/2032 CD-3 CD-3 CD-3 CD-3 CD-3 CD-3 CD-3 CD-3
YEAR	INFN R&D K Euro	INFN IN-KIND K Euro	DESCRIPTION	Construction IR-6 ready
2025	27	15	Pre-Production	for installation ↓ ↓ Ready for with Commissioning Beam done in beam position done in beam position beam position beam position beam beam beam beam beam beam beam beam
2026	30	40	Pre-Production	done in IP-6 Assembly Hall installed Solenoid Detectors installed delivered Barrel Ecal dRICH
2027	-	100	Production	ready <u>ECal</u> for installation
2028	-	100	Production	Barrel <u>Hcal</u> Barrel <u>D</u> etectors & Solenoid installed installed installed ECal DIRC+URWell EDUCL
2029	-	100	Production	installed <u>ECal, DIRC+µRWell</u> , <u>bRICH</u> followed by low <u>ToF+MPDG</u> <u>ECal</u> power test <u>Mar o-barrer bicks</u> MPDG-Disks
2030	-	55	Electronics, QA, Commissioning	Workforce
2031		50	Electronics, QA, Commissioning	INFN Groups: Roma Tor Vergata Catania
2032		40	Installation	• LNF
тот	57	500		 Genova JLab Temple University
				Seul University

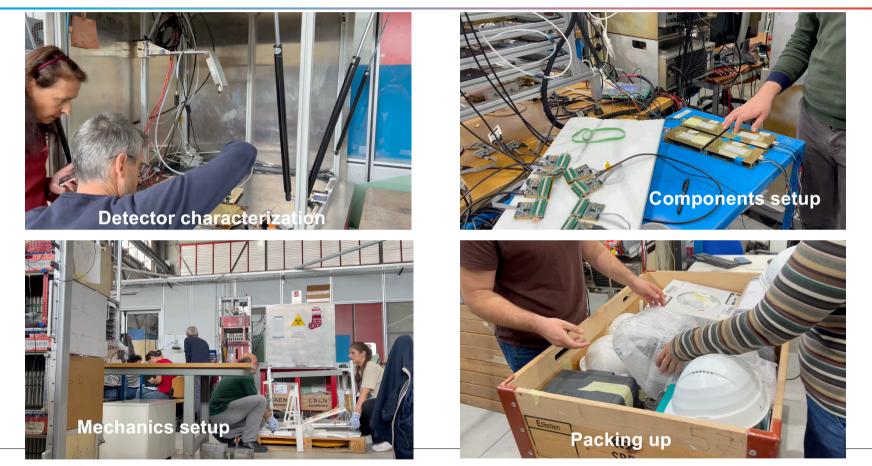
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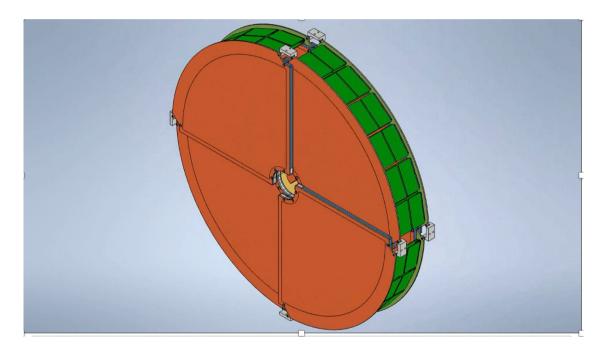
Workforce: Setting up the Test Beam @LNF



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2025 On-Going Activities:

Integration Studies



- GEM Stretcher realization
- MPGD-ECT Integration Studies
- First Engineering Test Articles (ETA) design and procurement
- Test Beam at CERN: November 2025 to characterize the ETA



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2026 Activity Plans

Milestones 2025

30/06/2025 - Studio ricostruzione tracce " μ TPC" nei prototipi GEM-uRwell (MPGD-ECT) Stato di completamento: **100%**

31/12/2025 - Disegno e ordine di produzione del "First Engineering Test Article" Ordine sottomesso. Stato di completamento: **75%**

NEXT STEPS:

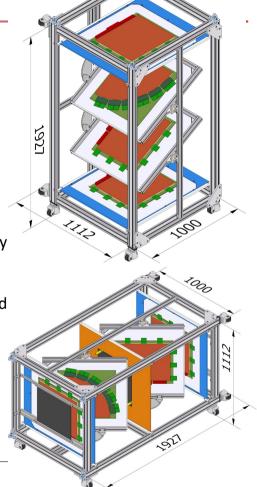
- November 2025 Test-Beam data analysis
- Design and procurement of Light-Weight final version of ECT disk (II Engineering Test Article)
- Completion of the MPGD laboratory at Roma Tor Vergata, by procuring a second CAEN HV supply module
- Emulation of the the GEM- μ Rwell charge signals acquisition using SALSA chips.
- Procurement and assembly of a detector mechanical support for large area detectors to be used for both cosmic-rays and test beam (compatible with Goliath at CERN.

Proposed Milestones 2026:

31/12/2026 – Caratterizzazione del primo set dei "First Engineering Test Article"
 31/12/2026 – Disegno e ordine di produzione di un secondo set di "Engineering Test Article" su supporti a bassa massa ed ottimizzati per l'integrazione nel rivelatore ePIC.

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2026 Financial Requests to INFN

Richieste Roma Tor Vergata						
Item	Costo Totale IVA inclusa k€					
2 set di Engineering test articles light weight a forma di quadrante ottimizzati per la loro integrazione in ePIC → IN-KIND	40					
Supporto meccanico per rivelatori a grande area per test con raggi cosmici e test beam $\rightarrow R\&D$	7					
2 moduli CAEN A1561HDN - SYx527 H.V. channels -6 KV 20 uA SHV (12ch) - 50pA res. → R&D	13.5					
Gas box per Quality Assurance $GEM \rightarrow R\&D$	3.5					
KIT di elettronica di backend basato su VLDB+ per acquisizione dei segnali delle MPGD in emulazione del chip SALSA e della DAQ prevista in ePIC \rightarrow R&D	4.5					
Sostituzione Filtri EPA per camera pulita (50% del costo)	12					
Materiali di consumo per accesso in camera pulita (tute, calzari, cuffie, materiale per incollaggio)	4					
Licenza Autodesk Inventor per disegni meccanici	3					
Totale	87,5					
	87,5					

A financial travel request of **17.5** K€ for the RM2 participation to Test Beam (**10** K€ s.j), collaboration meetings and exchanges with Catania and Genova groups.

Total Request 105 K€

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GEM-µRWELL ECT: Summary

- Geometrical Acceptance and Technical Performances of hybrid GEM-μRwell endcap trackers have been assessed.
- A detector layout compliant with position resolution and tracking efficiency requirements has been identified.
- The disks are segmented into four quadrants, connected and attached to the inner tracker support, maximizing the azimuthal and polar acceptances. Integration studies are on-going
- Readout Electronics is based on SALSAASIC, being developed at Saclay. A SALSAASIC emulator is being designed at INFN Roma Tor Vergata to test the final electronics chain.
- Workforce is available to meet the timeline of the production and assembly plans.
- Production timeline is consistent with the overall ePIC detector schedule.
- INFN In kind financial support has been re-modulated accordingly.

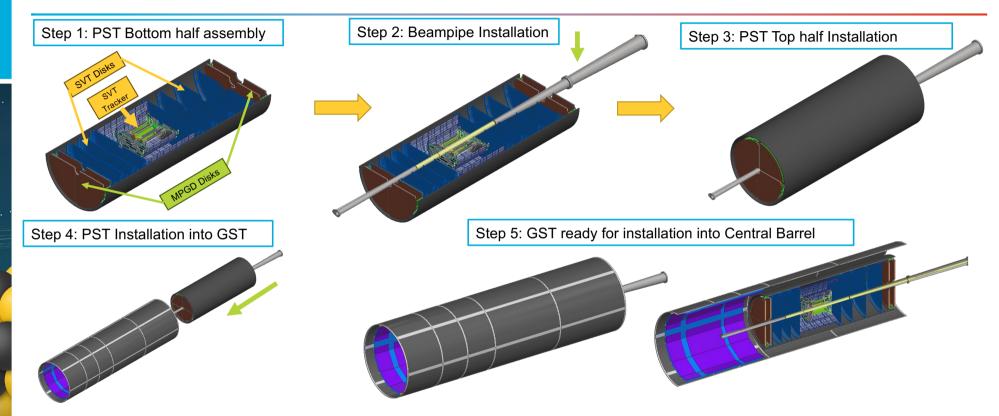
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Thank you

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ECT Integration - Global Support Tube (GST) Assembly



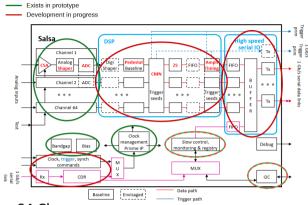
• Global Support Tube (GST) is the Carbon fiber support structure for inner detectors.

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FY25 Status & Progress – Cont.

MPGD – SALSA (CEA-Saclay, U. Sao Paulo)



- 64 Ch
- 65 nm CMOS
- Peaking time: 50 500 ns;
- Inputs: Cdin<200 pF; Dual polarity; Q: 3 250 fC
- ADC: 12 bits, 5 50 MSPS.
- Extensive data processing capabilities
- I2C configuration.
- Triggerless and triggered operation;
- Several 1 Gbps links.
- Power: 15 mW/Ch
- Radiation tolerant.

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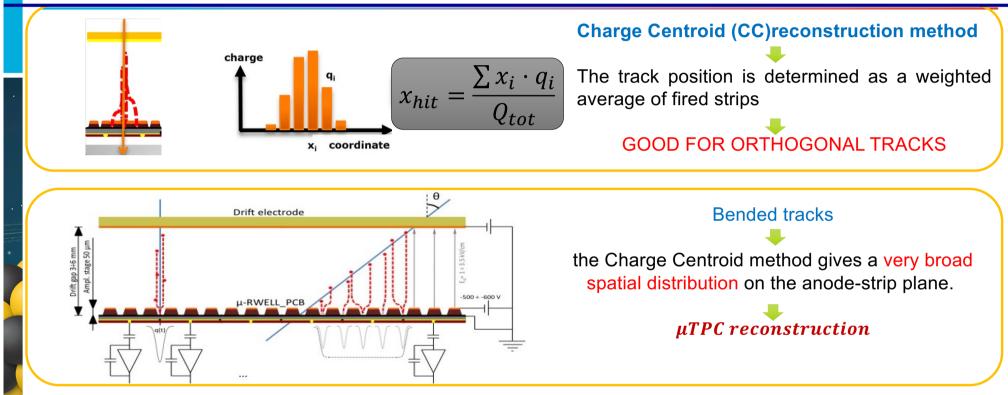
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SALSAO analog 1.5x1 mm²



- Progress Summary:
 - SALSA0_analog and SALSA0_digital blocks fully characterized.
 - PLL block + services prototyped under PRISME. PRISMEv1 April 2025.
 - SALSA1 (full frontend and ADC chain) under tests.
 - SALSA2 (32 ch fully featured with DSP) design in progress for submission in October 2025.
 - Tests in 2026, available to users.
 - SALSA0 (IP blocks): FY23
 - SALSA1: FY23 FY24
 - SALSA2: FY23 FY25
 - SALSA3: FY25 FY26
 - SALSA: FY27 FY28 Production

Possible position resolution improvement - μTPC



The spatial resolution is strongly dependent on the impinging angle of the track =>

A not uniform resolution in the solid angle covered by the apparatus => Large systematical errors.

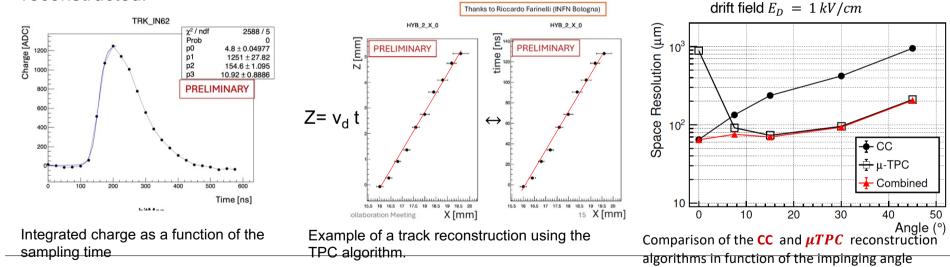
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Possible position resolution improvement - μTPC

A possible solution :

- > The electrons created by the ionizing particle drift towards the amplification region
- In the µTPC mode from the knowledge of the drift time and the measurement of the arrival time of electrons, the track segment in the gas gap is reconstructed
- > The fit of the analog signal gives the arrival time of drifting electrons.
- By the knowledge of the drift velocity, the 3D trajectory of the ionizing particle in the drift gap is reconstructed.



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