

EUROPEAN
PLASMA RESEARCH
ACCELERATOR
WITH
EXCELLENCE IN
APPLICATIONS



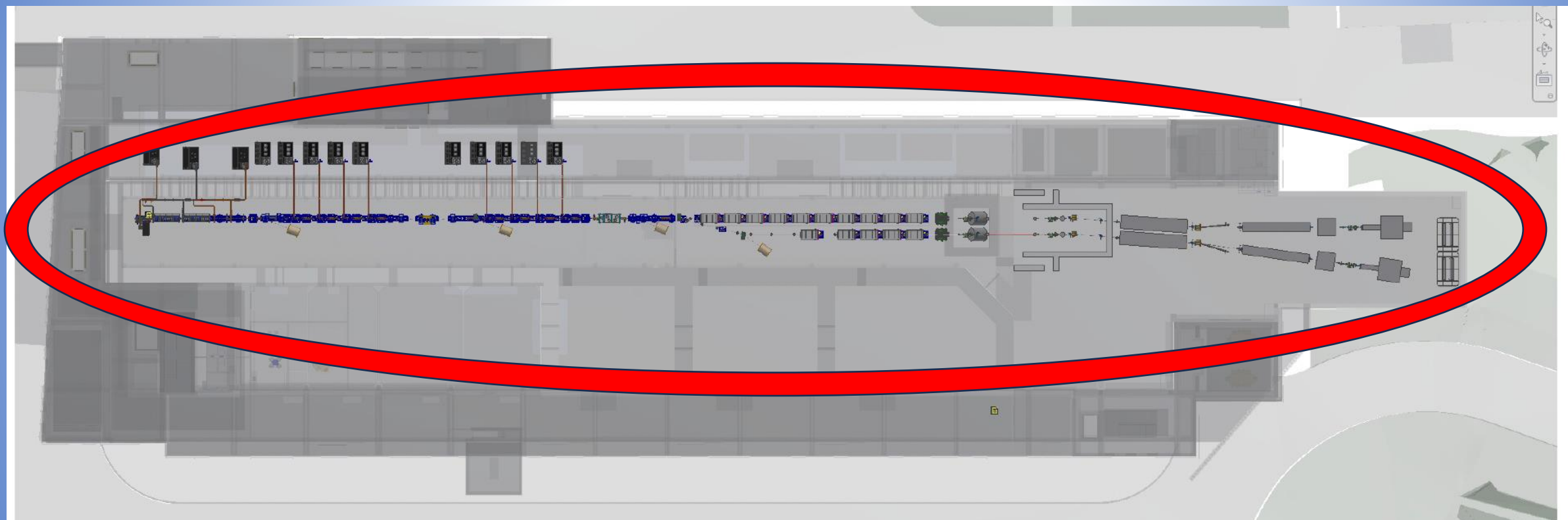
Chapter 20: Functional Safety Systems

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1	Functional Safety Systems	9
1.1	Overview	9
1.2	Scope and Compliance	9
1.3	Machine Protection System (MPS)	10
1.3.1	Requirements and Specifications	11
1.3.2	Architecture Prototype	12
1.3.3	Functional Safety	12
1.3.4	Software Design	13
1.3.5	Hardware Design	13
1.3.6	Validation and Verification	14
1.4	Personnel Safety System (PSS)	14
1.4.1	Requirements and Specifications	14
1.4.2	Architecture Prototype	15
1.4.3	Functional Safety	16
1.4.4	Software Design	16
1.4.5	Hardware Design	16
1.4.6	Validation and Verification	17

Objective

Ensure the safety of personnel and the protection of accelerator equipment by developing a robust, safe and reliable system in compliance with applicable safety standards and risk assessment.

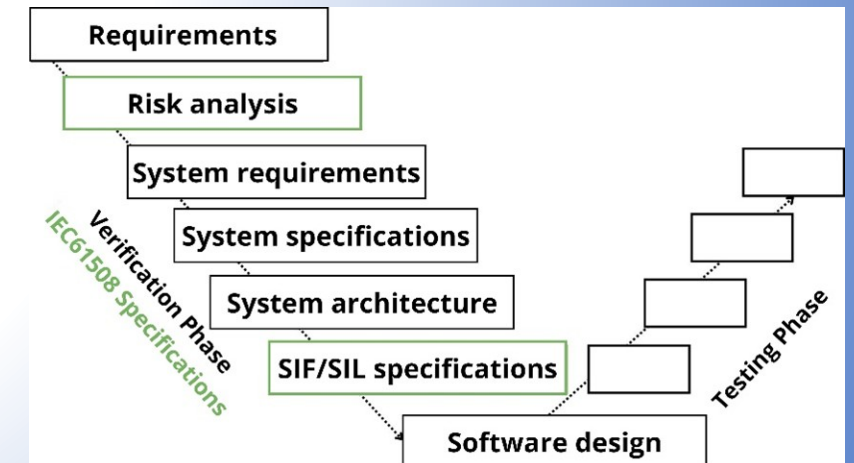
Involved Systems

- MPS (Machine Protection System): safeguard the accelerator and associated equipment
- PSS (Personnel Safety System): protects personnel from radiation and other hazards



System Architecture:

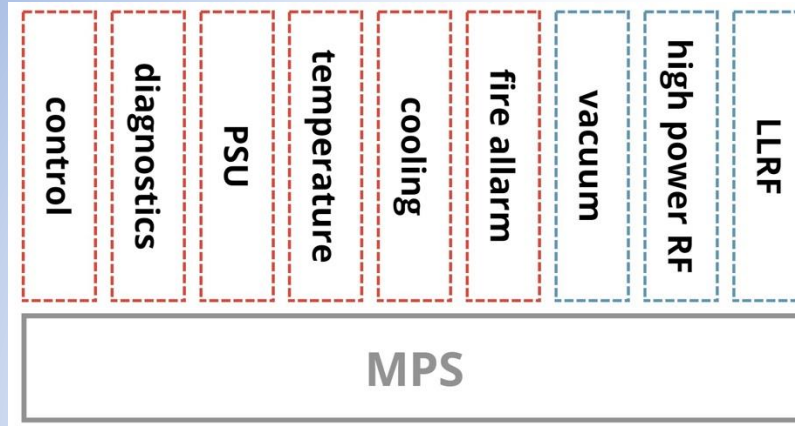
- Designed following the V-model development lifecycle
- Validation and rigorous verification at each stage of the system's design and implementation, ensuring systematic traceability
- This approach supports the development of high-integrity safety systems, from initial requirements to final testing and deployment.



Standards Compliance: IEC-61508, ANSI N43.1, NCRP REPORT N.88

MPS

Detect interlocks
Prevent anomalies
Optimize operations
and performance



Requirements (examples):

SR01: RF, Vacuum, Temperature Monitoring

SR05: Operate reliably during electrical anomalies or outages

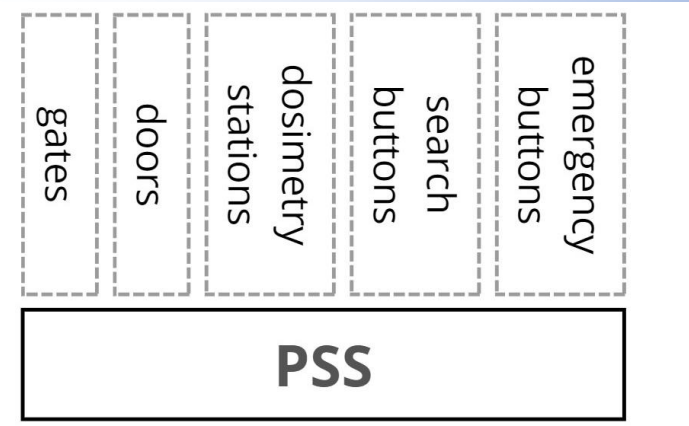
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Specifications (examples):

SS01: A multi-head system is required for signal monitoring (Ref.SR01)

SS02: The cRIO PSU must be backed by a UPS to guarantee 10 minutes (600 s) of operation during outages. (Ref. SR05)

...



PSS

Preventing access
to bunker and
restricted areas
Enable/disable RF
and triggers

Requirements (examples):

SR02: Access Lock During Active Beam

SR05: Event and Access Tracking

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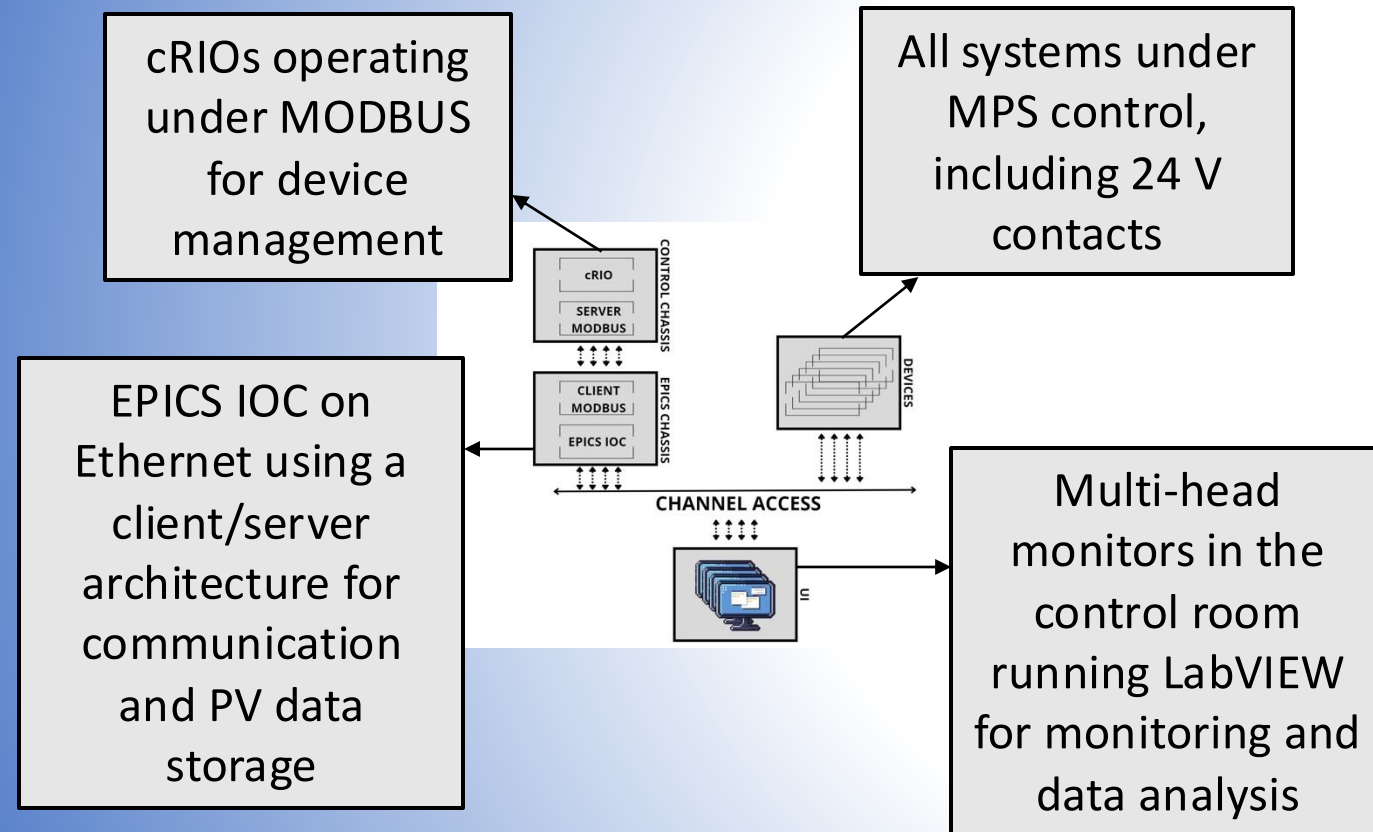
Specifications (examples):

SS03: Electromechanical locks shall be installed on all controlled-area doors. (Ref. SR02)

SS09: All critical safety events shall be logged with timestamps and user IDs. (Ref. SR05)

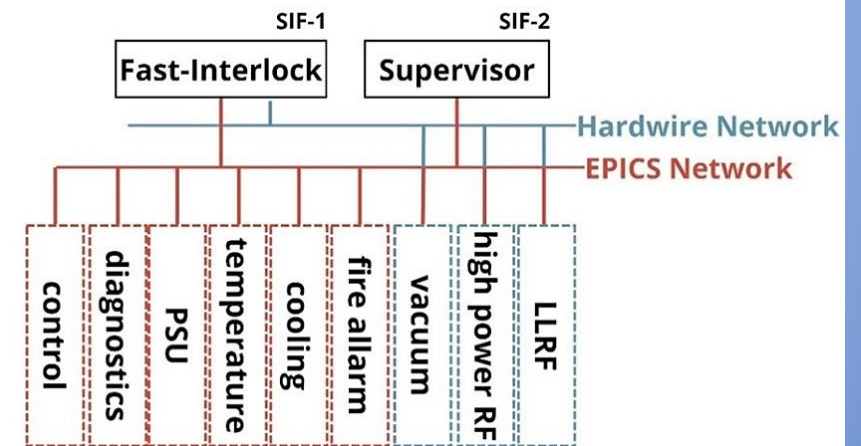
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Architecture



Functional Safety

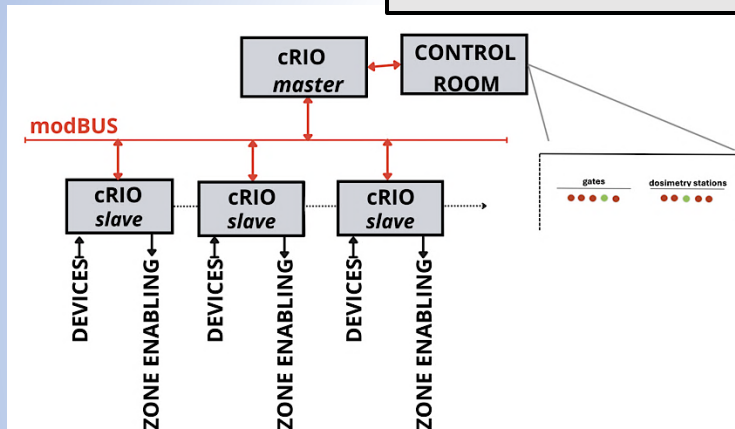
Based on the risk matrix [1], the required SIL for the SIFs is **SIL-1**, corresponding to a Probability of Failure per Hour (PFH) in continuous mode between 10^{-6} and 10^{-5} .



Architecture

Distributed architecture in master - slave configuration

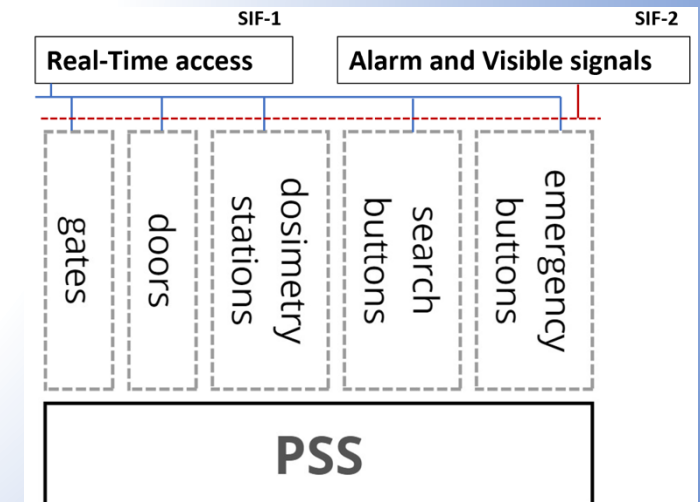
A LabVIEW-based supervisor monitors the status of enabled/disabled devices.



All devices and all cRIO output modules, configured normally open with interlocks assumed active-low. Redundant 24 VDC contacts are used to enhance reliability.

Functional Safety

Based on the risk matrix [1], the required SIL for the SIF-1 is **SIL-2** - PFH in continuous mode between 10^{-7} and 10^{-6} ; for the SIF-2 is **SIL-1** - PFH in continuous mode between 10^{-6} and 10^{-5} .



Achievements:

Technological Readiness Level (Sub-Components)

Sub - systems	TRL	Comments
PSS	9	The PSS architecture has already been defined based on a proven configuration, currently in use at the TEX, SSRIP and soon at EuAPS facilities, where it has been validated with the same operational logic and hardware setup.
MPS	9	The MPS architecture has been defined and validated based on a configuration already implemented at the TEX, SSRIP and soon EuAPS facilities and further tested in other accelerator environments, confirming its reliability and scalability.