

Introductory course to VHDL and HLS FPGA programming

Thursday, 26 June 2025

Introduction to HLS - U6-11 (09:30 - 13:00)

time	[id] title	presenter
09:30	[21] HLS - Introduction and Numerics	SUMMERS, Sioni
11:00	Coffee break	
11:30	[22] HLS - Introduction and numerics	SUMMERS, Sioni

Friday, 27 June 2025

Introduction to HLS - U6-01e (09:30 - 13:30)

time	[id] title	presenter
09:30	[25] HLS - Loops	SUMMERS, Sioni
11:00	Coffee break	
11:30	[26] HLS - Loops	SUMMERS, Sioni