## Output lines monitored through pins



DOUT(4) DOUT(2) DOUT(0)

CLK\_OUT

## Output lines monitored through pins



## **Deserializer & Spy Buffer**

fcaafcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaafcaa

fcaafcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaa fcaafcaafcaafcaafcaafcaafcaafcaa for for for for for for

- Words of 128 bits saved into a Spy buffer
- Without "START" command, only IDLE words into output lines
- PHASE: settable parameter to be maximized (thanks to an IDLE word Counter)

## I<sup>2</sup>C communication clock

- Actual clock --> 100 kHz
- Test1: 10 kHz --> not working (problem with the ACK)
- Test2: 400 kHz --> does it work better? Seems yes... Is it perfect? No

Few numbers for communication @400 kHz:

- Read stage: 70-80% completed successfully.
- Write stage: > 90% completed successfully.
- Works better with configuration files than 100 kHz
- Sometimes need a RESET (random effect)