



# Silicon On DIAMOND CHIP concept (CHIPSODIA)



INO-CNR

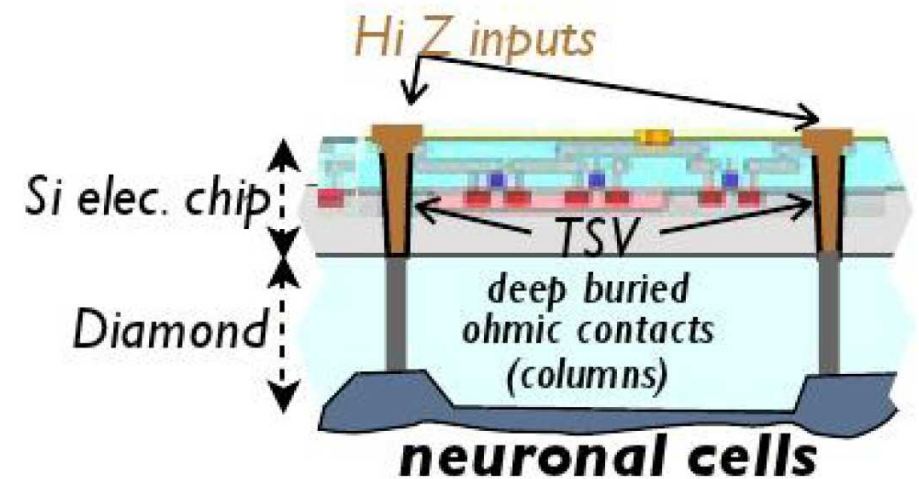
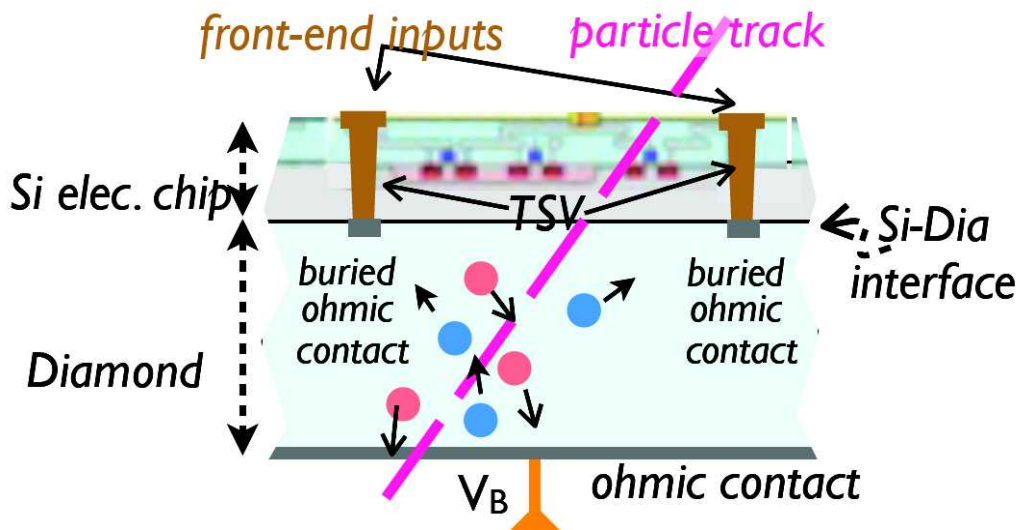
Silvio Sciortino, Giuliano Parrini,  
Stefano Lagomarsino  
for the CHIPSODIA experiment



Firenze, June 4-8 2012 INFN

# CHIPSODIA aims at two prototypes

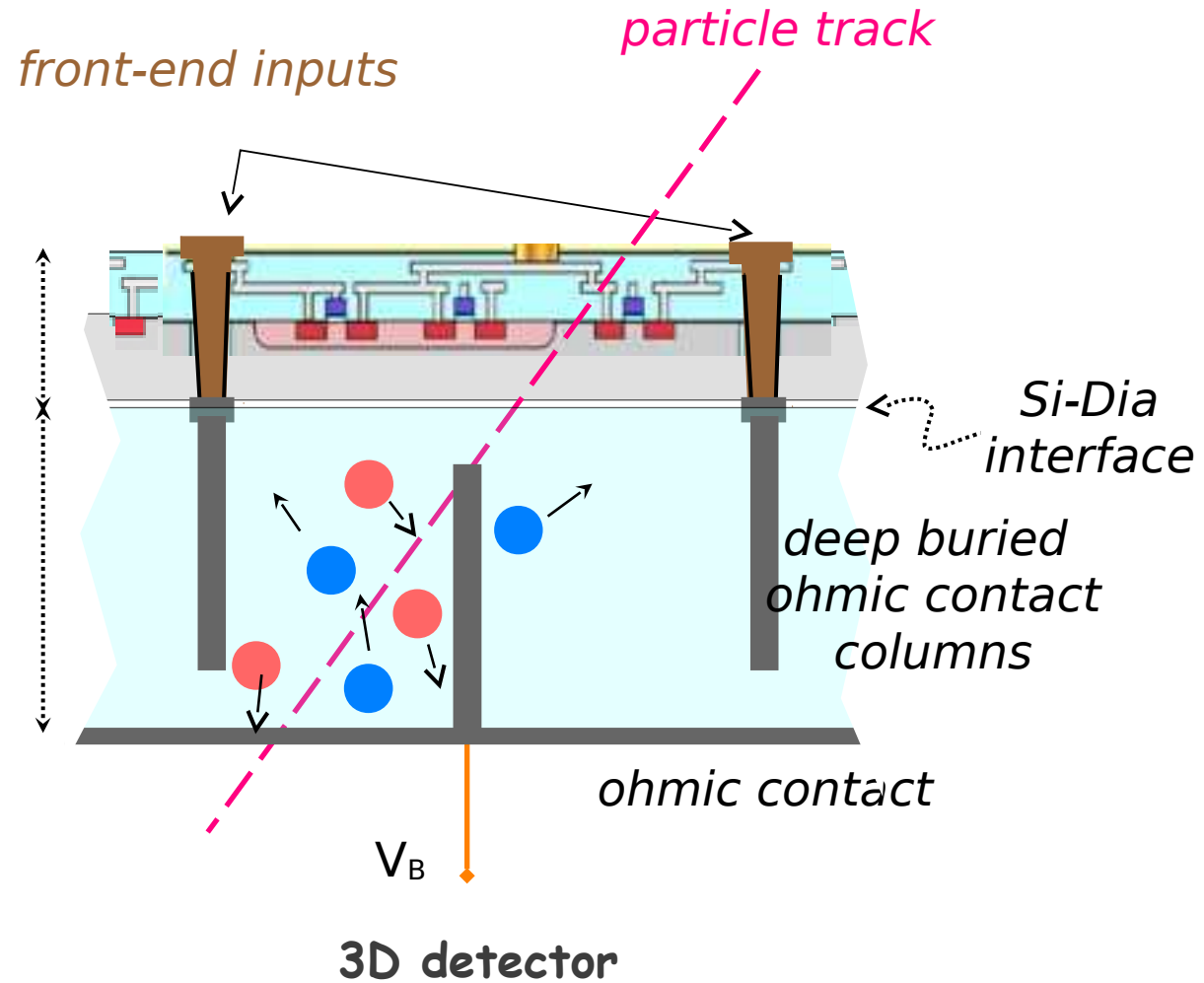
Proof of concept within 2012



a) Chip-On-Diamond Sensor: diamond connected to the readout electronics by Through Silicon Vias (TSV).

b) SOD Micro-Electrode Array (MEA): diamond hosting neural tissue connected to the R/W electronics by conductive channels and TSVs

Feasibility of a 3D structure is also investigated



## Main issues

Silicon to Diamond Bonding and Characterization

Chip bonding to diamond plates

Connection of the die electronics to the diamond surface

Growing ohmic contacts by laser graphitization on the diamond surface

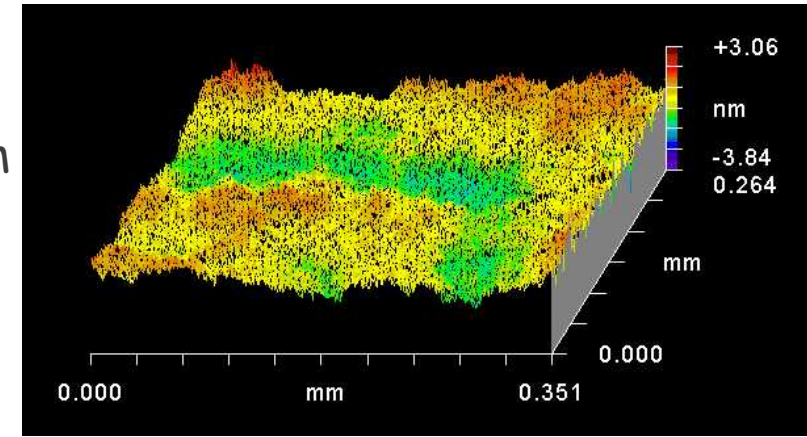
Growing graphitic channels through the diamond bias

Functionalization of the diamond surface and implantation of neural cells for MEA applications

# Silicon On Diamond Fabrication: Si & D samples



Si wafer cut in  
**5 × 5 mm<sup>2</sup> plates**  
Thickness from 390 to 50 μm  
 $\rho = 1 \text{ k}\Omega \text{ cm} \rightarrow 10 \text{ }\Omega \text{ cm}$   
Roughness ~1 nm

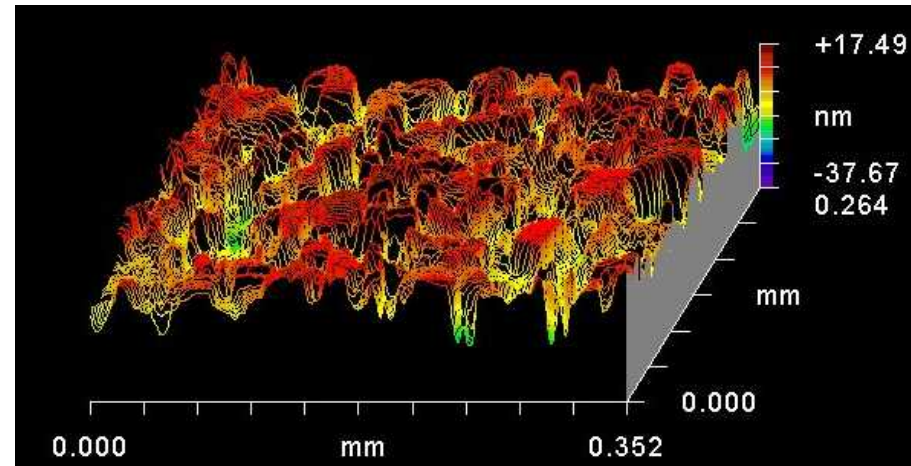
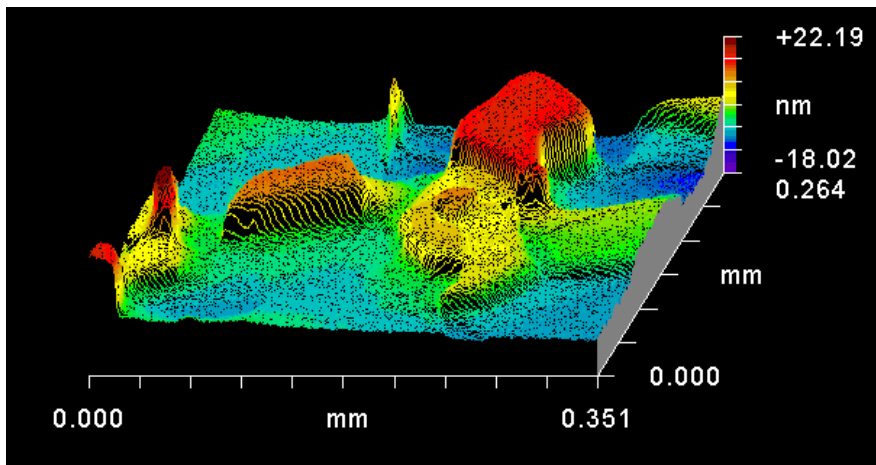


Diamond polyCVD 5 × 5 mm<sup>2</sup> plates  
from DDL Ltd

Thickness from 500 to 50 μm  
Roughness 5 nm at best

**Some scCVD plates recently available**

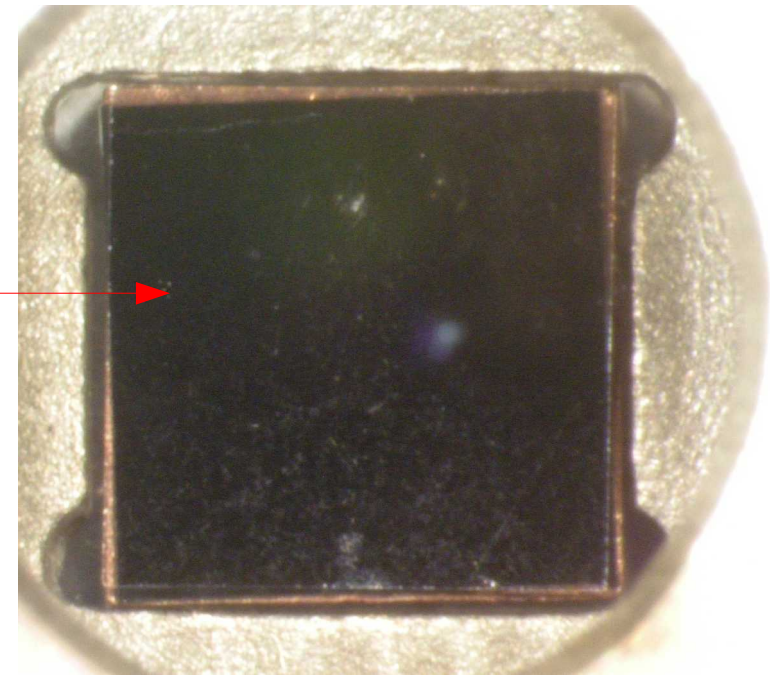
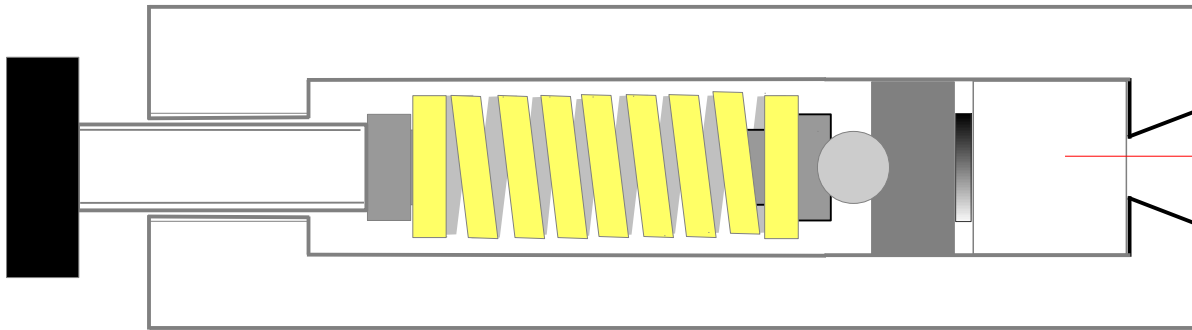
bad aspect ratio on the nucleation side for  
optical grade samples



# Silicon On Diamond Fabrication: **Cleaning and mounting**

Si & D plates are cleaned in a white chamber in ultrasonic bath assembled in a laminar flow hood

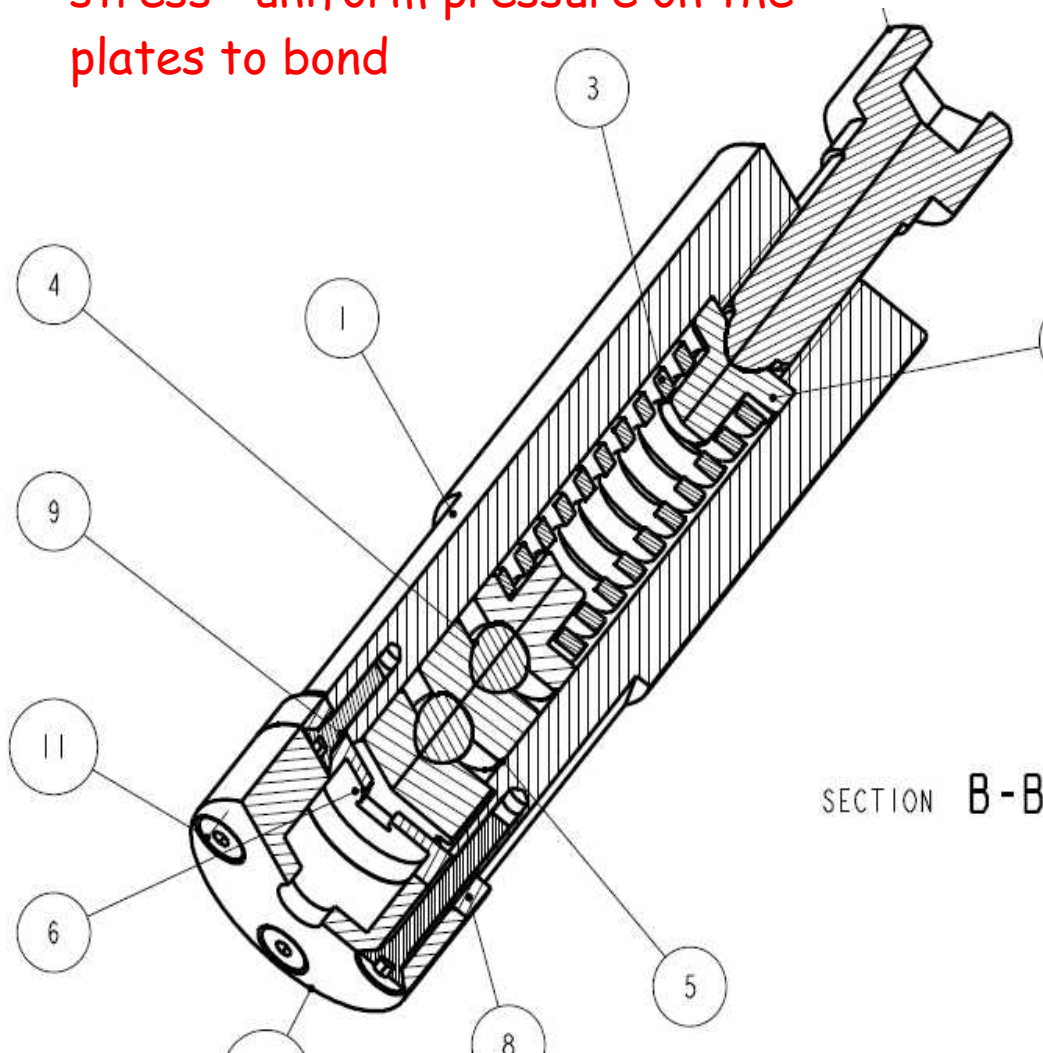
Diamond 5 × 5 mm<sup>2</sup> plate over silicon seen through the fused silica viewport



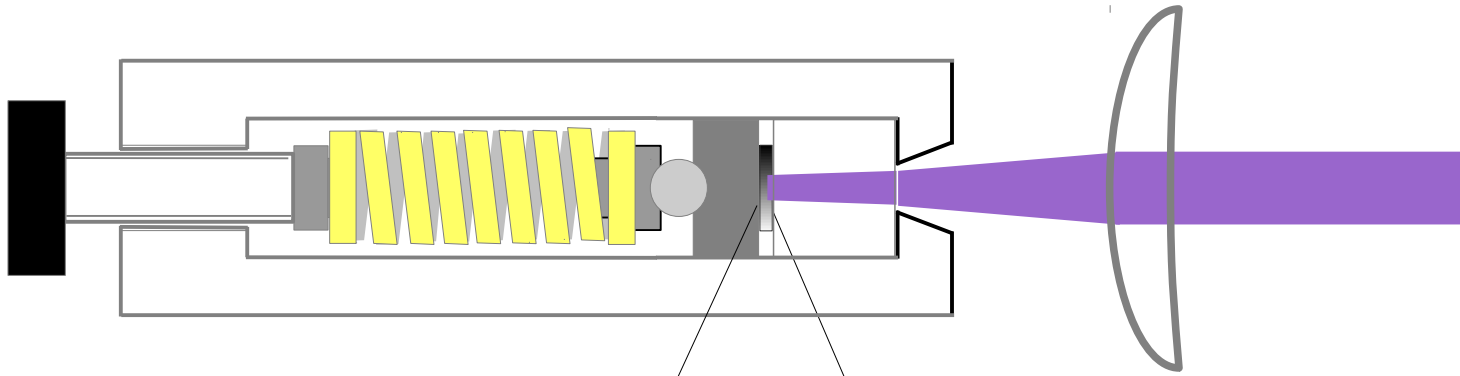
ASSEMBLY IMPLEMENTED BY IIT  
(3rd version just released)

Ease to manipulate and assemble  
pieces

Particular care in ensuring uniaxial  
stress—uniform pressure on the  
plates to bond

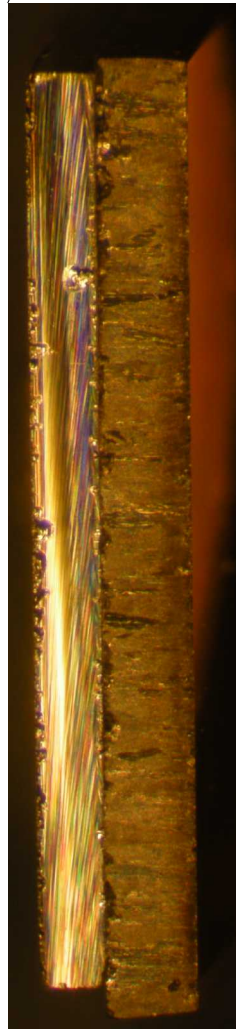


# Silicon On Diamond Fabrication: **Laser bonding**



Uniaxial stress: **800 atm**  
**needed\*** for 90 % adhesion  
with the present  $R_a \sim 5$  nm

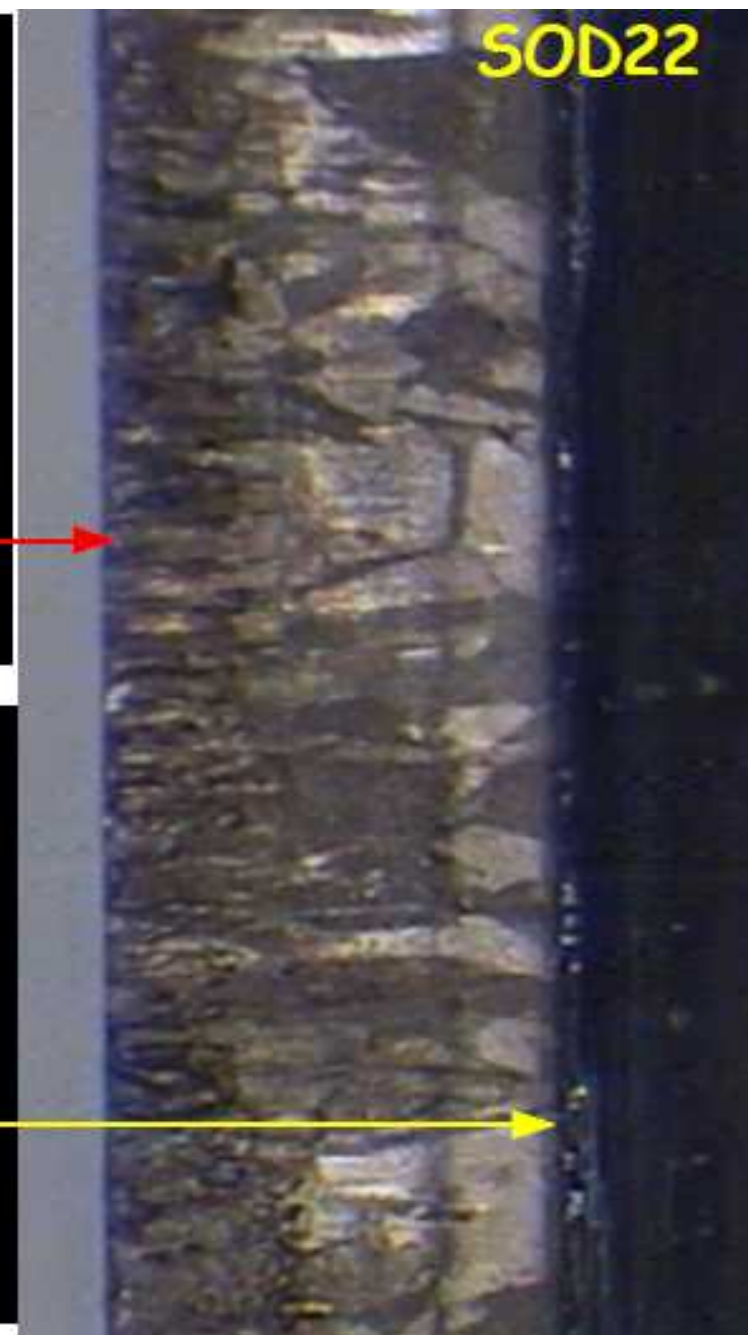
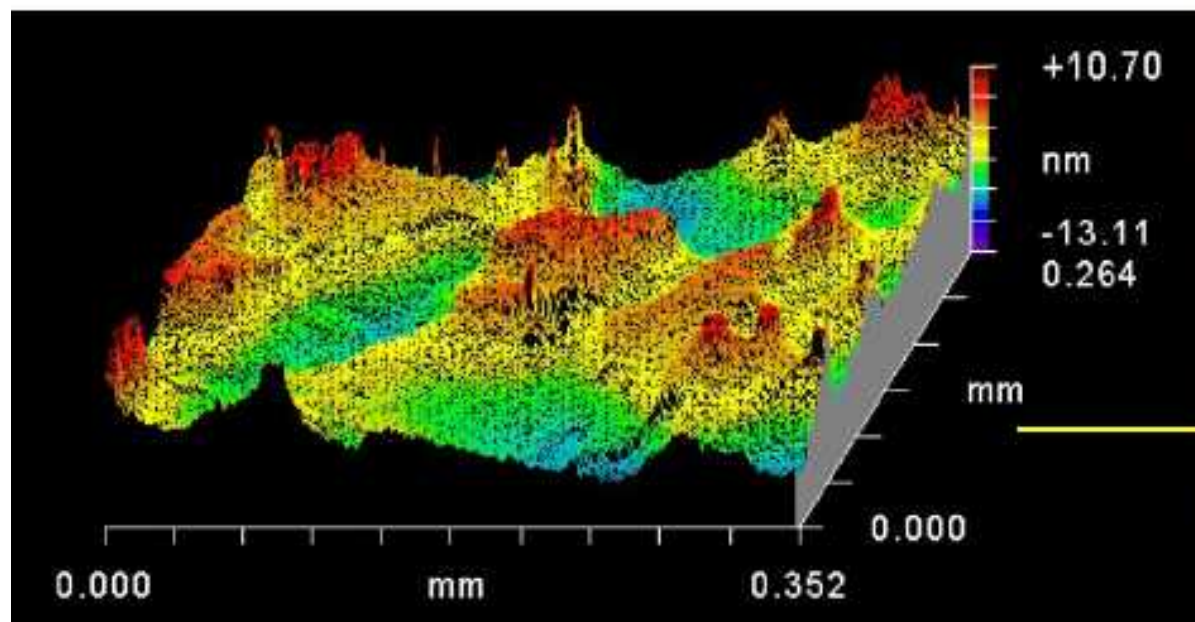
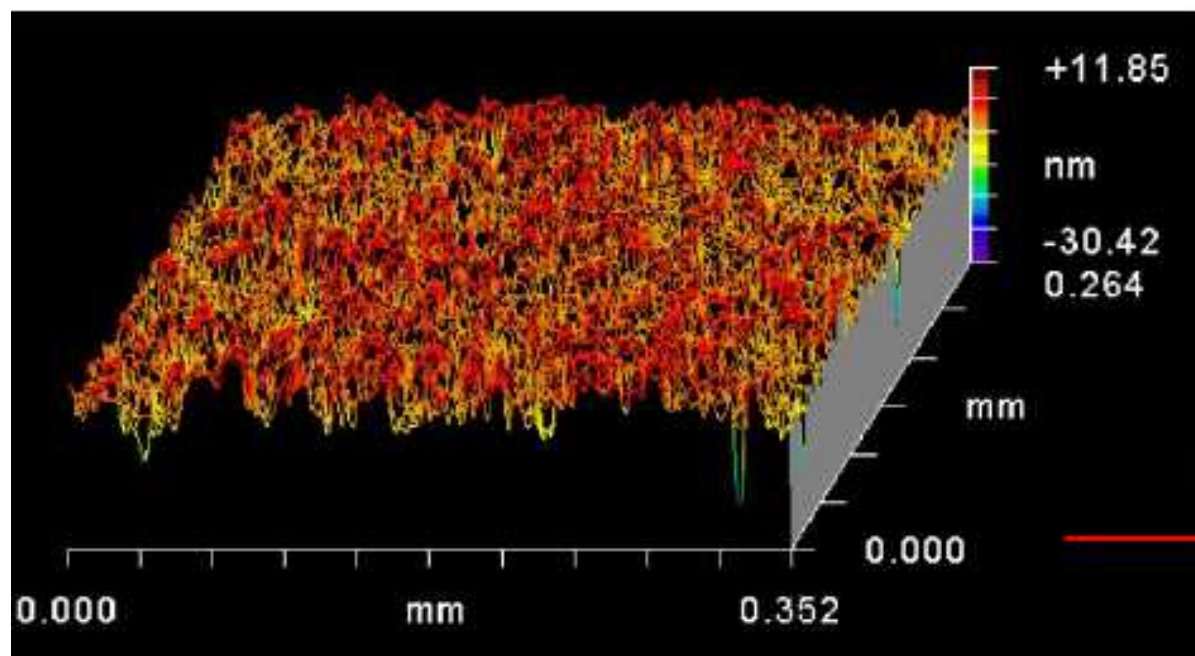
The diamond silicon interface is  
irradiated by UV laser pulses  
 $\lambda = 355$  nm  
 $\tau = 20$  ps  
Energy density =  $2-0.5$  J/cm<sup>2</sup>



\*Stefano Lagomarsino Ph,D  
Thesis  
[http://hep.fi.infn.it/sciortino/  
Research/dissertation\\_Lagomarsino.pdf](http://hep.fi.infn.it/sciortino/Research/dissertation_Lagomarsino.pdf)

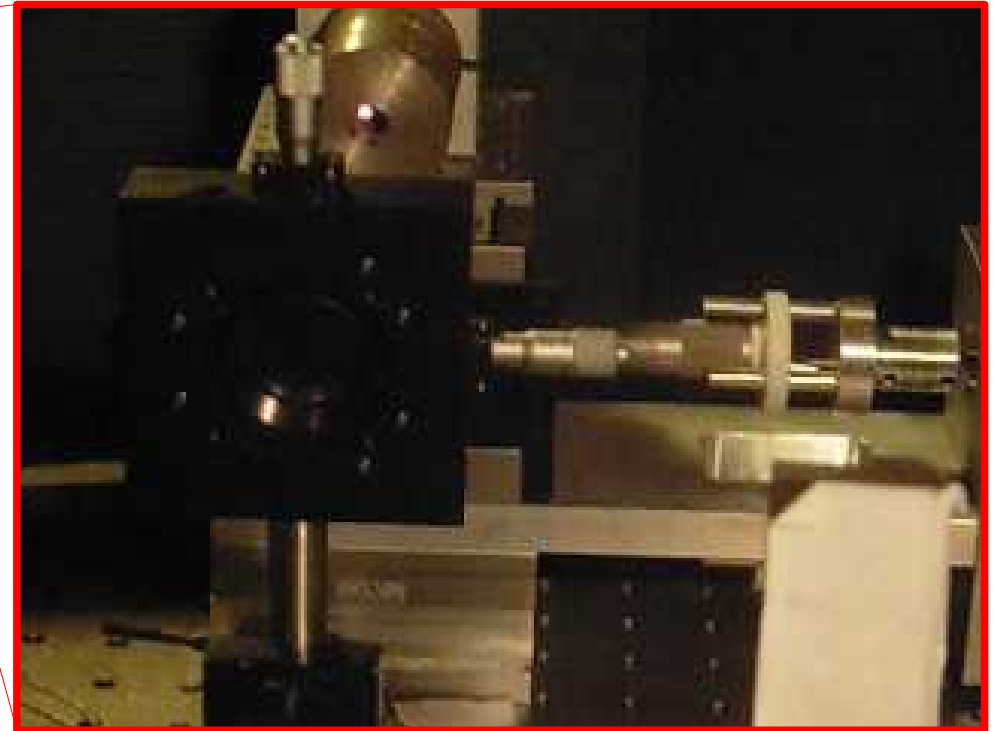
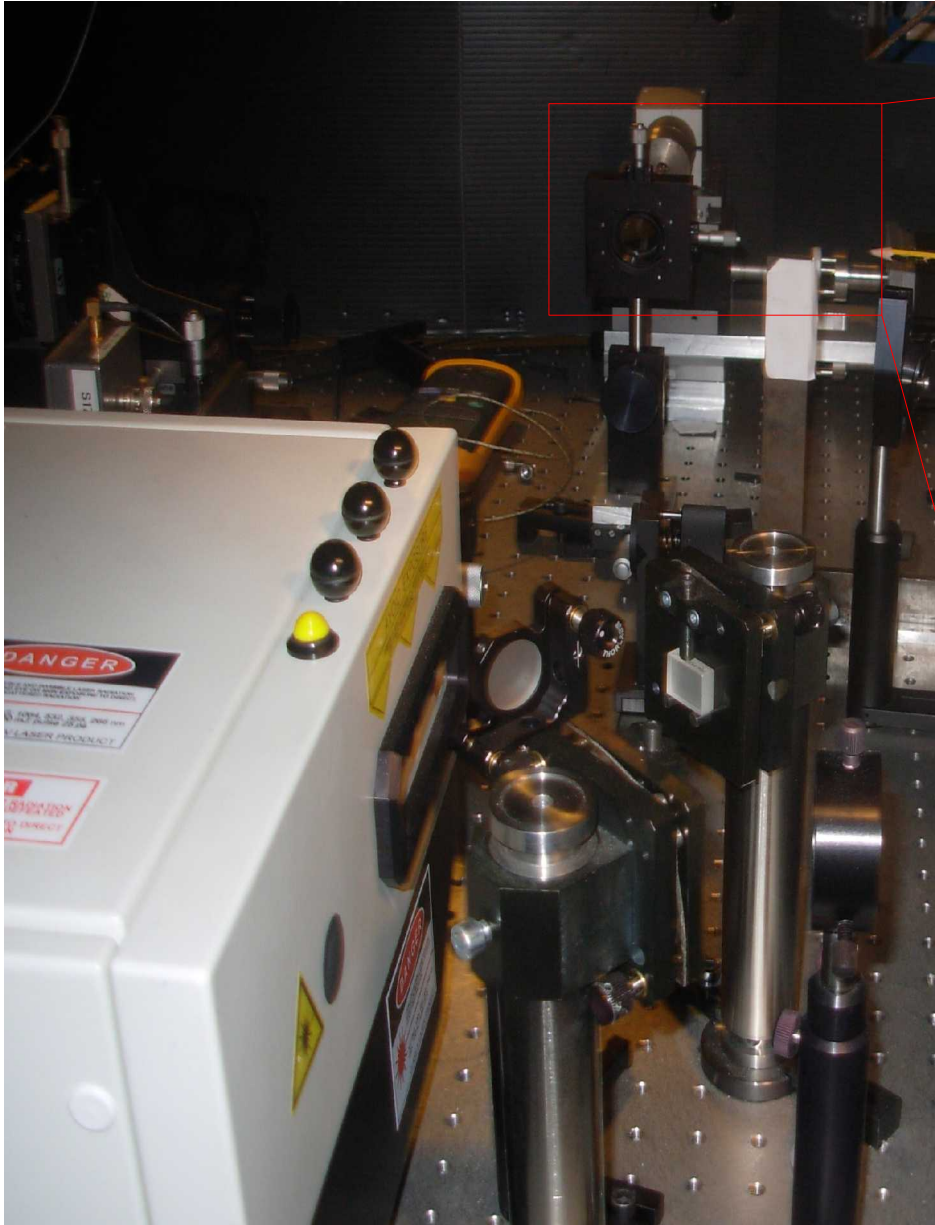


# Growth side of diamond mounted in contact with the silicon surface



# Silicon On Diamond Fabrication: **Laser bonding**

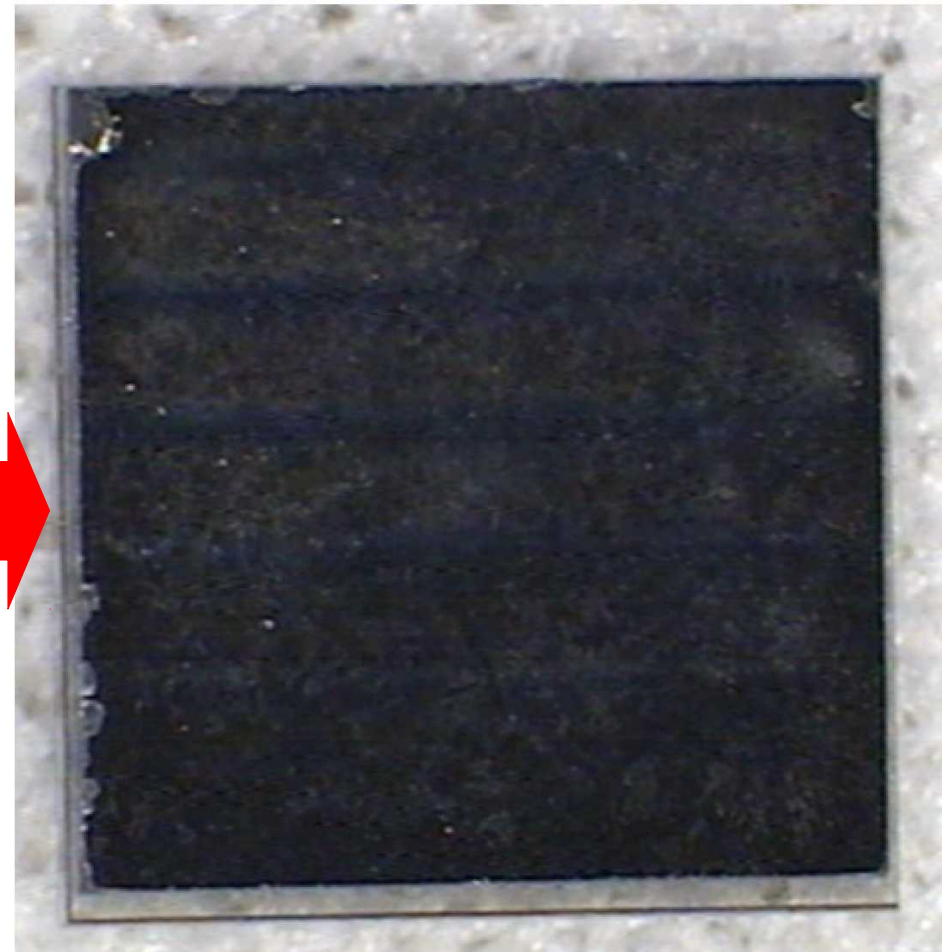
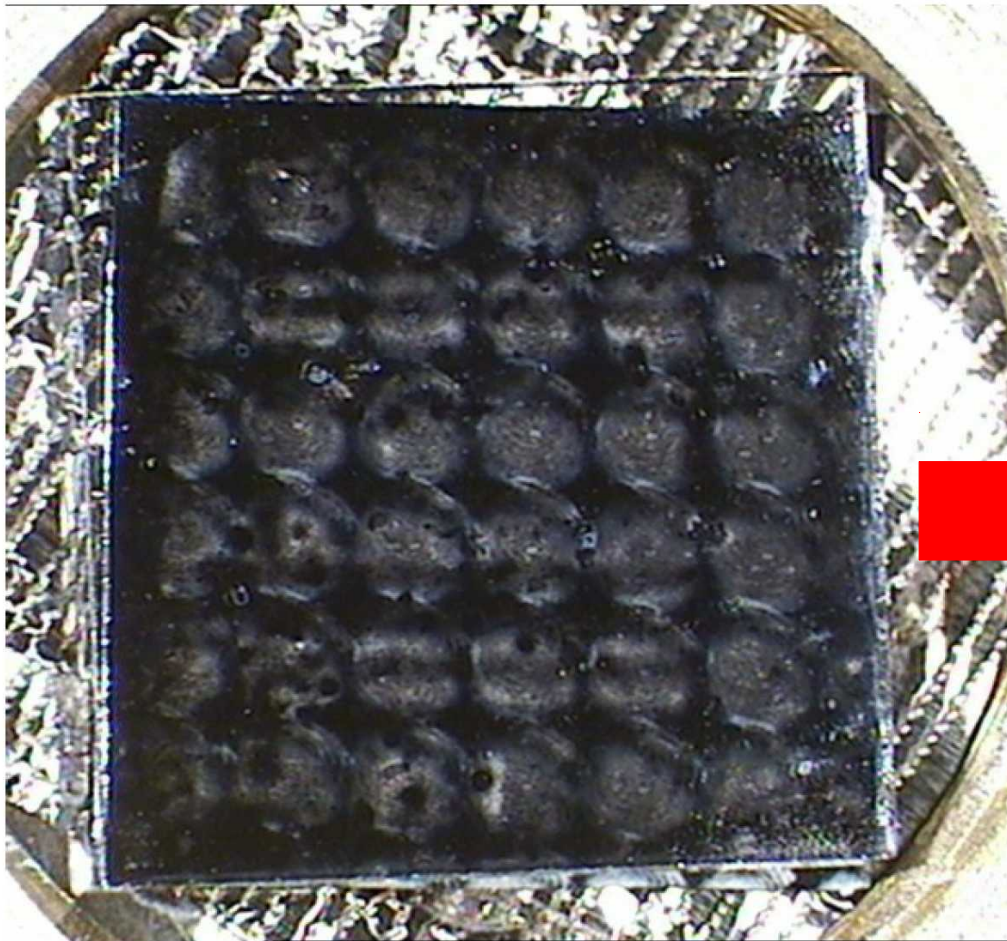
- Automated continuous scanning of the laser beam



# Silicon On Diamond Fabrication: **scanning the laser beam on the interface**

- More uniform illumination of the sample with continuous scanning

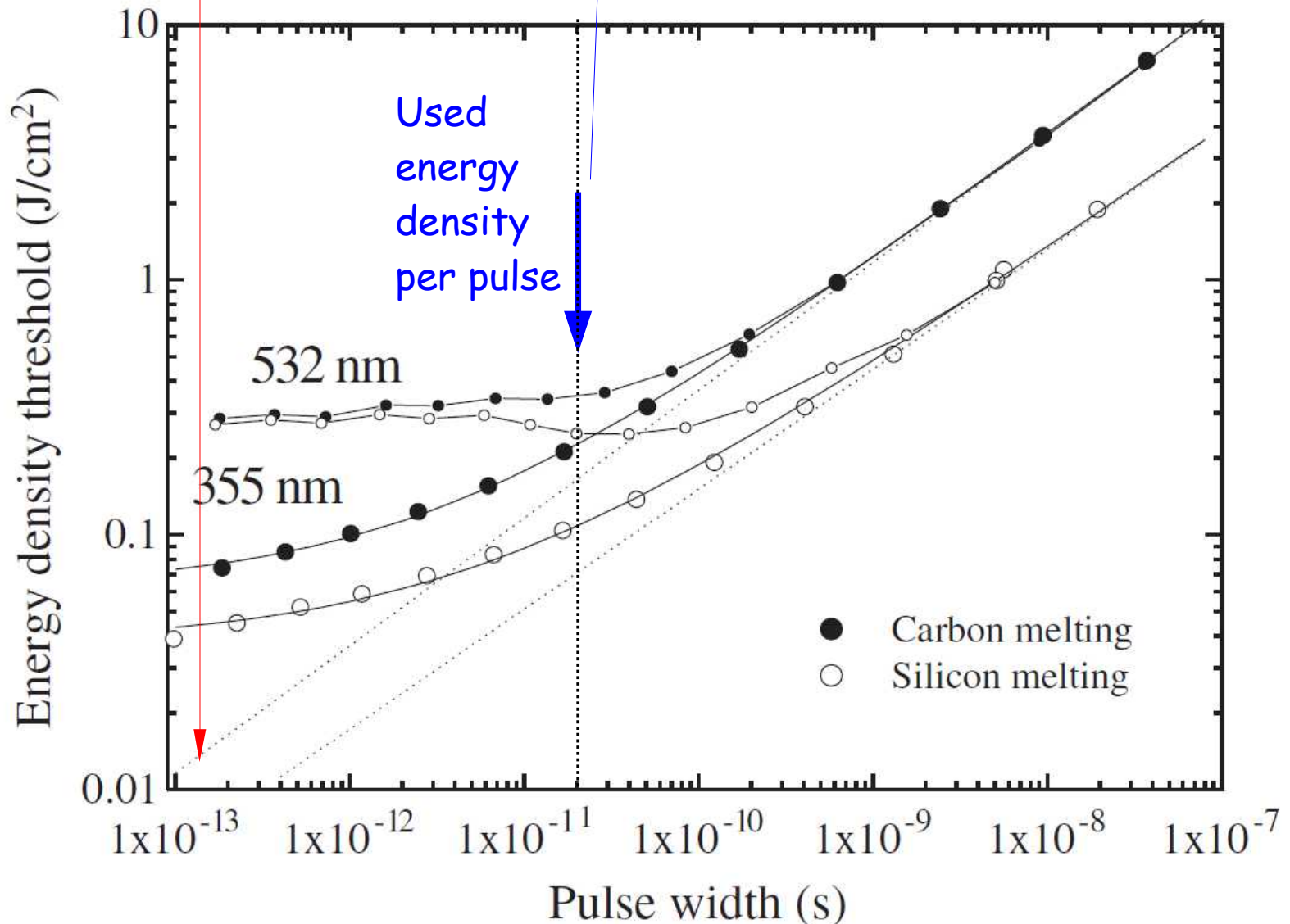
Spot on target 0.9 mm  
16 shots per mm on a row  
Row separated by 0.7 mm  
**Energy density 0.5 J/cm<sup>2</sup>**



**SOD12 OCTOBER 2009 & SOD23 JUNE 2011**

We can decrease energy density/pulse to threshold at 20 ps about  $0.22 \text{ J/cm}^2$ , expected interface thickness 40 nm

Or even lower the pulse width to lower the interface thickness to about 10 nm



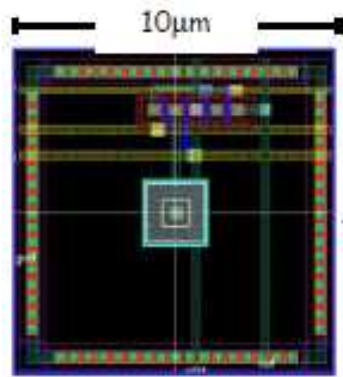
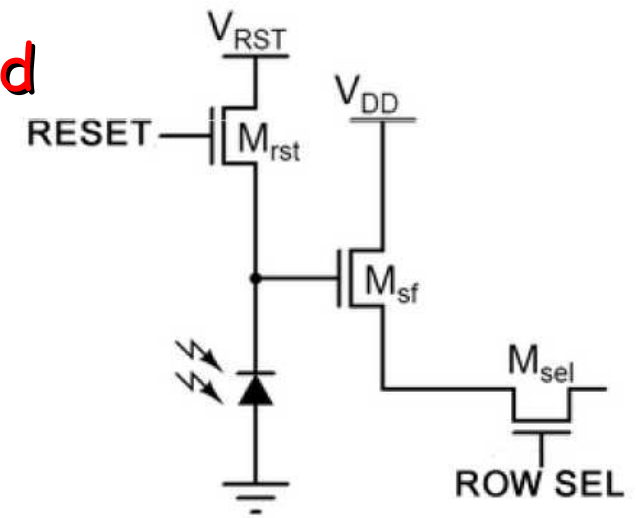
# RAPS on DIAMOND: successfully tested

## GOAL:

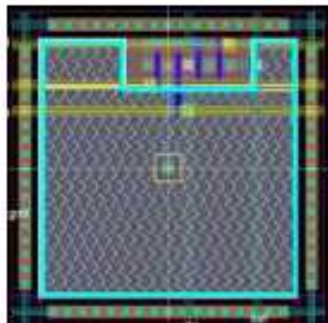
To test the functionality of a real chip

After  $\Rightarrow$  thinning (down to  $40\ \mu\text{m}$ )

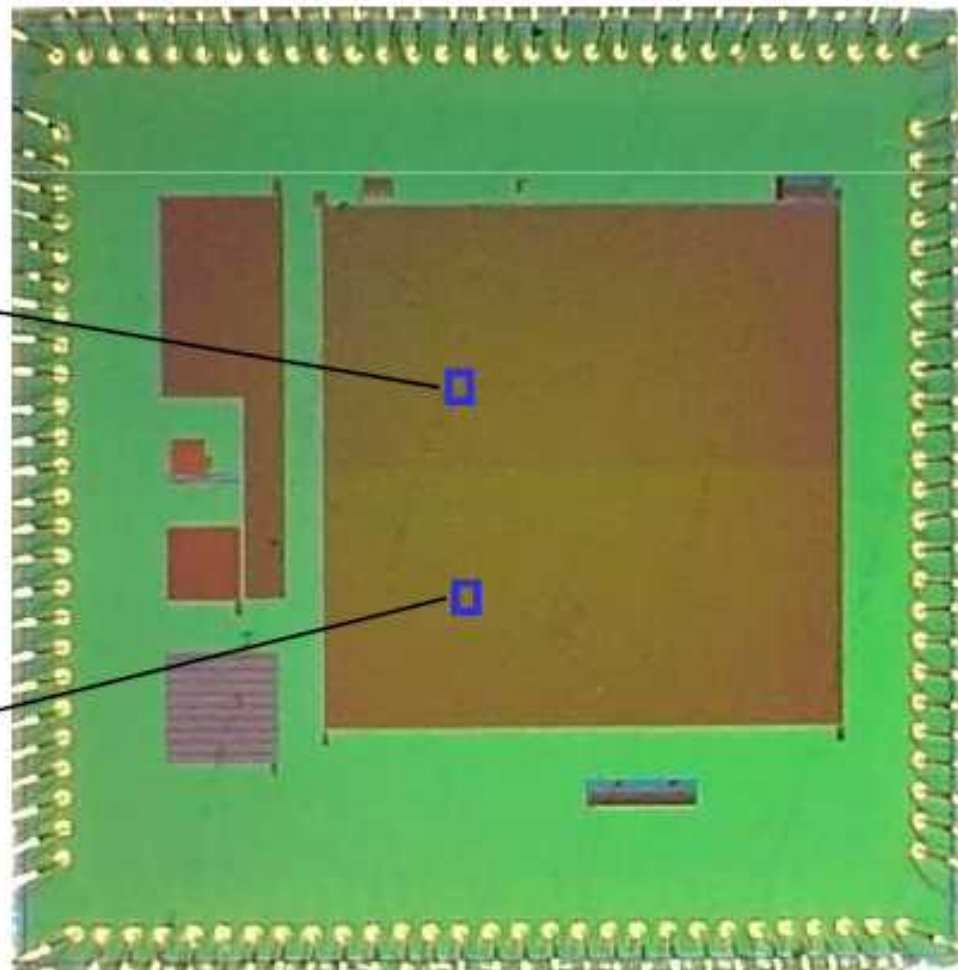
and  $\Rightarrow$  bonding to diamond



Small n-well, Low C

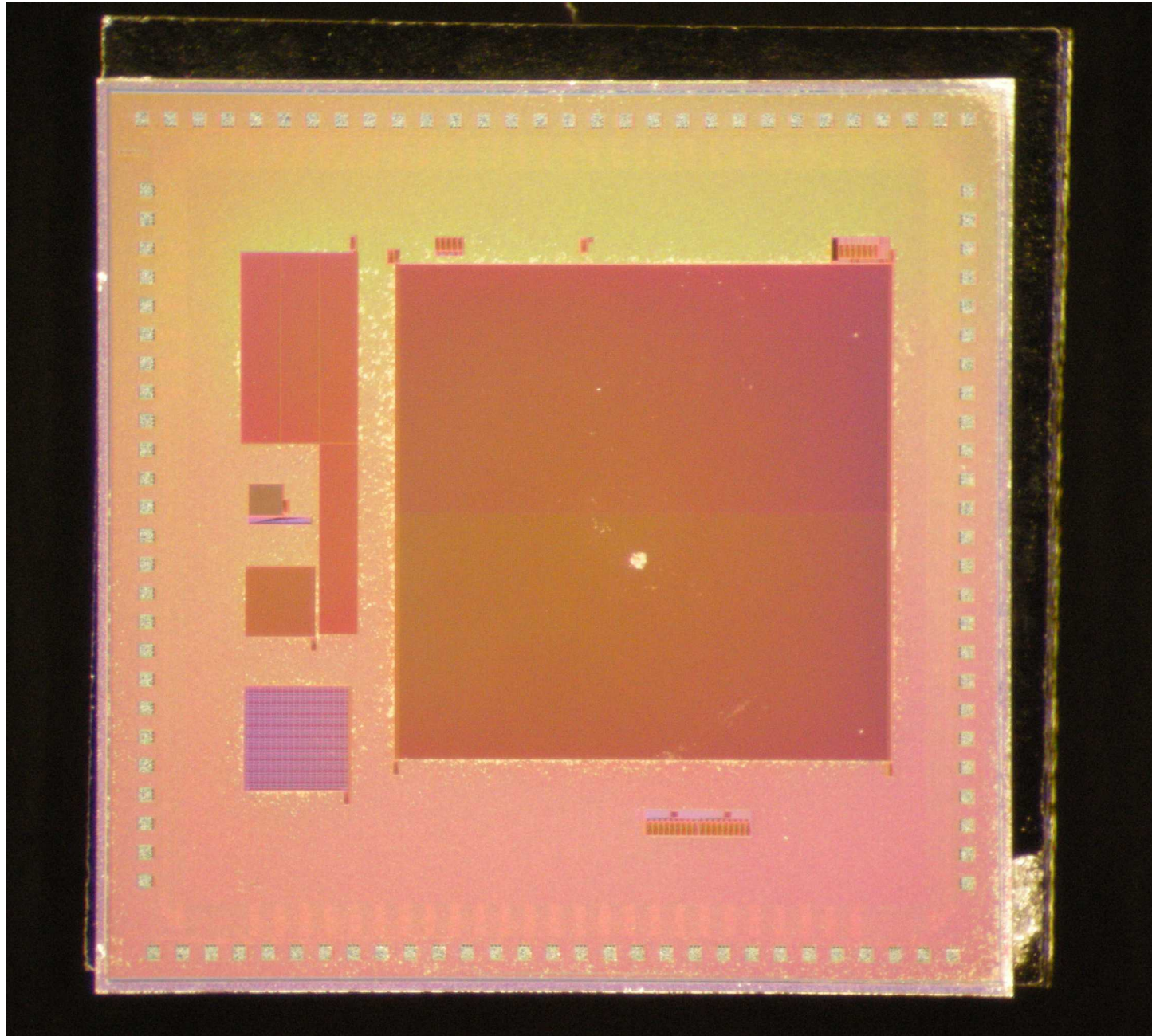


Large n-well, High FF

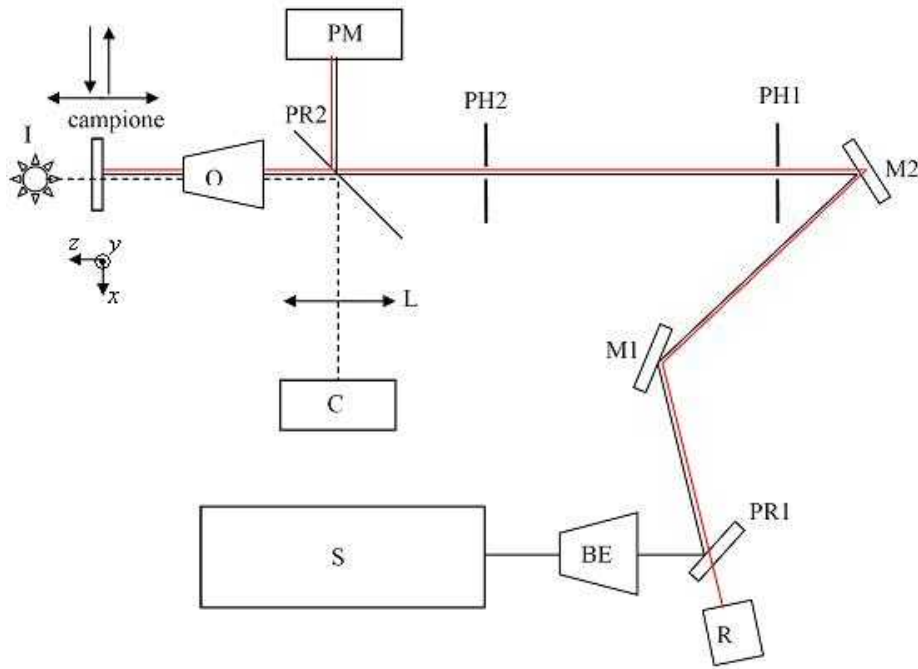


CMOS Active Pixel Sensors  
256 × 256 matrix

RAPS bonded on diamond (SOD\_34) successfully tested



# LASER graphitization: sources



Nd:YAG Q-switched  
laser source

1064 nm wavelength

8 ns pulse width

<100  $\mu\text{J}$  per pulse

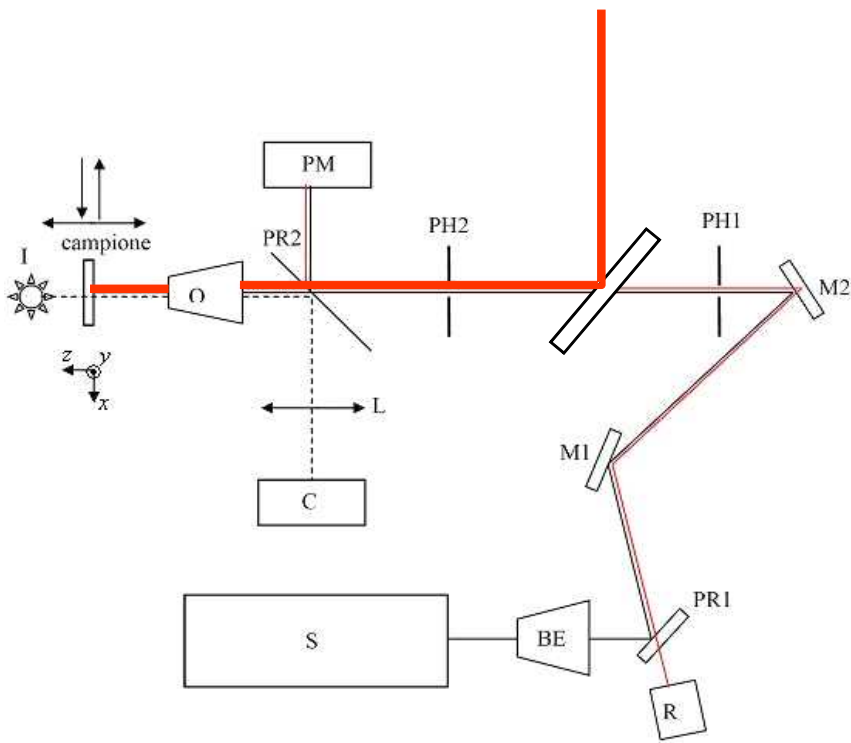


Real-time visualization  
system: 1  $\mu\text{m}$  resolution

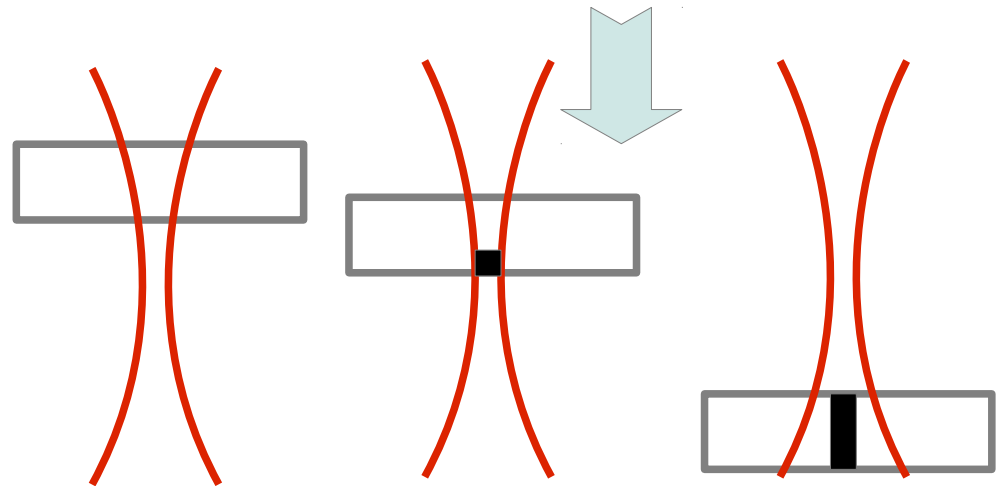
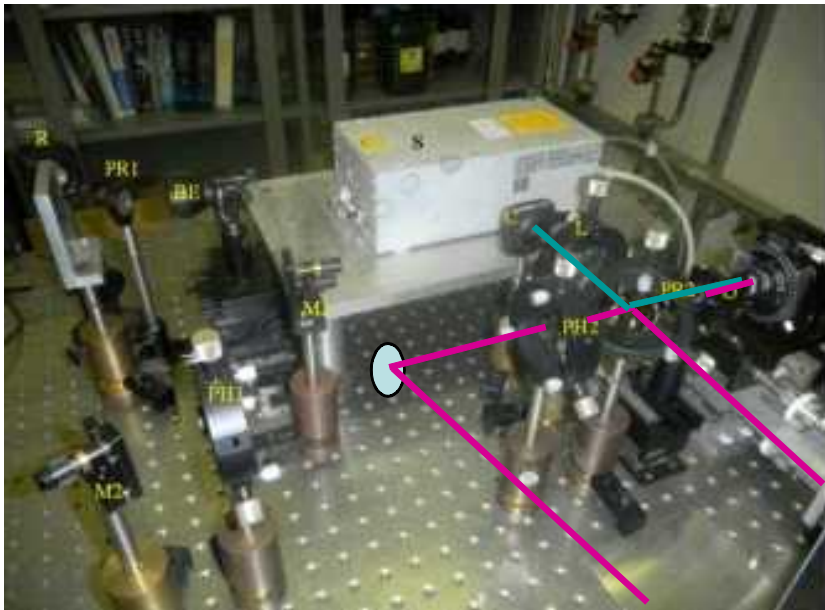


xyz  
automated  
stages

Inserted a line for a Ti:Sa  
800 nm, 30 fs laser beam  
<3 J/pulse

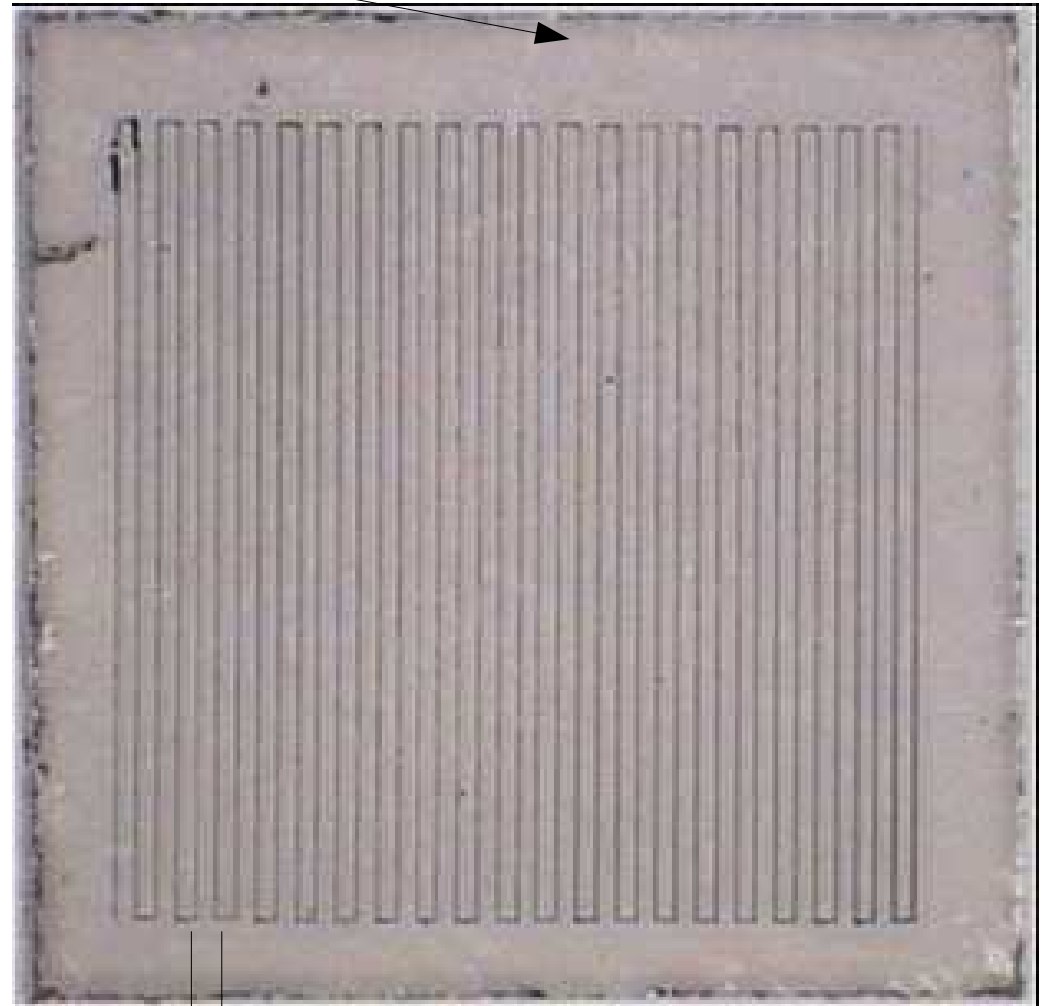
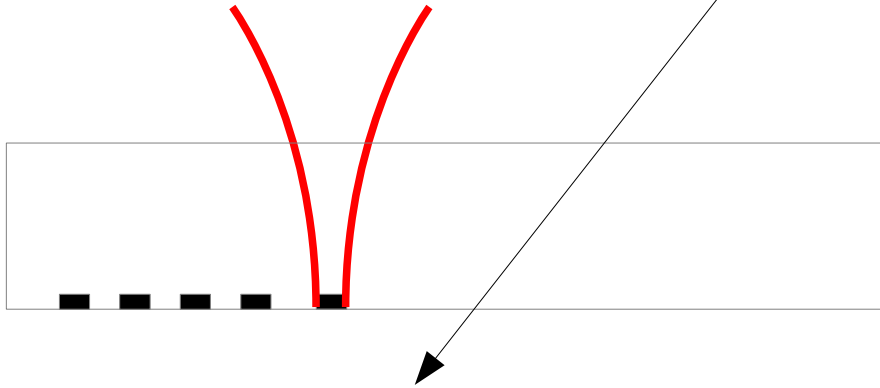


Channels bulk fabrication  
Sample is moved across the focus  
and columnar growth occurs above a  
threshold energy





Graphitic superficial dots or stripes can also be fabricated

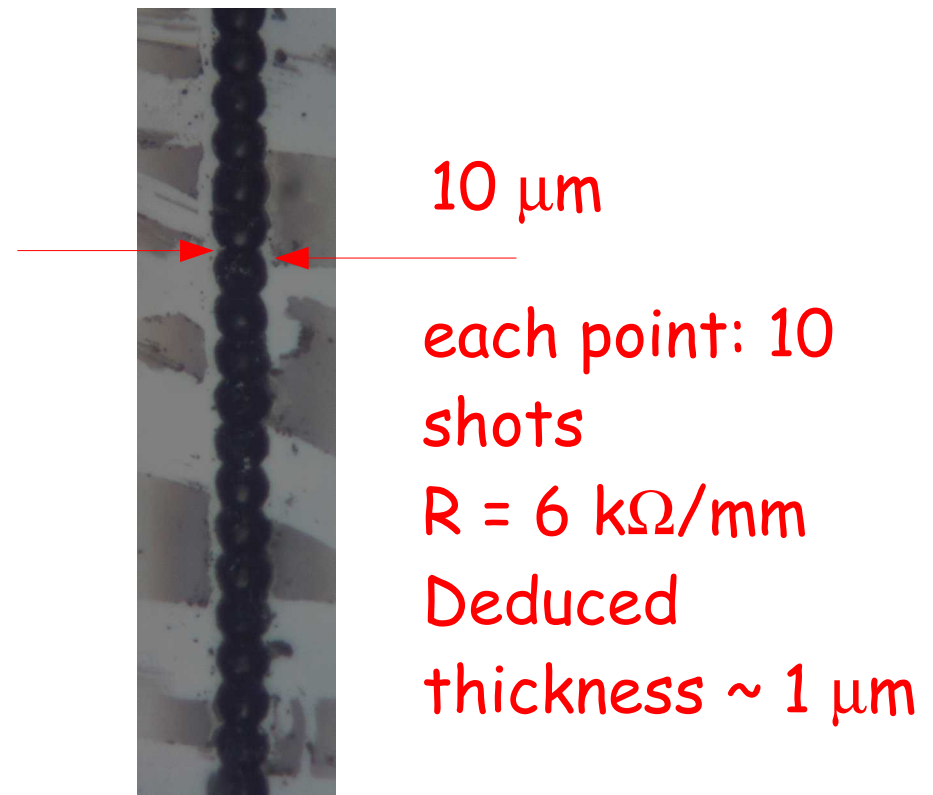


200  $\mu\text{m}$  pitch 4 mm wide  
serpentine (ns laser)

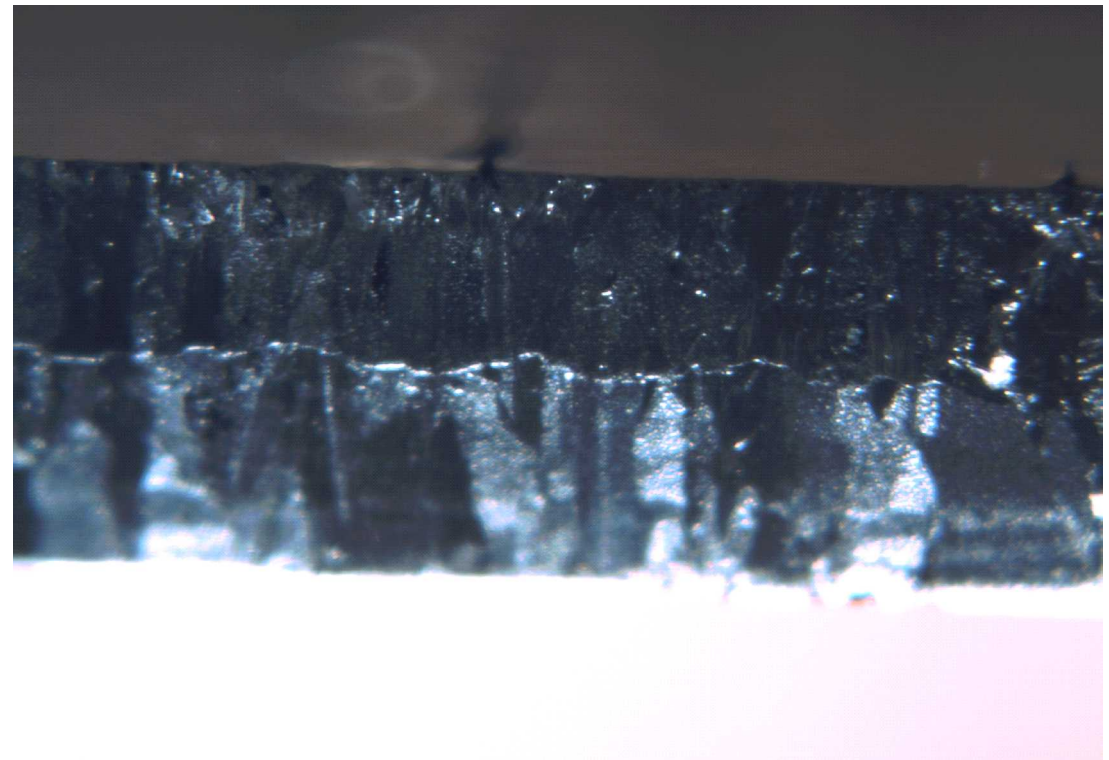
The superficial channels are graphitic as assessed by Raman spectroscopy

$$\rho = 4.5 \text{ m}\Omega \text{ cm}$$

in agreement with the amorphous graphite value



We can modulate thickness (and hence resistance) varying the number of shots per point.



# Nanosecond columns through diamond bulk

9  $\mu\text{J}/\text{pulse}$

1.5 - 5.5  $\mu\text{m}$   
diameter



Higher resistivity

$$\rho = 0.3 - 0.4 \Omega \text{ cm}$$

Only traces of graphite  
signature by graphite  
spectroscopy

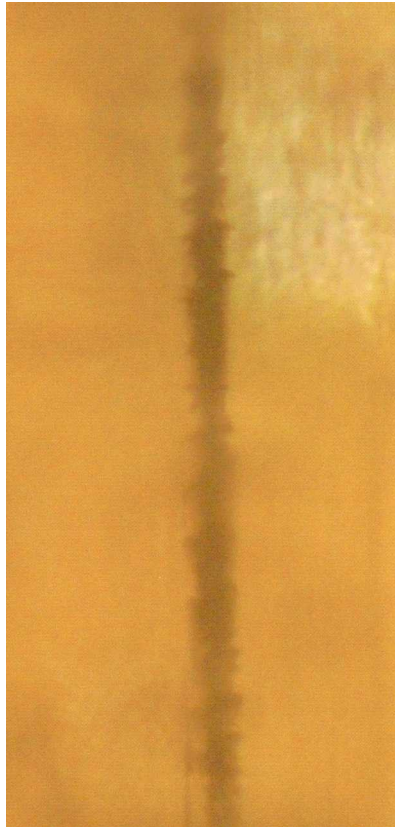
High pressure gradient formed,  
up to 10 GPa

Fractures, non-uniformity

# Femtosecond columns through diamond bulk

1.5  $\mu\text{J}/\text{pulse}$

8-10  $\mu\text{m}$   
diameter



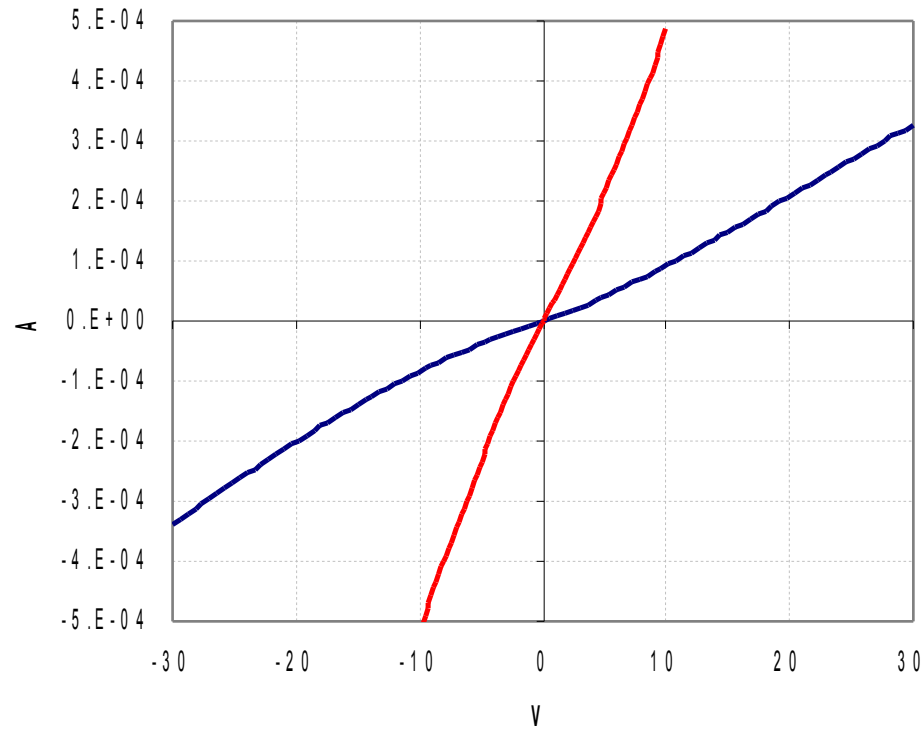
Even higher  
resistivity

$$\rho = 1.3 \ \Omega \ \text{cm}$$

No damage to  
the diamond  
lattice!

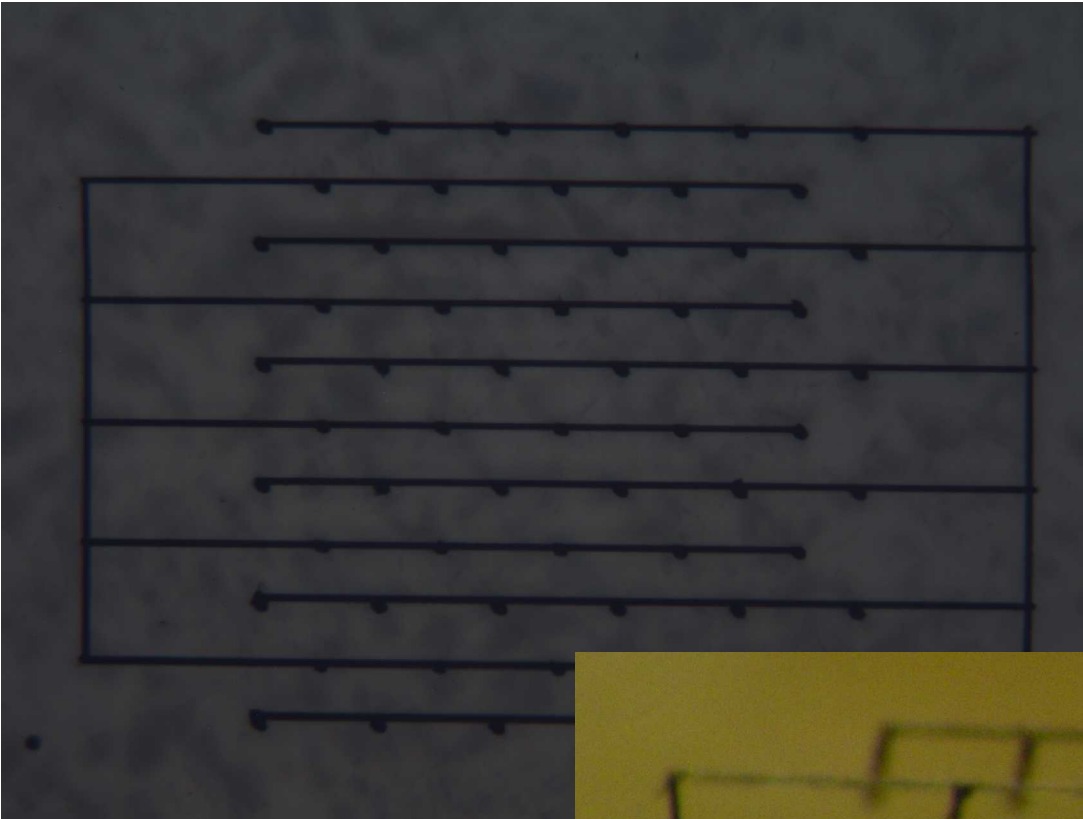
Low residual  
stress!

# Annealing of the bulk nanosecond channels

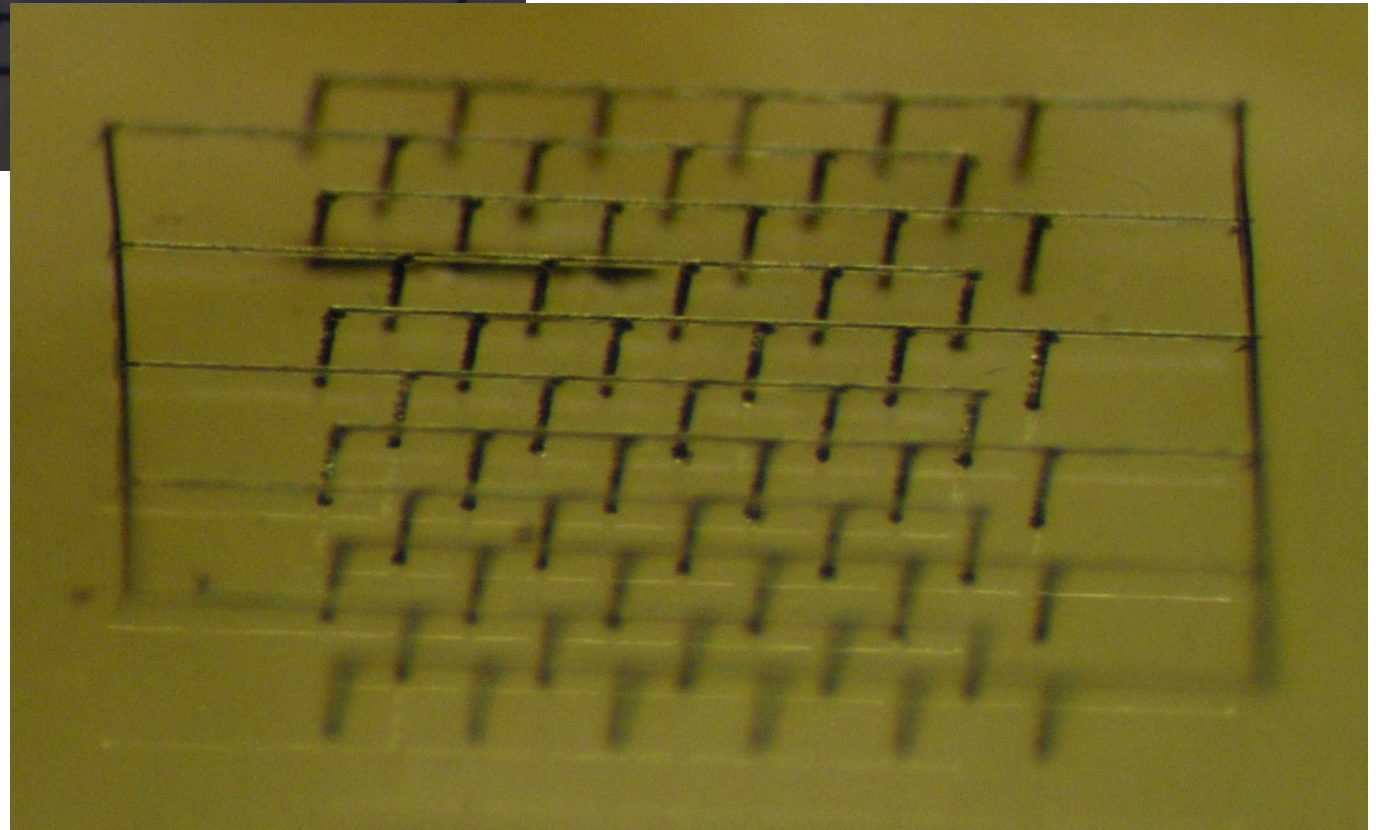


After annealing at 1000 K for 1h under Ar atmosphere ,  
resistance of ns pulses-fabricated columns decrease of a factor  
5

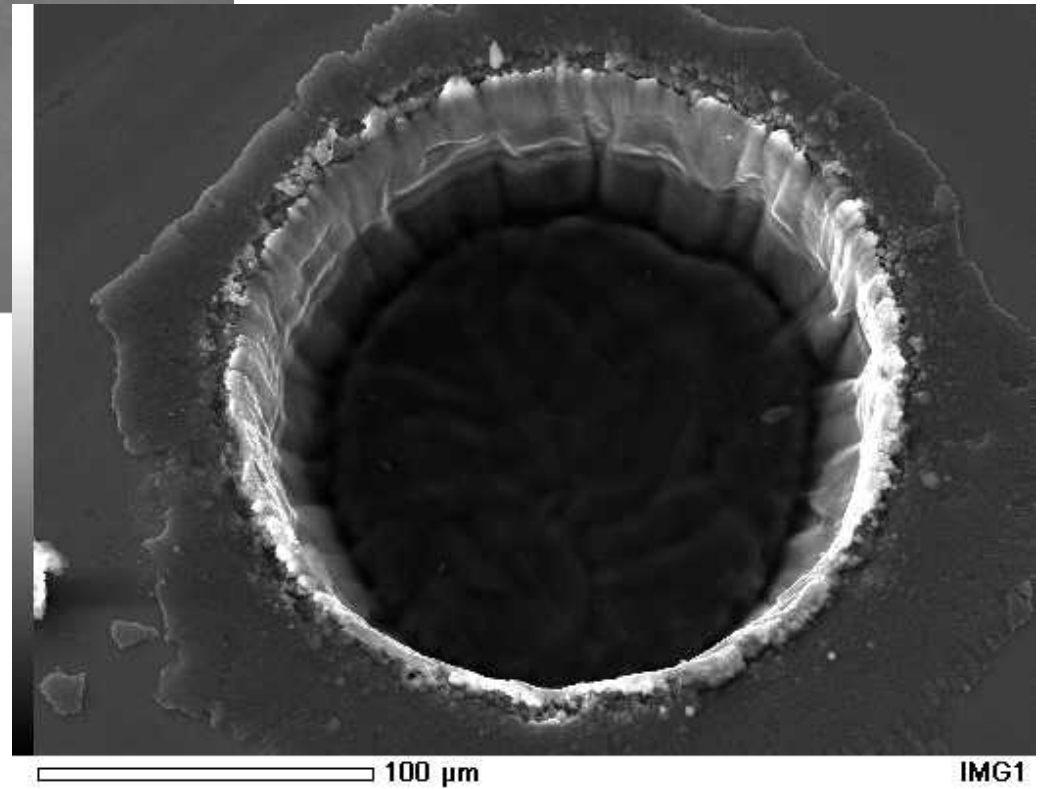
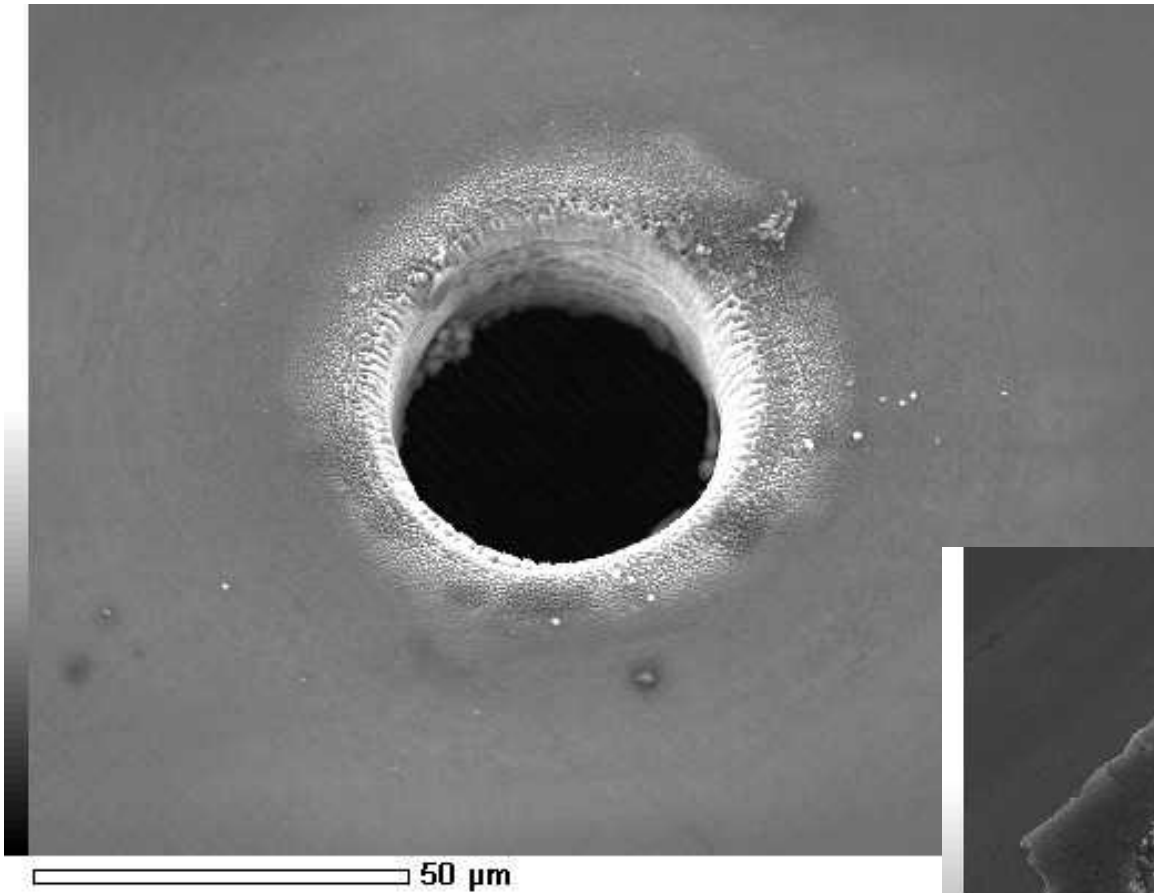
We have to assess possible modifications in the raman spectra  
We have to anneal also fs pulses-fabricated columns



Three-D structure to be tested

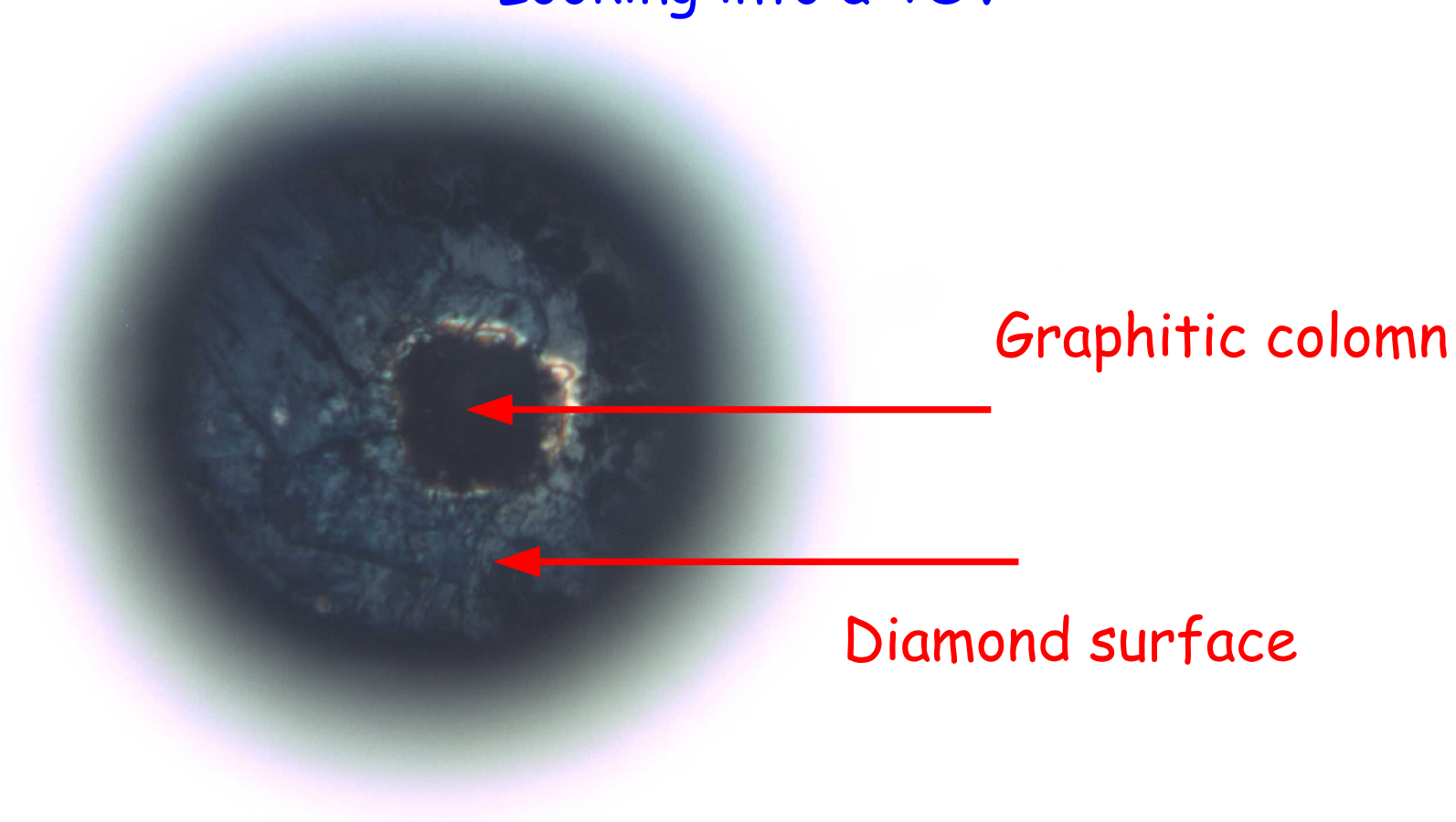


Through Silicon Vias drilled  
in the silicon by excimer  
laser  
(Si typical thickness 50  $\mu\text{m}$ )

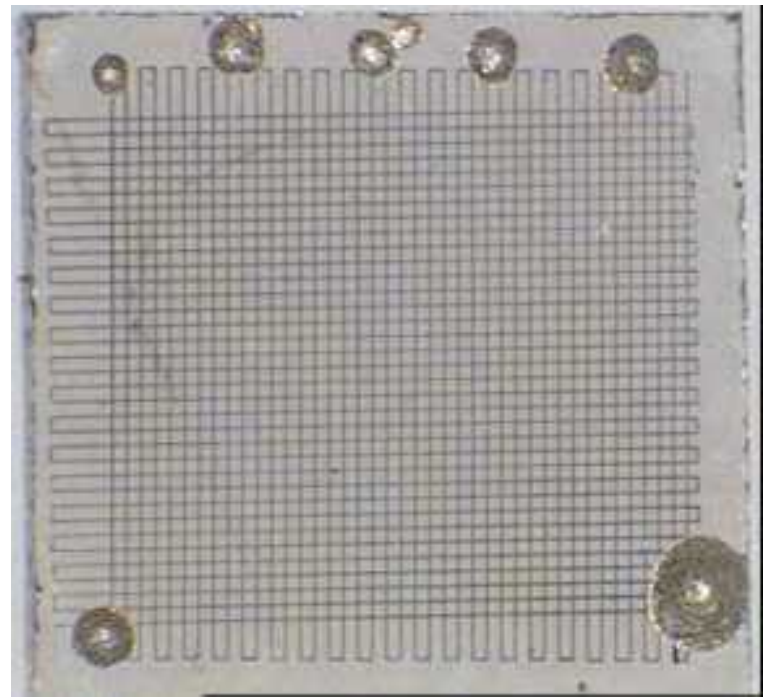
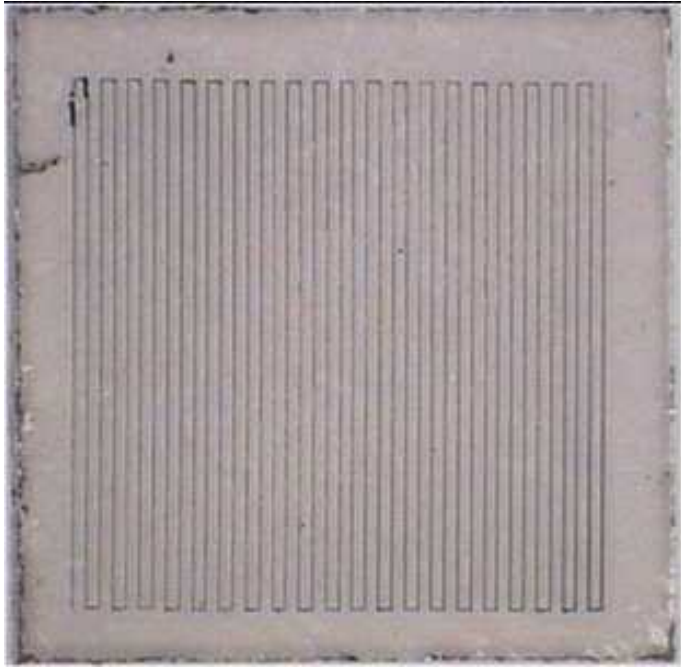


TSV and graphitic channels are presently fabricated on the SOD samples

Looking into a TSV







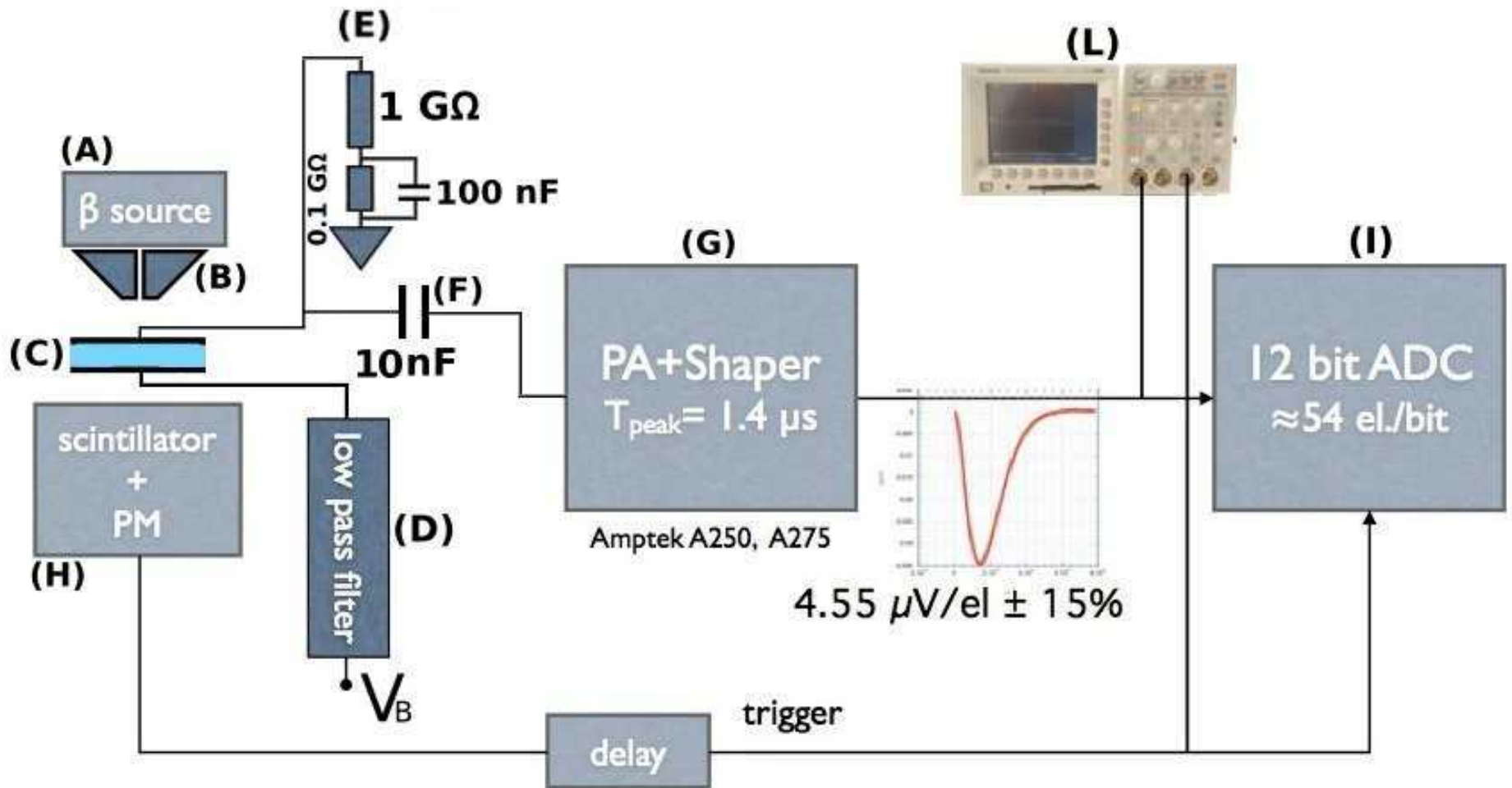
**Diamond detector with  
graphite contacts**



# Detector Characterization

## Charge Collection Efficiency Measurement

(A) beta source (B) collimator (C) DUT (D) power supply (E) GW load resistor



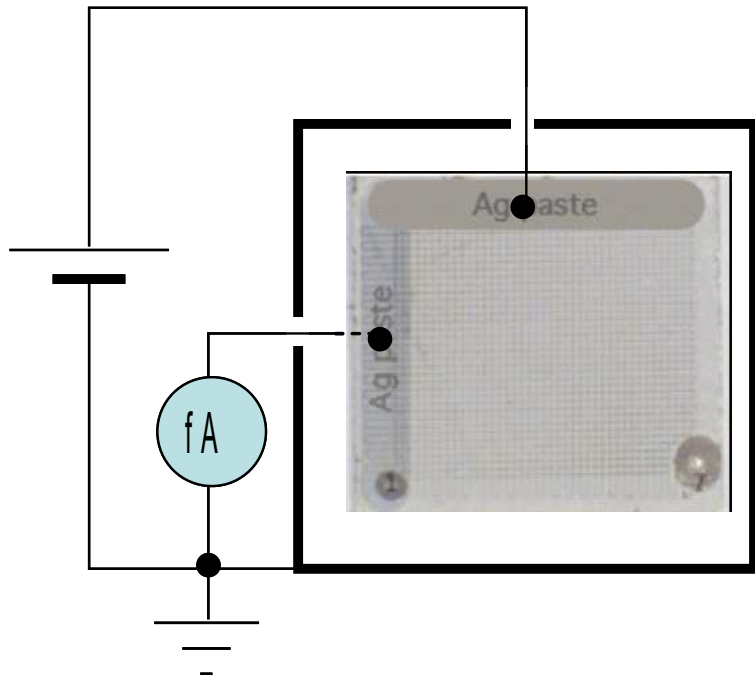
(F) decoupling C (G) Amptek shaper (H) Trigger (I) PCI NI ADC Board (L) Scope



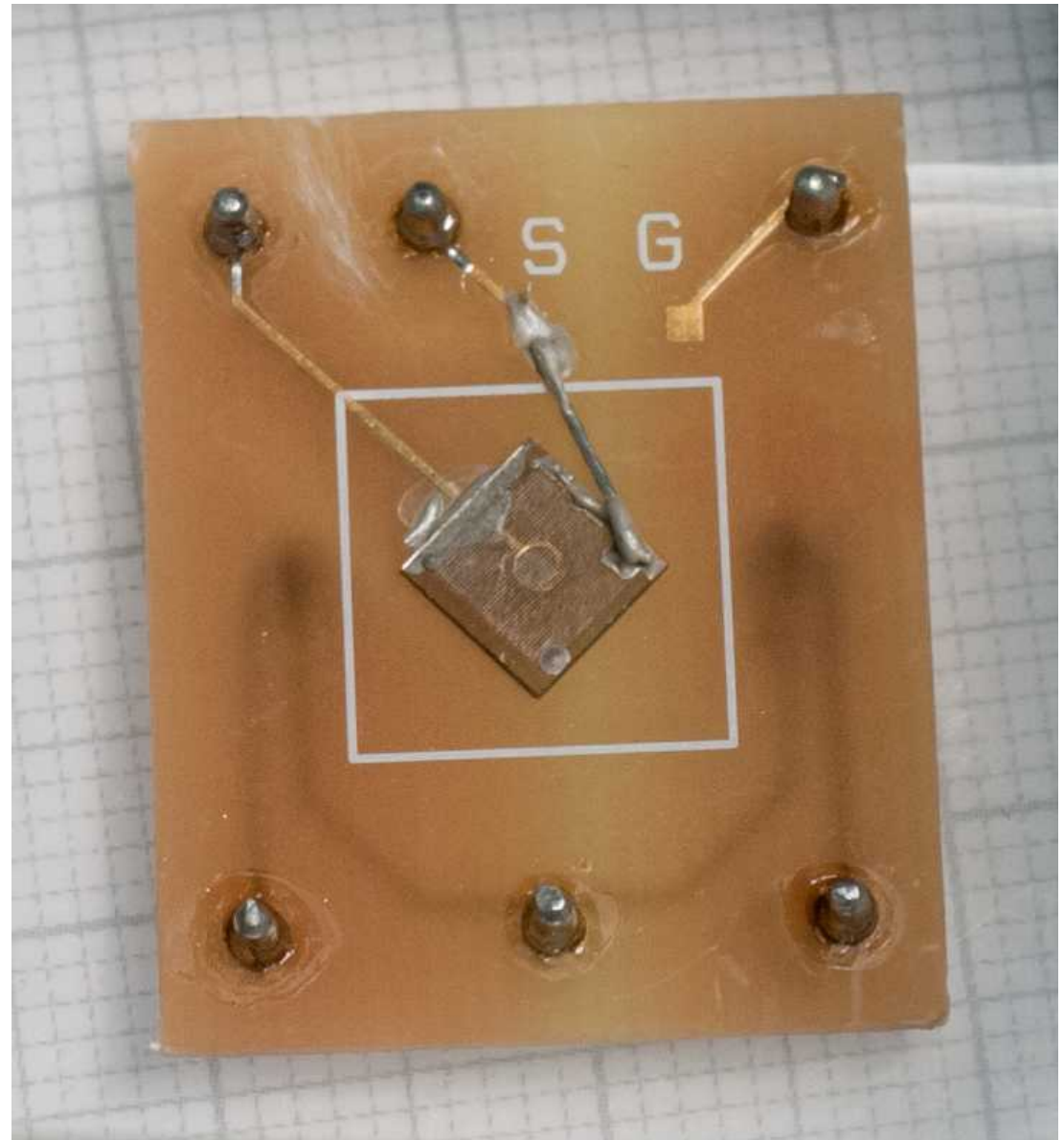




$$C = 2.4 \text{ pF}$$

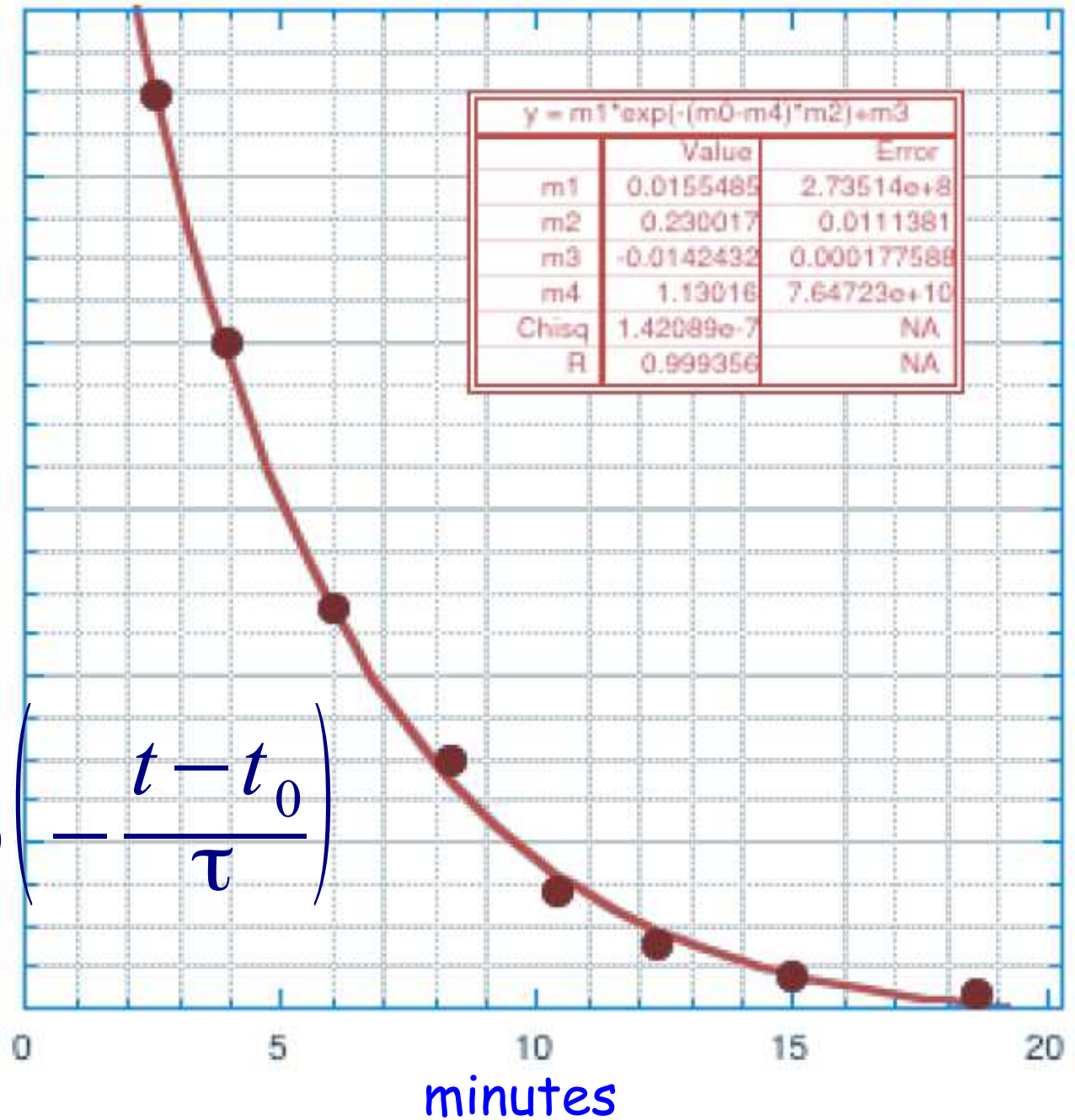


$$R \sim 10^{15} \Omega$$



# Relaxation of the zero bias signal due to polarization

a. u.



$$Q_{av} = A + B \exp\left(-\frac{t - t_0}{\tau}\right)$$

## Charge Collection Efficiency vs. Mean Free Path

Mean Free Path (for each carrier)

$$\lambda = v \tau$$

Drift Velocity

$$v = \frac{\mu E}{1 + \mu E / v_{\text{sat}}}$$

For an homogeneous, i. e., single crystal diamond

$$\frac{Q_{av}}{Q_{gen}} = \frac{\lambda}{L} \left[ 1 - \frac{\lambda}{L} \left( 1 - e^{-\frac{L}{\lambda}} \right) \right] = z \left[ 1 - z \left( 1 - e^{-\frac{1}{z}} \right) \right]$$

$L$  is the sample thickness,  $z = \frac{\lambda}{L}$



## Diamond with MFP linearly increasing with thickness

$$\lambda = ax + \lambda_{min}, \quad a = \frac{\lambda_{max} - \lambda_{min}}{L}$$

$$\frac{Q_{av}}{Q_{gen}} = \frac{1}{\zeta^2} \frac{B}{2 \cdot B - 1} \left\{ \zeta - \frac{1}{2} \zeta^2 B \left[ (1 + \zeta)^{\frac{1}{B}} - 1 \right] \right\}$$

$$\text{with: } z = a \frac{L}{\lambda_{max}} = \frac{\lambda_{max} - \lambda_{min}}{\lambda_{max}}, \quad B = \frac{a}{a - 1}$$

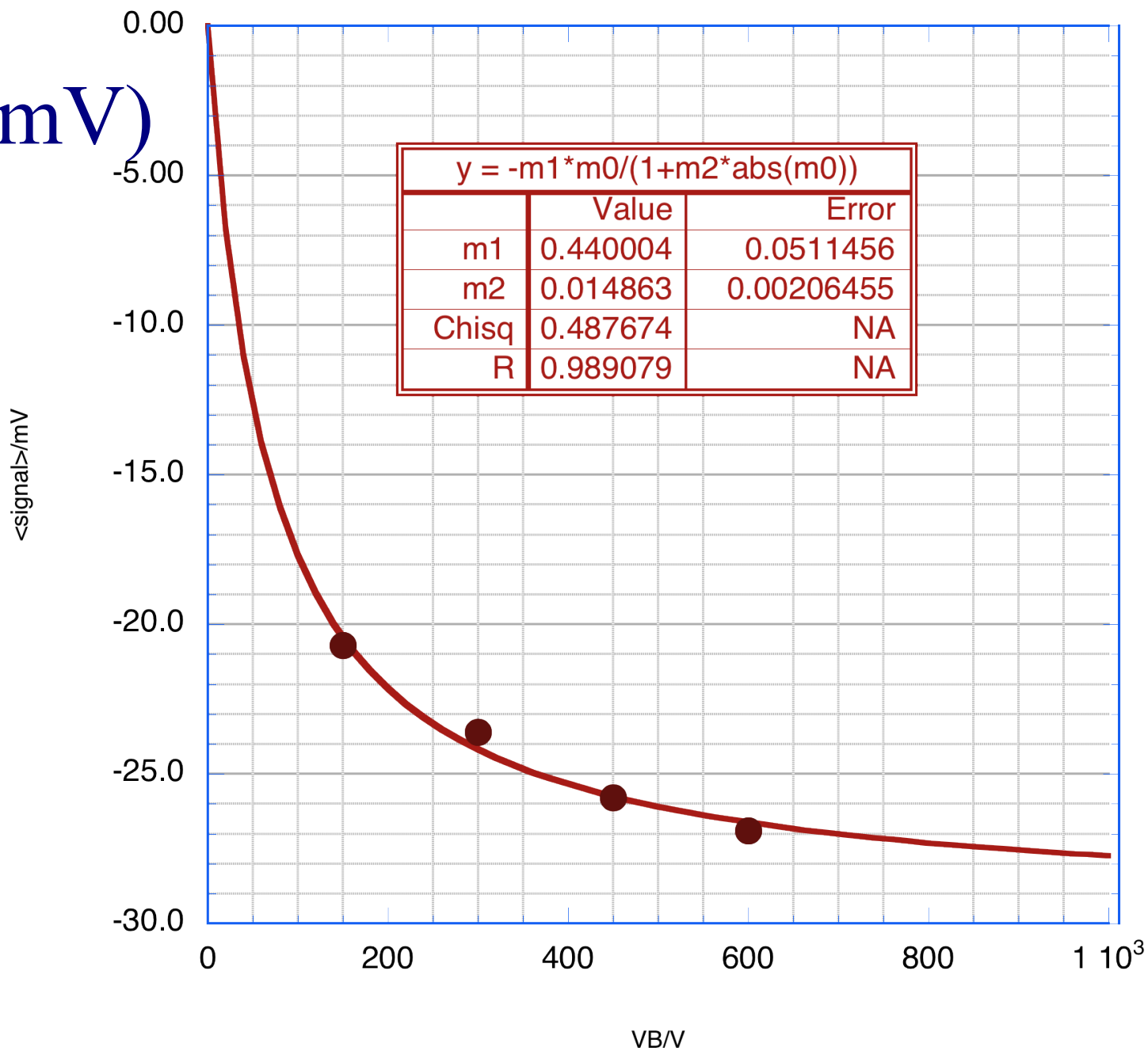
Numerical approximation within a percent:

$$\frac{Q_{av}}{Q_{gen}} = \frac{1}{2} \frac{\frac{\lambda_{max}}{L}}{\frac{\lambda_{max}}{L} + m \left( \frac{\lambda_{min}}{\lambda_{max}} \right)}$$

$$m = 0.36 - 0.4 \text{ for: } \frac{\lambda_{min}}{\lambda_{max}} = 1 - 0.1$$

$Q_{av}$  (mV)

● — <signal>/mV **segnali beta**



Comparison between graphite and standard (Ti-Au) contacts

Agreement in the measurements considering that the graphite contacted sample has a slightly lower thickness



DET13 cfr 2EL100

