SVT - TDR

Friday, 27 January 2012

<u>Update on TDR sections with focus on open issues</u> (08:30 - 12:00)

time	[id] title	presenter
08:30	[5] Introduction & Background summary	RIZZO, Giuliana
08:45	[6] Sensors	BOSISIO, Luciano
09:05	[4] Fanouts Layer1-5	PREST, Michela
09:25	[7] FE chips	RE, Valerio
09:55	[1] Fanout Layer0, HDI, Transition cards and related issues	CITTERIO, Mauro
10:30	[9] Coffee break	
11:00	[8] DAQ and related issues	VILLA, Mauro
11:30	[0] Mechanics	BOSI, Filippo