

**SVT - TDR**

# **Report of Contributions**

Contribution ID: 0

Type: **not specified**

## Mechanics

*Friday, 27 January 2012 11:30 (30 minutes)*

**Presenter:** BOSI, Filippo (PI)

**Session Classification:** Update on TDR sections with focus on open issues

Contribution ID: 1

Type: **not specified**

## **Fanout Layer0, HDI, Transition cards and related issues**

*Friday, 27 January 2012 09:55 (35 minutes)*

**Presenter:** CITTERIO, Mauro (MI)

**Session Classification:** Update on TDR sections with focus on open issues

Contribution ID: 4

Type: **not specified**

## Fanouts Layer1-5

*Friday, 27 January 2012 09:05 (20 minutes)*

**Presenter:** PREST, Michela (MI)

**Session Classification:** Update on TDR sections with focus on open issues

Contribution ID: 5

Type: **not specified**

## Introduction & Background summary

*Friday, 27 January 2012 08:30 (15 minutes)*

**Presenter:** RIZZO, Giuliana (PI)

**Session Classification:** Update on TDR sections with focus on open issues

Contribution ID: 6

Type: **not specified**

## Sensors

*Friday, 27 January 2012 08:45 (20 minutes)*

**Presenter:** BOSISIO, Luciano (TS)

**Session Classification:** Update on TDR sections with focus on open issues

Contribution ID: 7

Type: **not specified**

## FE chips

*Friday, 27 January 2012 09:25 (30 minutes)*

**Presenter:** RE, Valerio (PV)

**Session Classification:** Update on TDR sections with focus on open issues

Contribution ID: 8

Type: **not specified**

## DAQ and related issues

*Friday, 27 January 2012 11:00 (30 minutes)*

**Presenter:** VILLA, Mauro (BO)

**Session Classification:** Update on TDR sections with focus on open issues



Contribution ID: 9

Type: **not specified**

## Coffee break

*Friday, 27 January 2012 10:30 (30 minutes)*

**Session Classification:** Update on TDR sections with focus on open issues