Workshop on Electronics for Physics Experiments and Applications @INFN

Thursday, 6 March 2025

Integration challenges - 2: DAQ/Data Transmission/Scalability to large area and multi channels/ sensor-ASIC connection/ packaging/ ASIC/ FPGA data processing - Aula Magna Lingotto (11:05 - 13:00)

time	[id] title	presenter
11:05	[16] AI Engine Technology in AMD Devices	BAGNI, Daniele GUASTI, Giovanni
11:30	[17] High speed serial links and radiation tolerant serdes devices for TTC distribution and DAQ in future HEP experiments	MAGAZZU', Guido
11:55	[18] ARCADIA FDMAPS development with LFoundry 110 nm CIS	MANDURRINO, Marco
12:20	[19] Roundtable Discussion: Q&A and Insights	