

Workshop on Electronics for physics experiments and applications @INFN

Thursday, 6 March 2025

**Integration challenges - 1: DAQ/Data Transmission/Scalability to large area and multi channels/ sensor-ASIC connection/
packaging/ ASIC/ FPGA data processing - Aula Magna Lingotto (08:50 - 10:30)**

time	[id] title	presenter
08:50	[38] The RETINA project: from R&D to integration in the DAQ of LHCb	LAZZARI, Federico
09:15	[13] The challenges of large ASIC integration and verification: the RD53 experience	LODDO, Flavio
09:40	[14] FEROCCE and the journey of data from the detector to the computing farm	TRIOSSI, Andrea
10:05	[15] APE Router: an IP enabling low-latency packet communications for FPGA-based distributed processing.	LO CICERO, Francesca