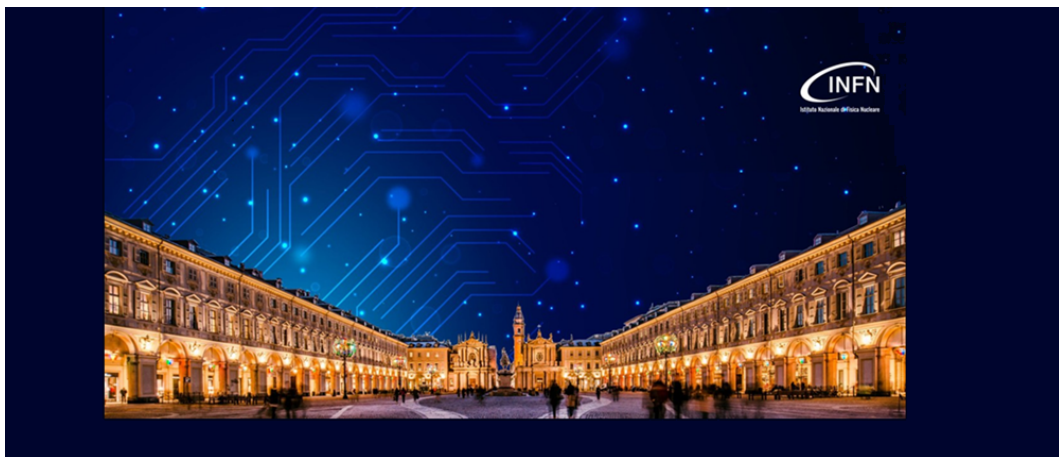


Session Program

Mar 5 - 7, 2025



Workshop on Electronics for Physics Experiments and Applications @INFN

Integration challenges - 1

Torino, Aula Magna Lingotto
Via Nizza 242, Torino

Thu, March 6

8:55 AM

Integration challenges - 1: DAQ/Data Transmission/Scalability to large area and multi channels/ sensor-ASIC connection/ packaging/ ASIC/ FPGA data processing

Session | Location: Torino, Aula Magna Lingotto, Via Nizza 242, Torino

8:55 - 9:20 AM

The challenges of large ASIC integration and verification: the RD53 experience

Speaker

Flavio Loddo

9:20 - 9:45 AM

FEROCE and the journey of data from the detector to the computing farm

Speaker

Andrea Triossi

9:45 - 10:10 AM

APE Router: an IP enabling low-latency packet communications for FPGA-based distributed processing.

Speaker

Francesca Lo Cicero

10:10 - 10:35 AM

The RETINA project: from R&D to integration in the DAQ of LHCb

Speaker

Federico Lazzari

10:35 AM