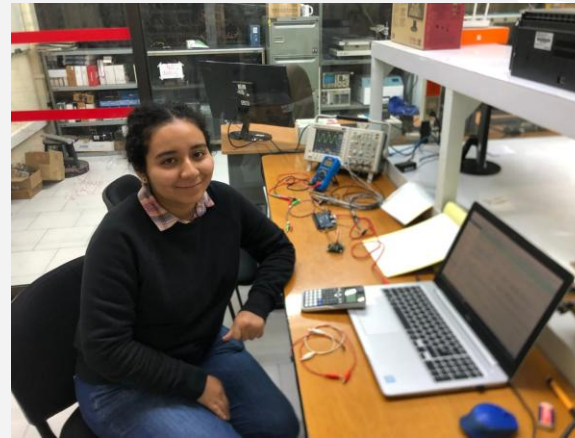# Introduce myself

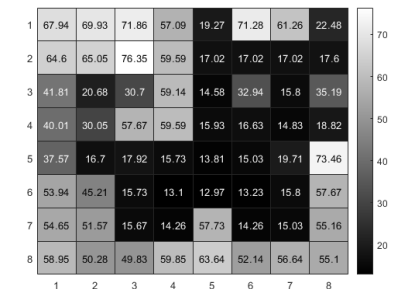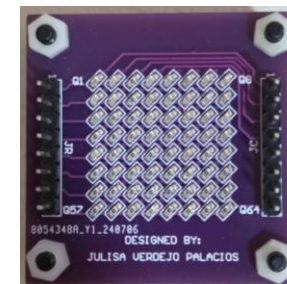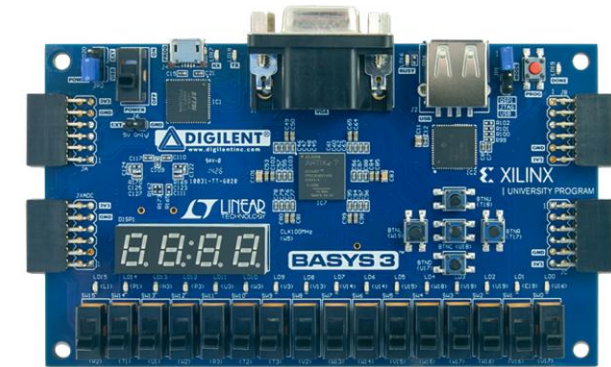Julisa Verdejo Palacios (Julie)

Previous work

- **Educational background**
  - Bachelor's degree in electronics (BUAP)
  - Master's degree in electronic instrumentation (INAOE)

- **Previous research:**

Design of a data acquisition system to capture images using light sensors, A/D and D/A converters and multiplexors, coordinated by an FPGA.
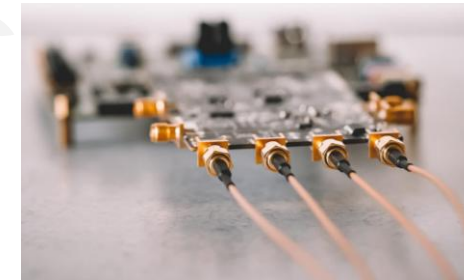
Current position

- **Hosting institution:** INFN Sezione di Bari

- **Tutor:** Giuseppe De Robertis

- **Description:**

The research focuses on the design and implementation of an FPGA-based test system for particle detectors, featuring multiple high-speed serial interfaces. The project includes developing readout and control architectures, as well as interface circuits, to create a scalable system for laboratory measurements and beam tests.

Waveform digitizer

MOSAIC board