



Solid states and gaseous tracking detectors

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Detector concepts

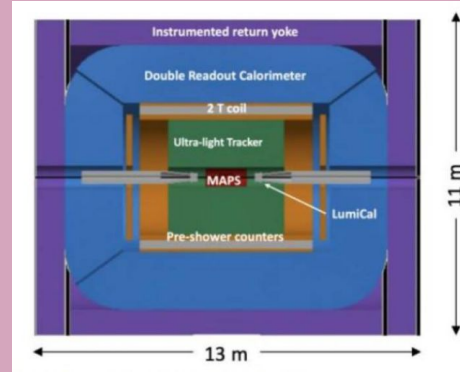
Solid-state devices are the choice for vertexing.

Solid-state and **gaseous detectors** are proposed for the tracking systems:

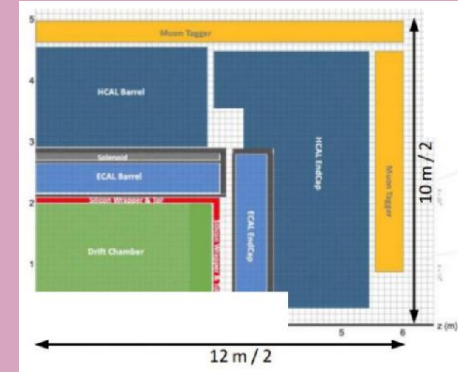
- **IDEA**, **ALLEGRO** and **ILD** are considering gaseous detectors
- **CLD** and **MuCo1** are inheriting the CLIC design with a solid-state approach

Gas detectors are mainly proposed for the muon system.

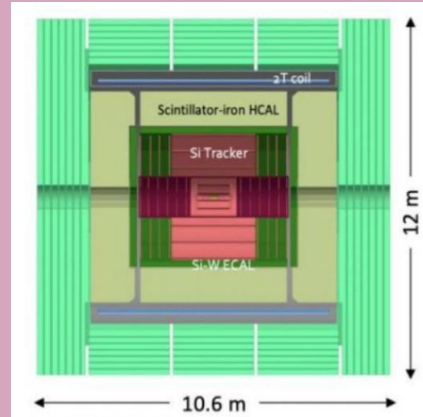
IDEA



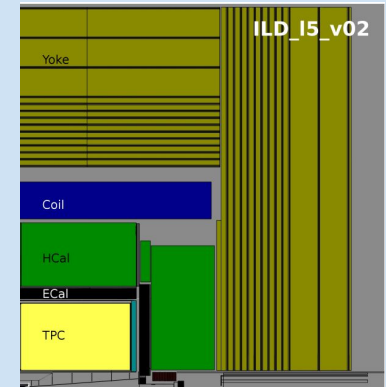
ALLEGRO



CLD



ILD



Detector concepts

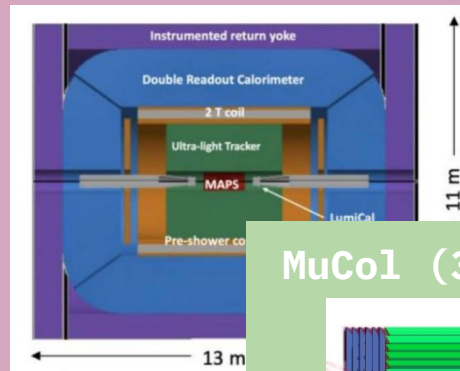
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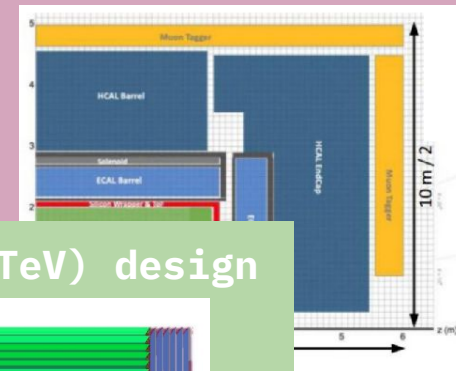
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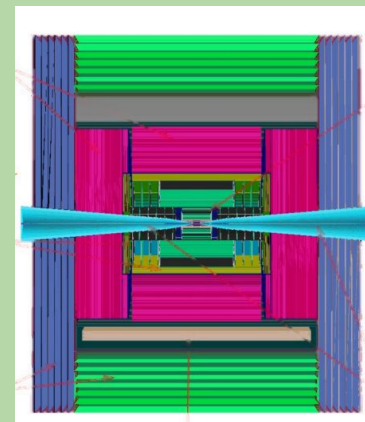
IDEA



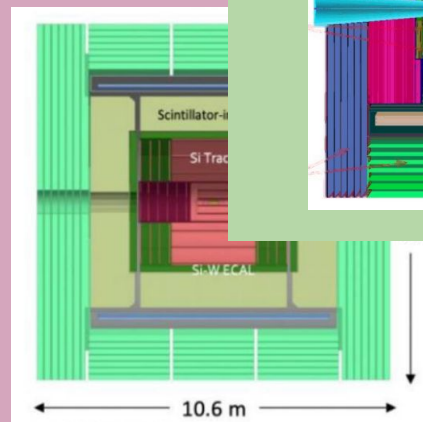
ALLEGRO



MuCo1 (3TeV) design



CLD



ILD_I5_v02



List of technologies/application



Solid-state detectors

Vertexing

Tracking

TOF



-> FD-MAPS, HV-CMOS, LGAD, RSD, bended

Gaseous detectors

Tracking

Muon system

TOF

Calorimetry

-> Drift chamber, TPC, Straw tubes

-> μ RWELL, RPC, Drift tube, Micromegas

-> Picosec, MRPC

-> MPGD

List of technologies/application



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-> MPGD

Requirements

Higgs boson tagging and BR into invisibles sets requirements on:

- Tracking and vertexing performance
- Material in the tracking volume.
- Magnetic field (and thickness of solenoid)

Transparency against multiple scattering and high precision momentum measurement play an important role.

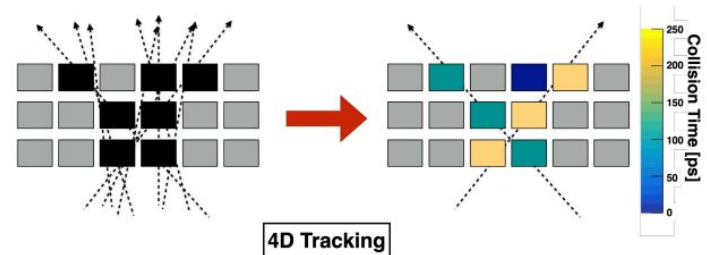
Particle identification interplay needed together with the tracking system.

High background level requires 4D tracking (MuCol) to remove combinatorial track reconstruction problem.

	Critical detector	Requirement
$ZH \rightarrow \ell^+ \ell^- X$	Tracker	$\frac{\sigma(p_T)}{p_T^2} \sim \frac{0.1\%}{p_T} \oplus 2 \cdot 10^{-5}$
$H \rightarrow b\bar{b}, c\bar{c}$	Vertex	$\sigma_{r_\phi} \sim 5 \oplus 15(p \sin \theta^2)^{-1} [\mu\text{m}]$

$$\left. \frac{\Delta p_T}{p_T} \right|_{res.} \approx \frac{12 \sigma_{r_\phi} p_T}{0.3 B_0 L_0^2} \sqrt{\frac{5}{N+5}}$$

$$\left. \frac{\Delta p_T}{p_T} \right|_{m.s.} \approx \frac{0.0136 \text{ GeV}/c}{0.3 \beta B_0 L_0} \sqrt{\frac{d_{tot}}{X_0 \sin \theta}}$$

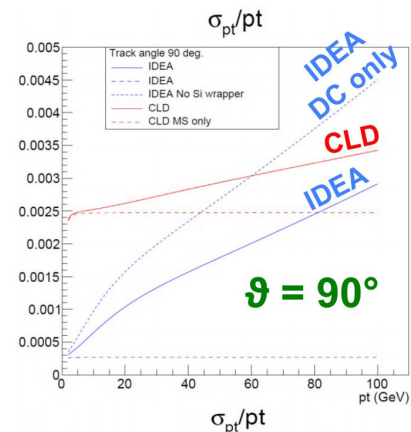
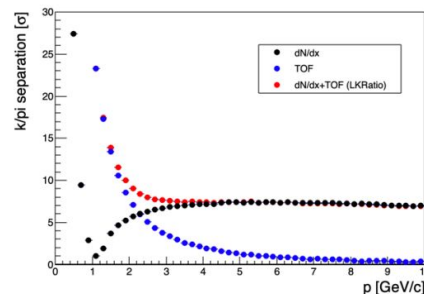
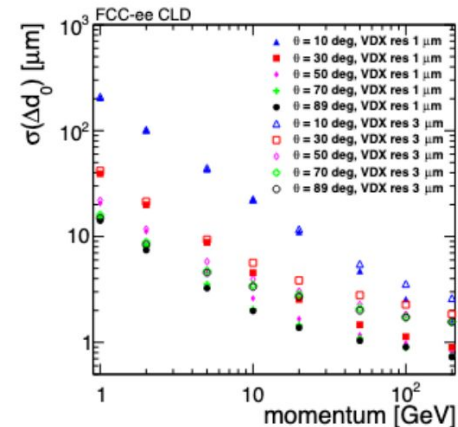
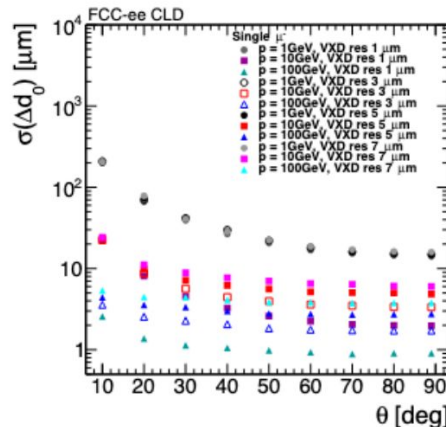


Silicon detectors are the unique choice for the vertex detector to fulfil the requirements.

Material budget plays an important role but R&D is ongoing to reduce its impact.

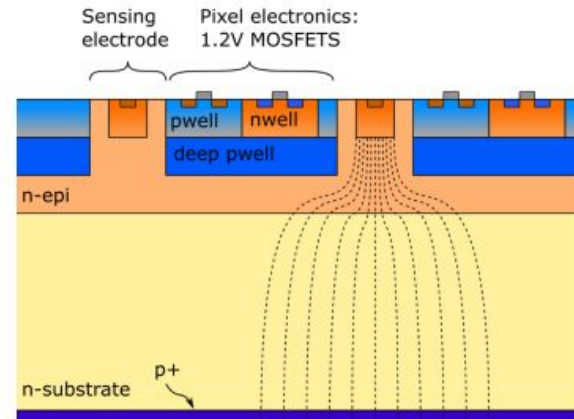
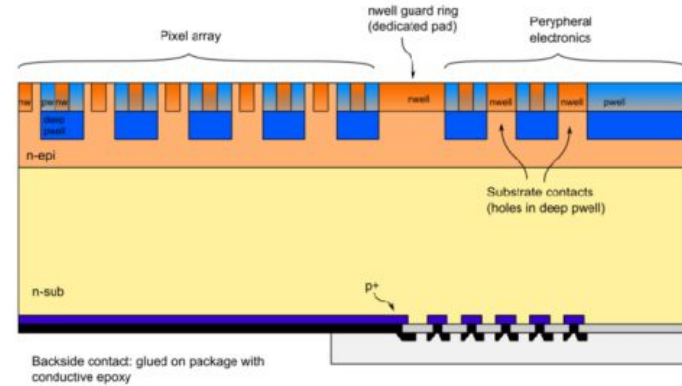
Timing performance for TOF measurement enhance the PID where dE/dx and dN/dx fail

I did a personal selection on the technology discussed in the next slides: FD-MAPS, LGAD, HV-CMOS, AC-LGAD, bended.



Advanced Readout CMOS Architectures with Depleted Integrated sensor Array
Fully Depleted Monolithic Active Pixel CMOS sensor technology platform allowing for:

- Active sensor **thickness** in the range 50 μm to 500 μm
- Operation in **full depletion** with fast charge collection by drift
- Small collecting electrode for optimal signal-to-noise ratio
- **Scalable readout** architecture with ultra-low **power capability** $O(10\text{mW}/\text{cm}^2)$
- Compatibility with standard CMOS fabrication processes
- Technology: **LF11is 110nm CMOS**



Matrix

- 512x512 pixels, Double Column arrangement
- **25x25 μm^2 pixels**
- Clockless

End of Sector

(x16) Reads and Configures 512x32 pixels

Sector Biasing

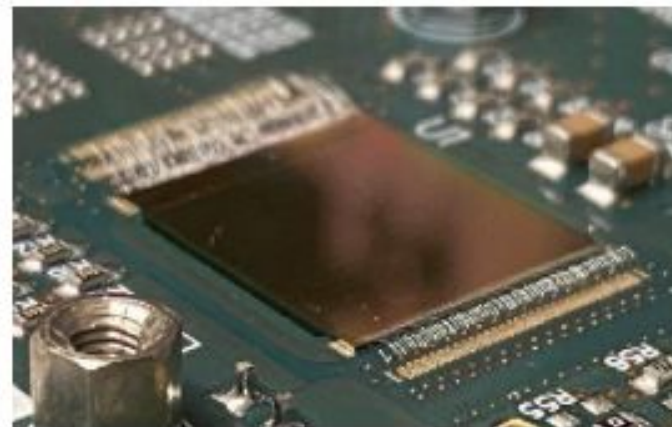
(x16) Generates I/V biases for 512x32 pixels

Periphery

- SPI, Configuration, 8b10b enc, Serializers
- Triggerless data-driven readout
- **Event rate up to 100 MHz/cm²**
- High-rate operation (16 Tx): **17-30 mW/cm²**
- Low-power operation (1 Tx): **10 mW/cm²**

Bottom Pad Frame

Stacked Power and Signal pads



Test beam at FNAL (120 GeV protons)

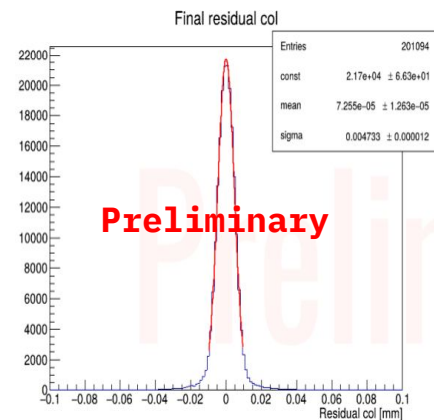
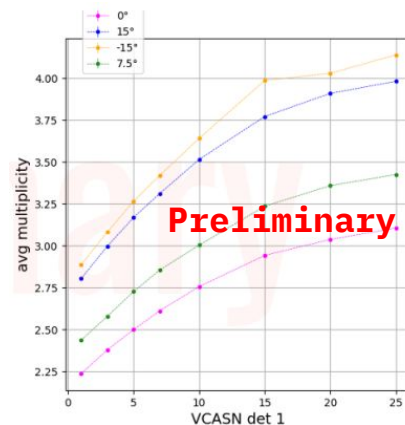
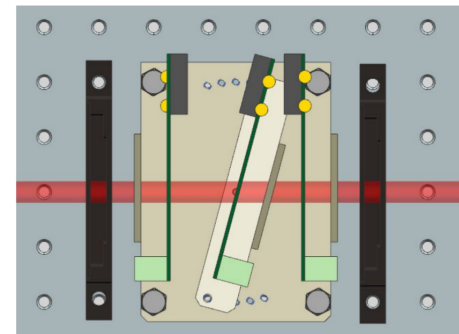
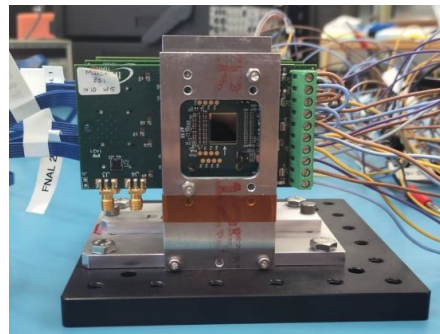
Telescope with 3 ARCADIA-MD3 200 μm thick sensors

Threshold, sensor HV and incidence angle parameterization: study of cluster size, collection efficiency, spatial resolution

Single-point resolution $\sim 4.7 \mu\text{m}$

@VCASN = 5 ($\sim 600 e^-$)
and angle of tilt = 0°

Average efficiency 0.9941 ± 0.0003

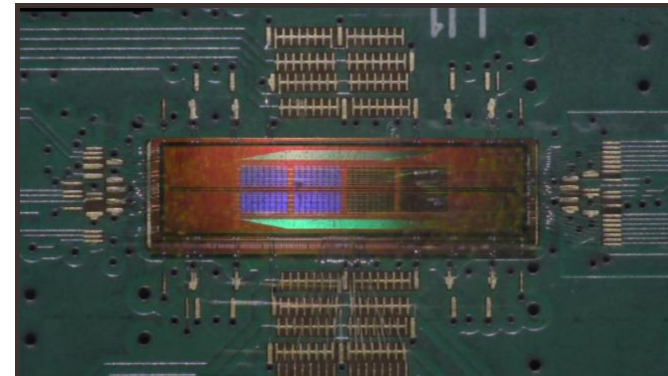
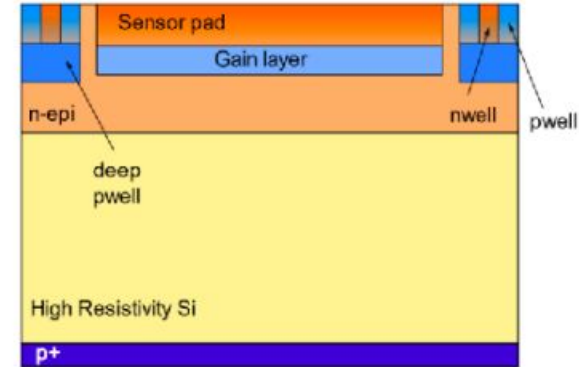


Monolithic CMOS Avalanche Detector **PIX**elated
 Prototype for ps Timing Application

MadPix prototype with gain layer and
 integrated electronics

Advantages of **CMOS LGAD** are a reduced
 material and cost together with a cheaper
 assembly through a monolithic approach

Add-on p-gain implant underneath the n+
 collecting electrode to **push the timing
 performances**



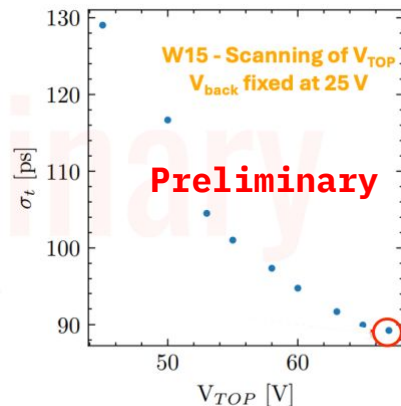
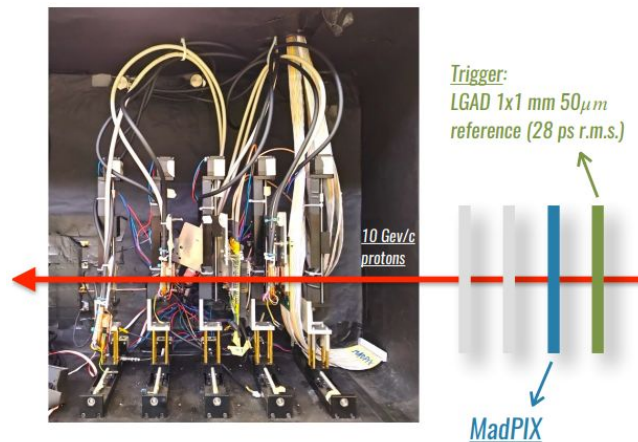
First small-scale demonstrator

- $4 \times 16 \text{ mm}^2$ and $250 \times 100 \text{ }\mu\text{m}^2$ pixel pads
- 8 matrices (64 pixel pads each) implementing different sensor and front-end flavours
- 64 analogue outputs on each side

ALICE3 TOF Test beam @ CERN PS in October 2024. **Timing resolution** measured $< 75 \text{ ps}$ (very preliminary results)

48 μm thick active layer on a p+ substrate, timing resolution is sensor limited

FEE jitter $\sim 20 \text{ ps r.m.s.}$ measured with laser

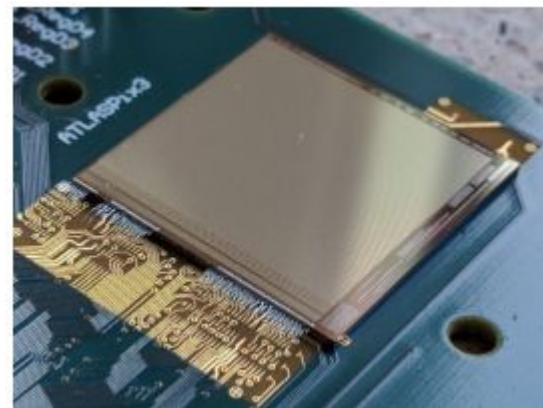
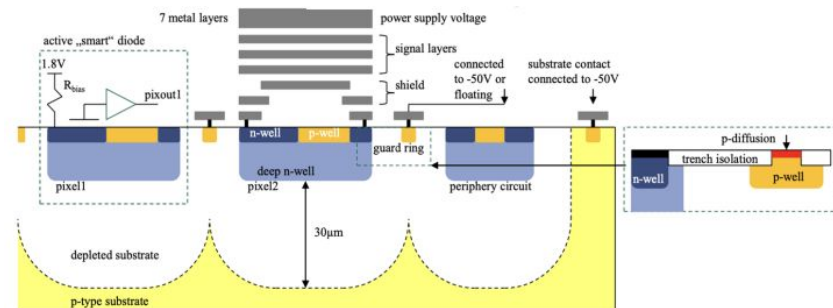


ATLASPix3 was developed for ATLAS ITk, now used widely as HV-CMOS demonstrator.

General features

- TSI 180 nm **HV-CMOS technology**
- full-reticle size $20 \times 21 \text{ mm}^2$ **monolithic pixel sensor**
- 132 columns of 372 pixels
- pixel size $50 \times 150 \mu\text{m}^2$ ($25 \times 150 \mu\text{m}^2$ on recent prototypes)
- **breakdown voltage $\sim -60 \text{ V}$**
- up to 1.28 Gbps downlink
- 25 ns timestamping
- analog pixel matrix, digital processing in periphery
- Threshold can be tuned to 800e, with dispersion $\sim 60\text{e}$, noise $\sim 70\text{e}$

Both triggerless and triggered readout modes

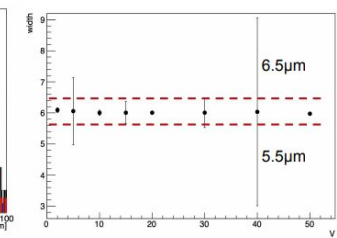
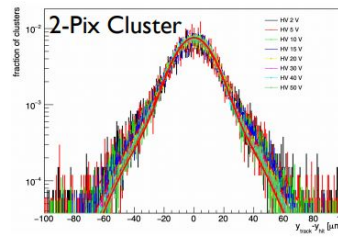
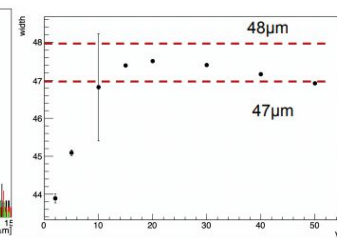
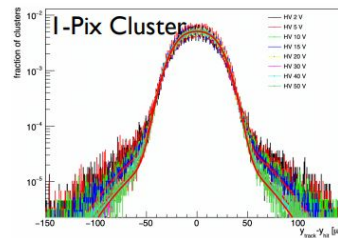
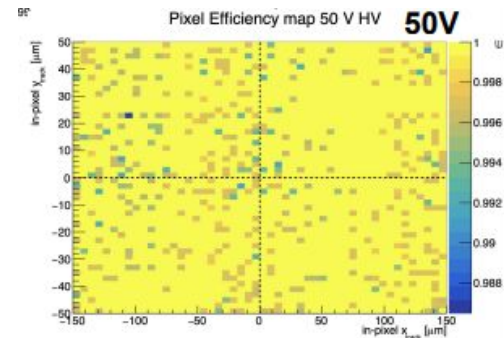
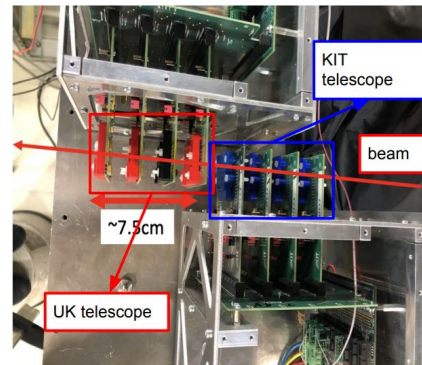


Two telescopes (KIT and UK) in standalone systems tested at DESY with electron beams.

For the presented data analysis a 3-6 GeV electrons beams is used and perpendicular beams.

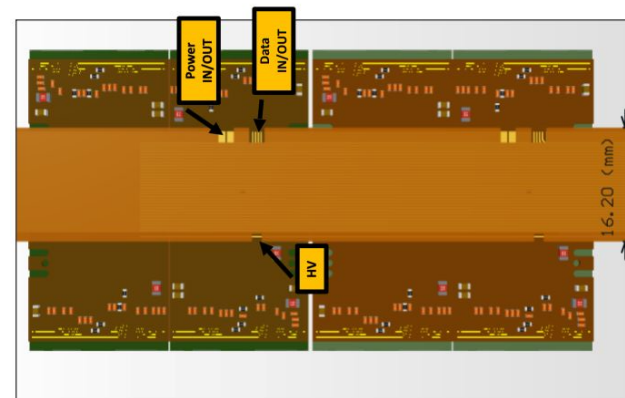
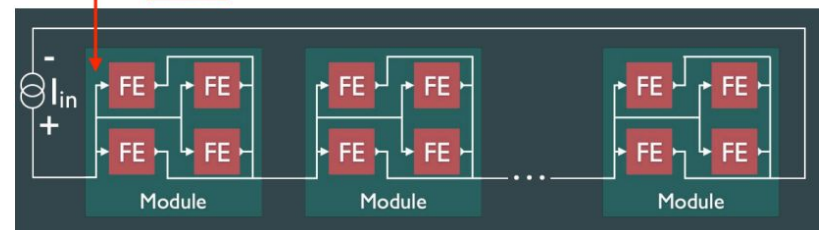
Resolution depends on the cluster size.

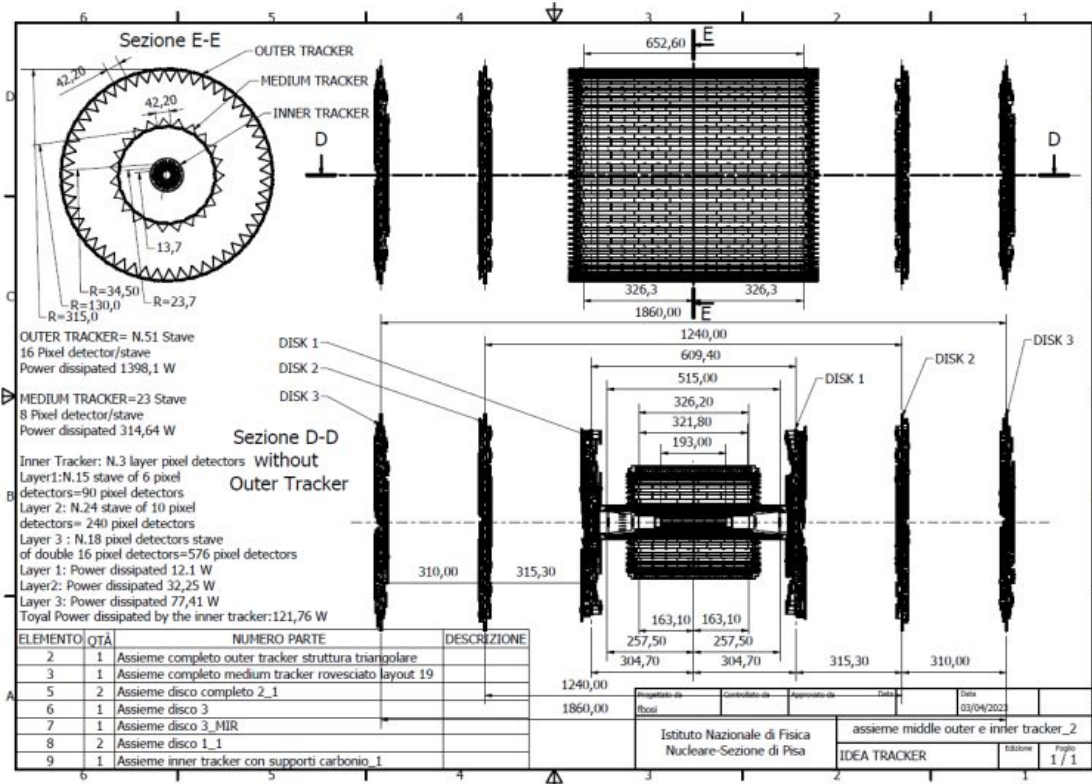
Overall efficiency > 99% achieved after 20V



Multi-chip module assembly

- **aggregates electrical services and connection for multiple sensors**
- quad module, inspired by ATLAS ITk pixels
- implemented interface to readout system
- developed software for module
- **no degradation of performances for quad modules**
- ATLASPix3 has serial powering capabilities
- **SP operation is not affected**





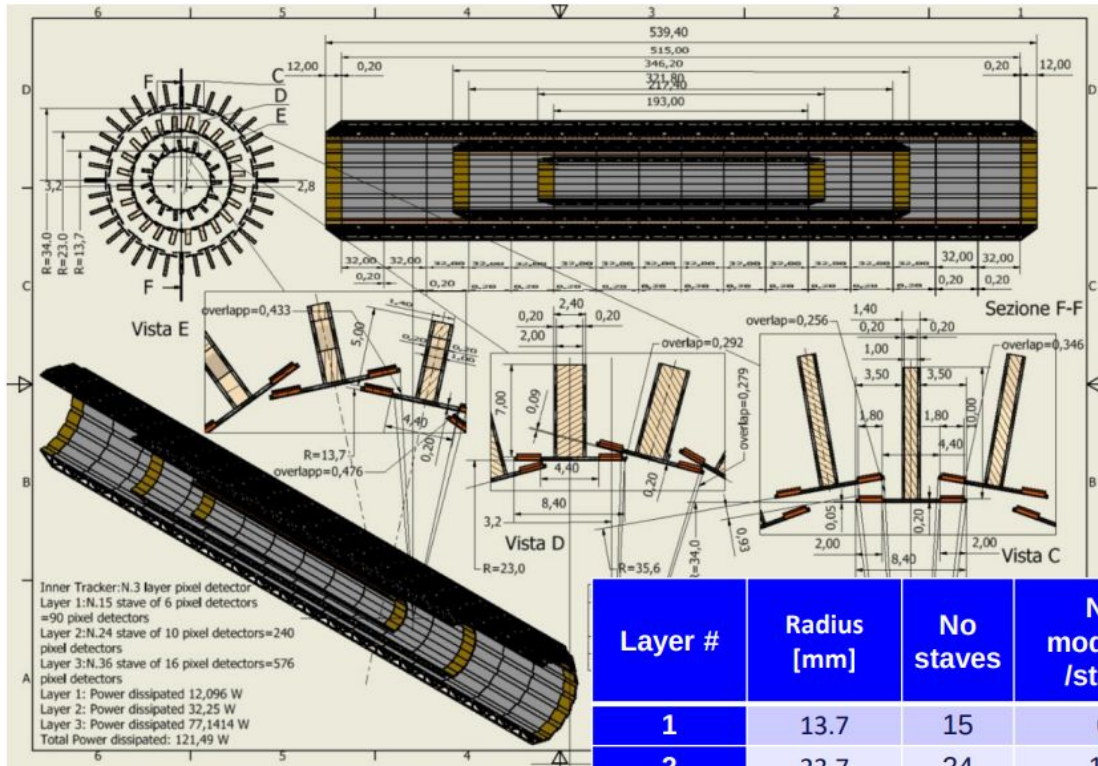
Outer vertex tracker:
ATLASPix3 based

- Modules of $50 \times 150 \mu\text{m}^2$ pixel size
- Intermediate barrel at 13 cm radius
 - Outer barrel at 34.5 cm radius
 - 3 disks per side

Inner Vertex detector:
ARCADIA based

Modules of $25 \times 25 \mu\text{m}^2$ pixel size

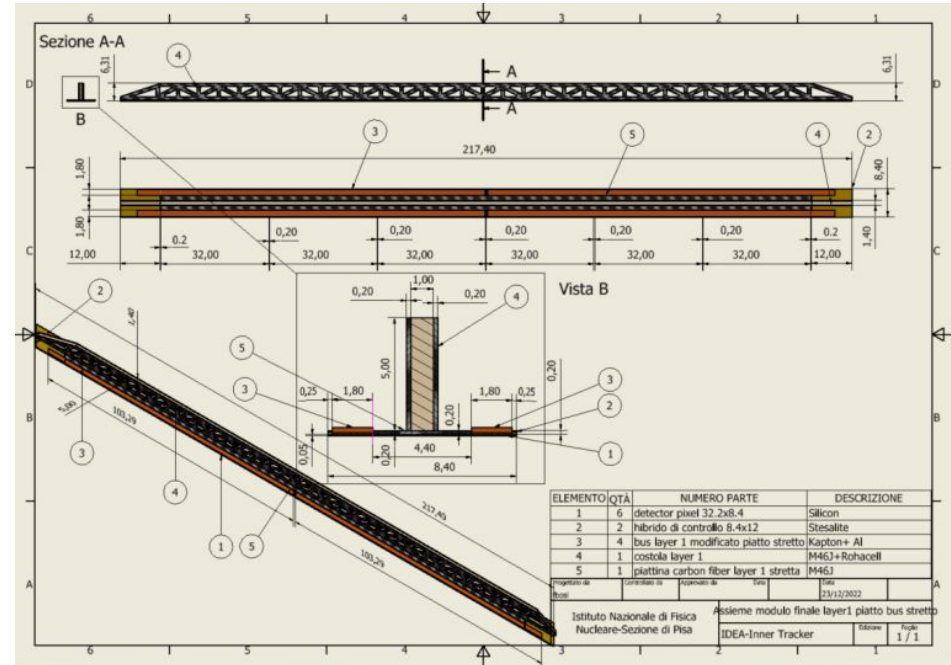
3 barrel layers at
- 13.7, 23.7 and 34/35.6 mm radius



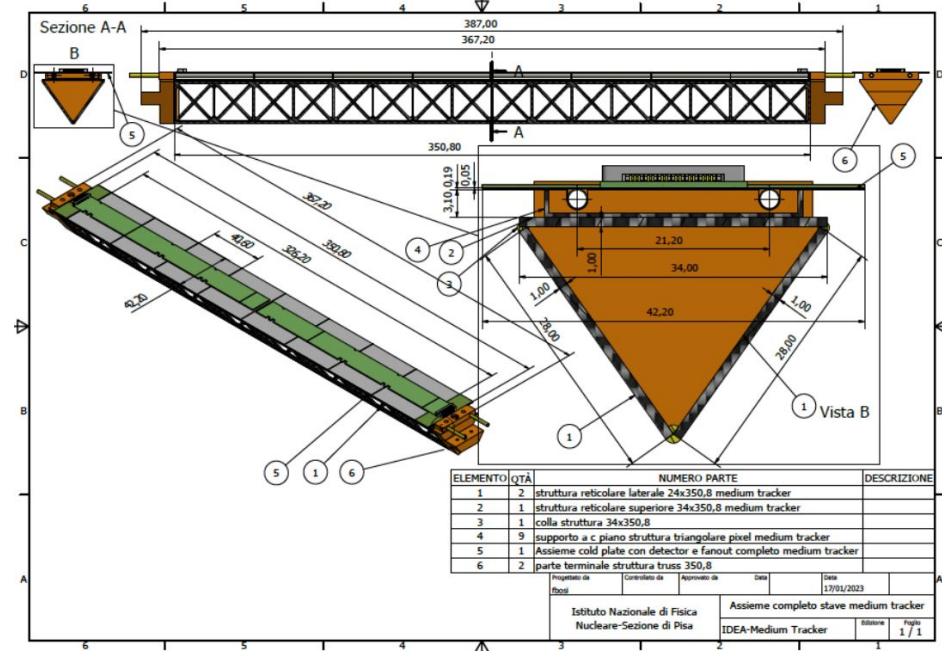
Total thickness per layer 0.25% X/X0
 Carbon Fibre ~60 %
 Silicon: ~20%
 Power and readout bus: ~20%

Layer #	Radius [mm]	No staves	No modules /stave	Total Length [mm]	Active Area [cm ²]	Power [W]
1	13.7	15	6	217.40	241.92	12
2	23.7	24	10	346.20	645.12	32
3	34 & 35.6	36	16	539.40	1548.29	77

- Reticular lightweight support to provide stiffness
- Thin carbon fiber walls interleaved with Rohacell
- 2 buses (data and power) 1.8 mm wide and 250 μm thick (50 μm Al, 200 μm kapton) per side
- Inspired to low mass hybrid R&D Sensors facing interaction point w/o any other material in front
- Readout chips either sides
- Air cooled



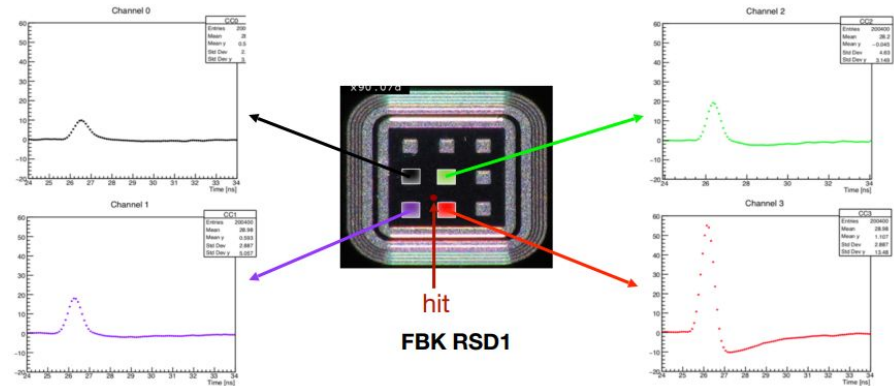
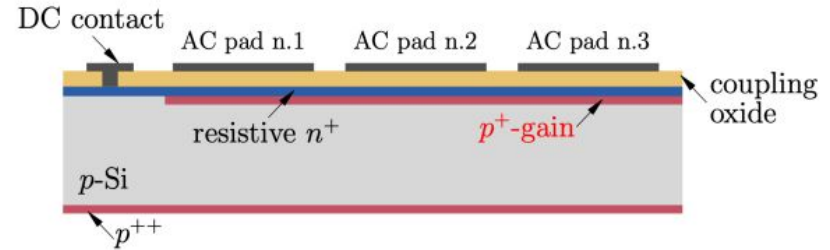
- Middle Barrel At 13 cm radius
- Similar for outer barrel
- 22 staves of 8 modules each.
- Lightweight reticular support structure (ALICE/Belle-II like)
- Readout chips either side
- Power budget ~ 342 W
- Total weight ~ 1 kg Water cooled (2 pipes of 2 mm diameter)
- Similar prototype built @ Pisa



Total surface for silicon wrapper about 20 times that of vertex detector and with similar pitch
⇒ $O(10^{11})$ channels

Resistive Silicon Detectors (RSD), an AC-coupled LGAD sensor, can provide high-resolution position measurement with relatively large pitch

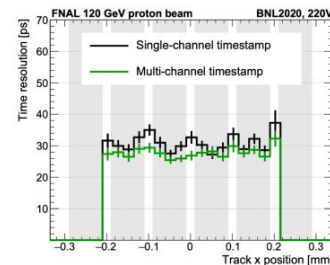
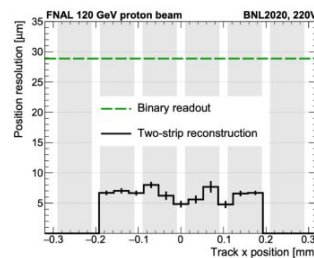
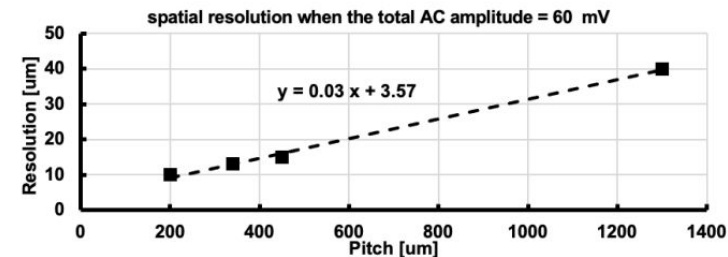
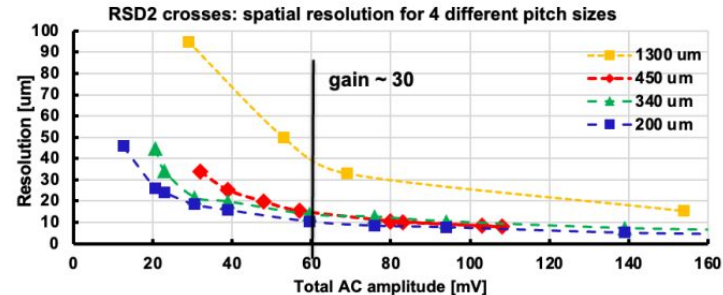
- Suitable for low occupancy environments
- Comparable material thickness
- Precision time measurement possible
- Ongoing R&D on implementation in DMAPS structures



Position performance measured with laser and several test beams with 120 GeV proton beam @ FNAL

Different pitch size tested and with a gain of 30 it achieves a spatial resolution of about 3% of the pitch size.

The time resolution achieved is about 30 ps, independently from the pitch used.

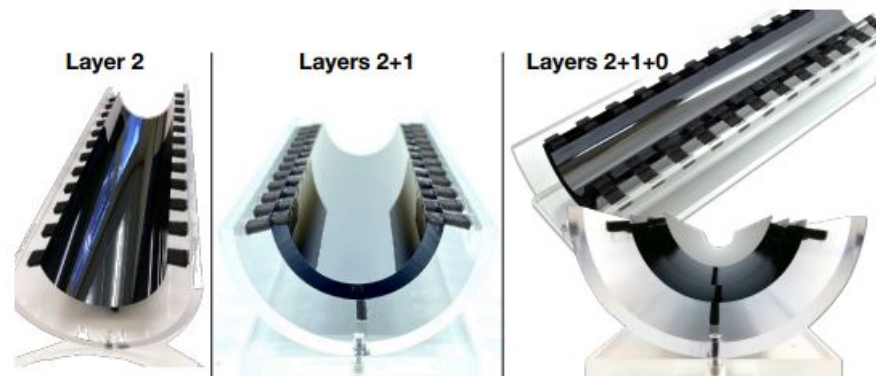


DMAPS in 65 nm TPSCo process with bended detector.

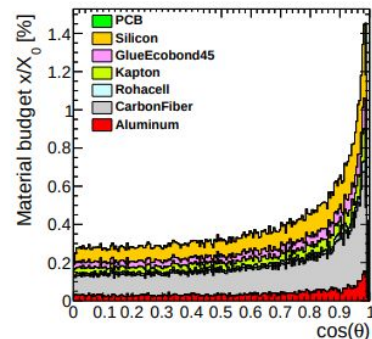
The **IDEA first layer** has a smaller radius comparing to ALICE, from 18 to 13.7 mm. Mechanically is okay, electrically to be demonstrated.

Proposal for FCC-ee to use **4 segments** (stitching)

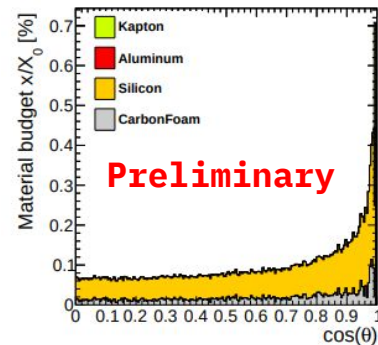
Preliminary material budget evaluation measure a **reduction of about 3-5 times** with respect standard geometry.



Layer 1 (classic vertex)



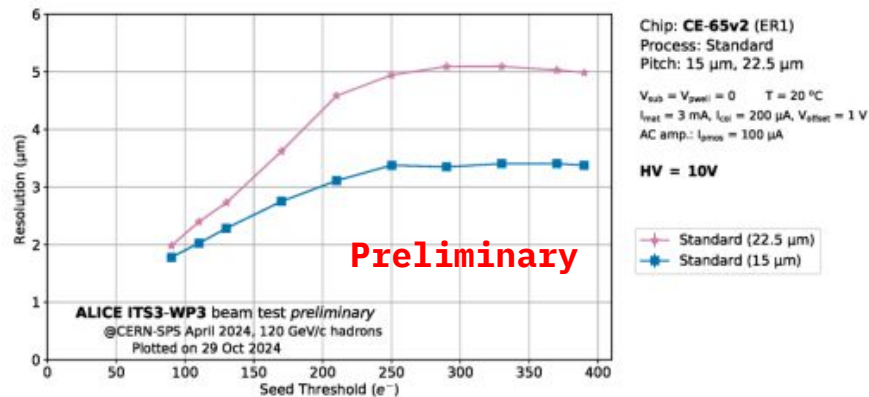
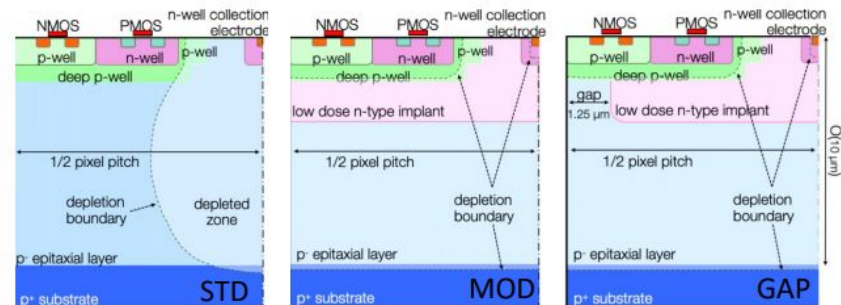
Ultra-light layer 1



Optimized CMOS Technology for Precision in Ultra-thin Silicon is a project for simulation, development and characterization of MAPS in TPSCo 65 nm CMOS Imaging Process targeting future Lepton Collider specifications with this focus:

- Single point resolution: 3 μm
- Time resolution: 5 ns
- Power dissipation: 50 mW/cm²
- Thickness: 50 μm
- Readout architecture scalable to large area
- 48 \times 24 pixel chip with analogue readout
- **3 process variants (STD, MOD, GAP) for depletion control**
- 3 pixel pitches: 15, 18, 22.5 μm
- AC pixels with $V_{\text{bias}} = (0; 10) \text{ V}$

Ongoing test beam campaign reported preliminary interesting results.

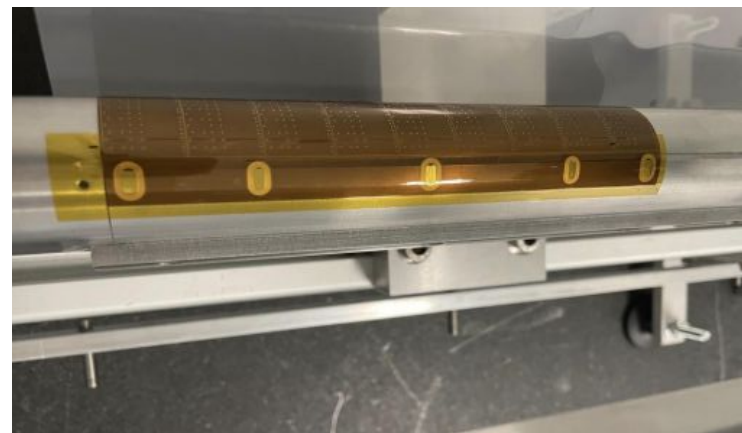
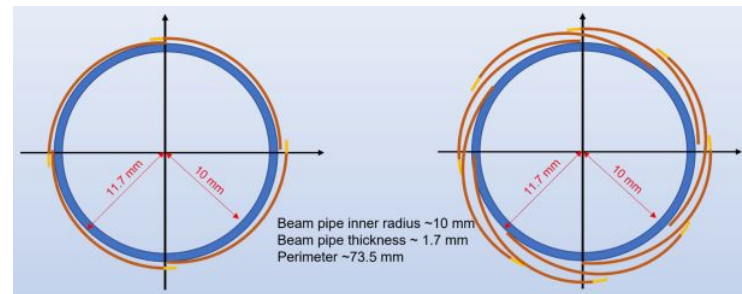


FCC-SEED is a full silicon ladders of 6/8 reticles

- With or without stitching along Z
- Ladders are bent to provide mechanical stiffness to build the layer and to reduce the material budget
- **Full r-phi coverage**
- Stitching yield less critical
- **Less constrain on the layer radius**

Project to build a prototype of a FCC-SEED layer using **MIMOSIS chips**

- Different thicknesses: 30/40/50 μm
- Different radii: 18/15/12/10 mm



Gaseous detector

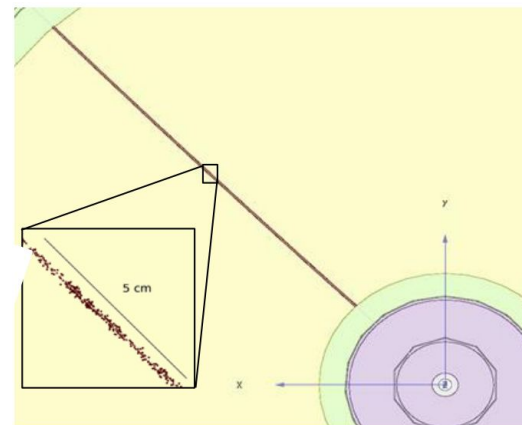
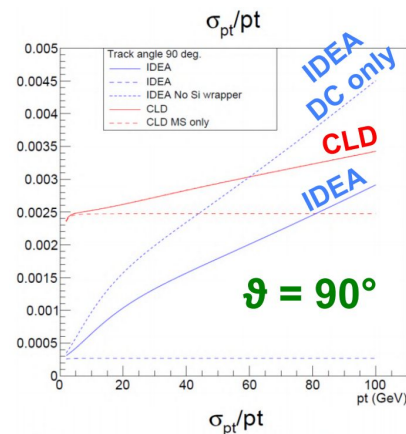
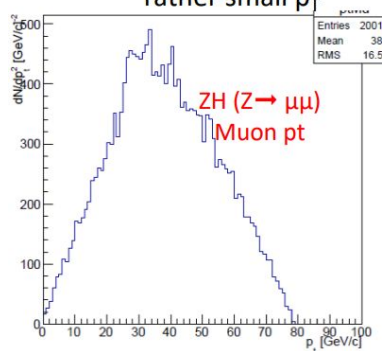
Gaseous detector are used for tracking purpose and also for muon measurement, timing and calorimetry.

The **low material budget** reduce the contribution on multiple scattering and improves the momentum resolution

Multiple measurement of the track path 0(100) allows together with the momentum measurement, the particle identification.

I did a personal selection on the technology discussed in the next slides: Drift chamber, Straw tube and TPC.

muons in ZH events have rather small p_T



Dimensions:

- 4m long (active volume)
- 35cm to 2m radius

Low material budget

- Barrel: $\sim 1.6\% X_0$
- Forward directions: $\sim 5\% X_0$

Stereo layout

- 112 layers ranging from 50 to 250 mrad

Operating gas mixture: He + 10% iC4H10

- Average drift velocity of $\sim 2 \text{ cm}/\mu\text{s}$
 \rightarrow drift time $t_0 < 400 \text{ ns}$

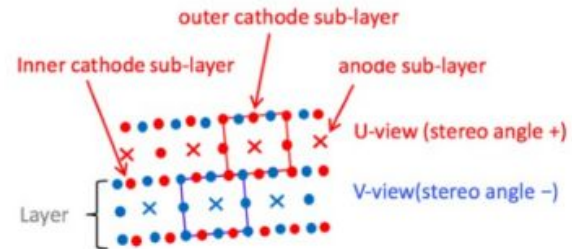
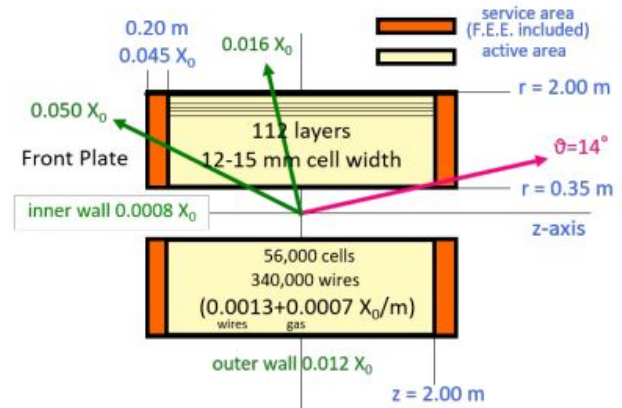
Sense vires: 20 μm diameter W(Au) \Rightarrow 56448

Field vires: 40 μm diameter Al(Ag) \Rightarrow 229056

F. and G. vires: 50 μm diameter Al(Ag) \Rightarrow 58464

Active volume: 56448 almost **squared drift cells**
(12 \div 14.5 mm)

Expected resolution: $\sigma_{xy} \sim 100 \mu\text{m}$ and $\sigma_z \sim 1 \text{ mm}$



5 : 1 field-to-sense wire ratio

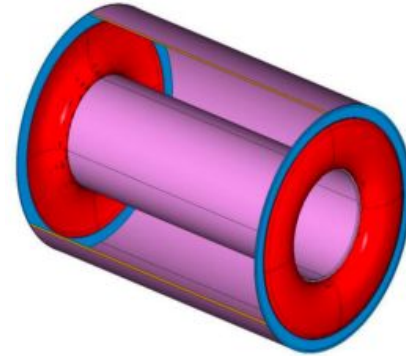
The mechanical design of the drift chamber is driven by two main objectives:

- Maximizing its transparency in terms of radiation length.
- Maximizing its mechanical stability by reducing to acceptable limits the deformations of the endplates under the total load of the wires.

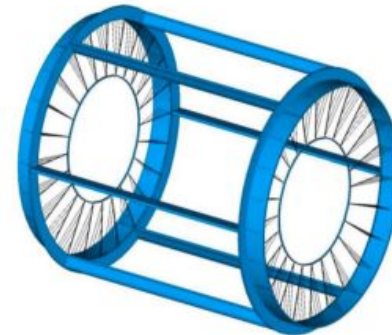
Gas containment: gas vessel can freely deform without affecting the internal wire position and mechanical tension.

Wire cage: wire support structure not subject to differential pressure can be light and feed-through-less

Gas containment



Wire cage



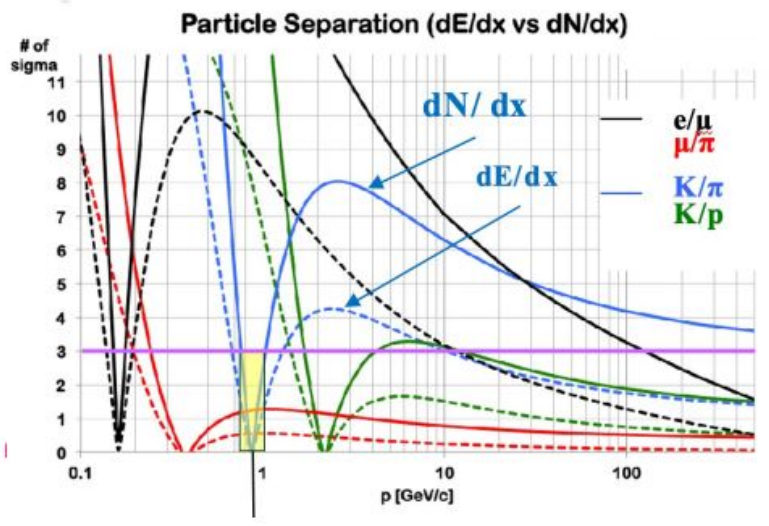
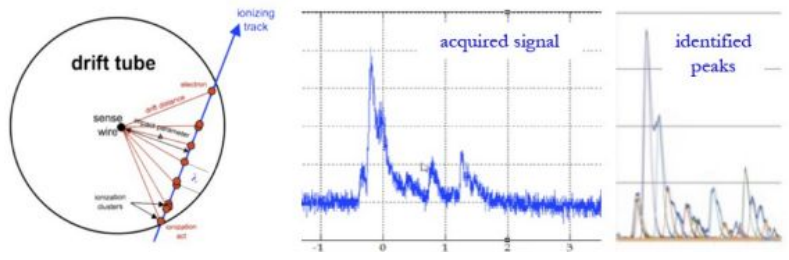
In **He based gas mixtures** the signals from each ionization act can be spread in time to few ns. With the help of a **fast read-out electronics** they can be identified efficiently.

By counting the number of ionization acts per unit length (dN/dx), it is possible to identify the particles (P.Id.) with a better resolution w.r.t the dE/dx method.

Landau distribution of dE/dx originated by the **mixing of primary and secondary ionizations**, has **large fluctuations** and limits separation power of PID

Primary ionization is a Poisson process, has small fluctuations

The cluster counting is based on replacing the measurement of an **ANALOG** information (the [truncated] mean dE/dx) with a **DIGITAL** one, the number of ionisation clusters per unit length.



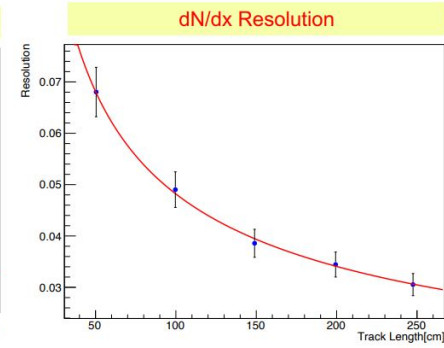
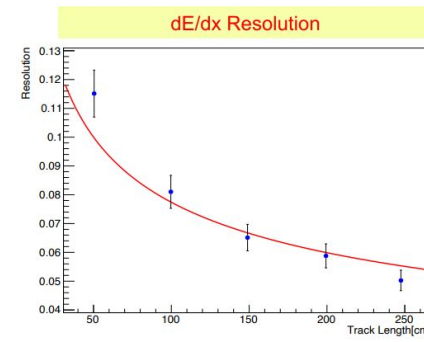
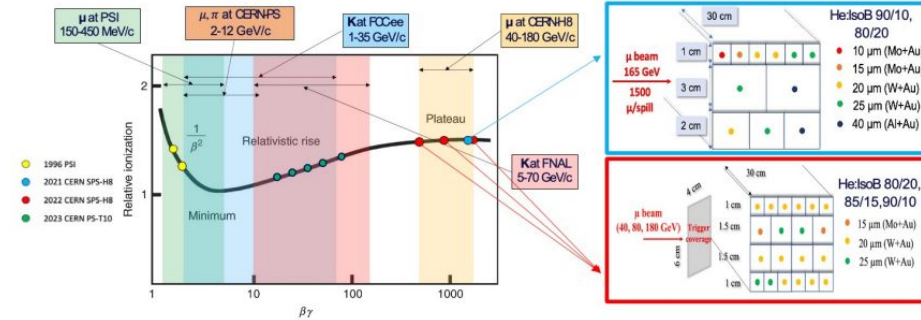
Beam tests to experimentally assess and optimize the performance of the **cluster counting/timing**.

dE/dx measurement has an **optimized truncation empirically**: selected the distribution with 80% of the charges.

To accurately identify electron peaks, Derivative Algorithm (DERIV) and Running Template Algorithm (RTA) are developed.

A factor 2 improvement in the resolution using dN/dx method.

Needs to be demonstrate its performance in cell with large occupancy



Reasonable single hit resolution: 120 μm

Similar or better momentum resolution than the pixel detector due to more hits detected and a longer lever arm

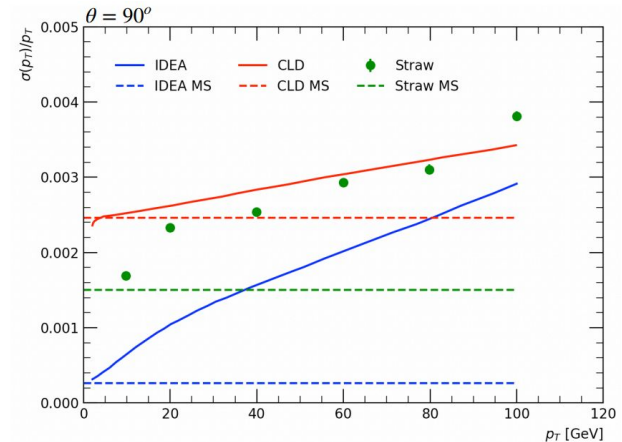
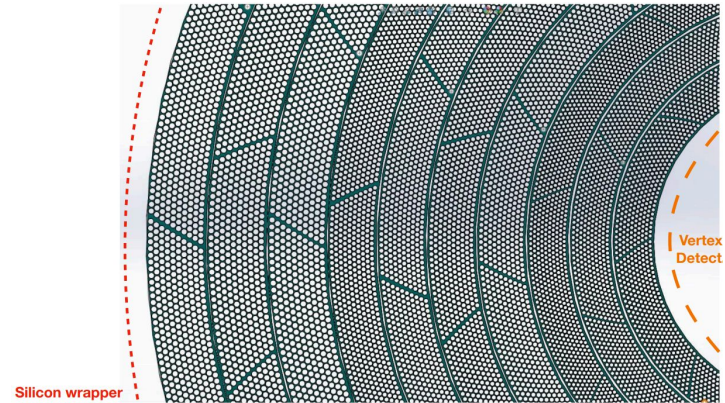
Low material and less multiple scattering with straws

Particle identification (π -K, K-p identifications)

Mylar wall thickness of 12 μm and 100 layers means ~ 3 mm Mylar films $\sim 1.2\% X_0$

Inner wall 0.05 μm Aluminum coating
Sense wire: radius of 10 μm Tungsten

Endplate supporting structure could be similar to DCH



Ongoing Garfield simulation to provide essential inputs for the gas optimization and $dE/dx(dN/dx)$ measurement for PID

- **Ar-based gas:**

high ionization density
(~40 clusters/cm)

moderate electron drift velocity
50 $\mu\text{m/ns}$ (@ $E \sim 2\text{kV/cm}$)

Mean cluster arrival time separation:

~5 ns

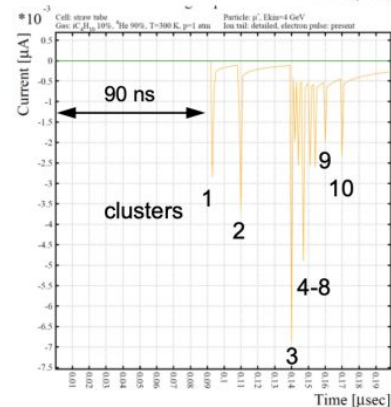
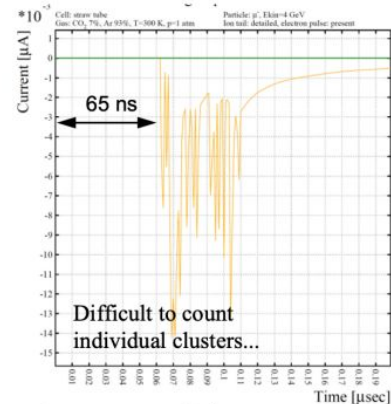
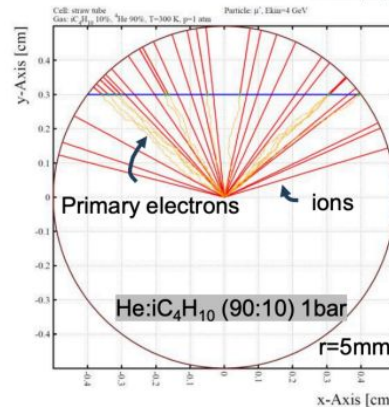
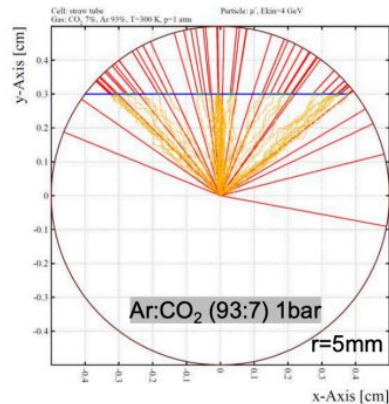
- **He-based gas:**

lower ionization density
(~15 clusters/cm)

drift velocity
30 $\mu\text{m/ns}$ (@ $E \sim 2\text{kV/cm}$).

Mean cluster arrival time separation:

~15 ns



Test beam at CERN H4 & T9

Straw diameters: 5, 10, 20 mm to evaluate operation working points (high voltage, thresholds, gas)

Tube-wall 36 μm coated with 20 nm gold and 70 nm copper

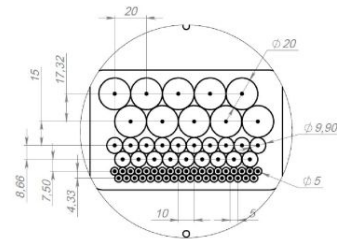
Central wire diameter 30 μm

2 prototypes (from different production) of straw chambers used

Straw signal amplified by NIM amplifiers. Digitized by an oscilloscope.

Individual clusters could be seen from the raw signal.

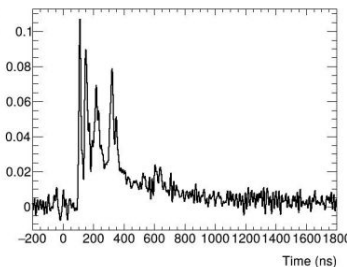
Cluster counting algorithm to be developed.



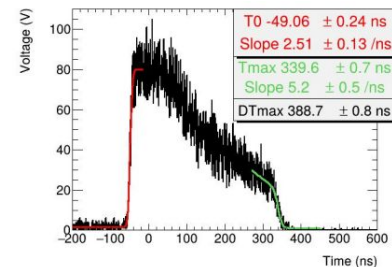
Prototype 1: 5 mm, 10 mm, and 20 mm tubes area



Prototype 2: 10 mm tube area only. Two planes of X, two planes of U (2'), two planes of V (-2') and two planes of X.



Raw signal from the straw tube



First leading edge time spectrum

Time Projection Chamber

$$0.3\text{m} < r < 1.8\text{m}$$

$$|z| < 2.4\text{ m}$$

highly-redundant pattern recognition

dE/dx and dN/dx measurement PID

Material budget is

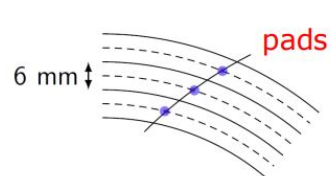
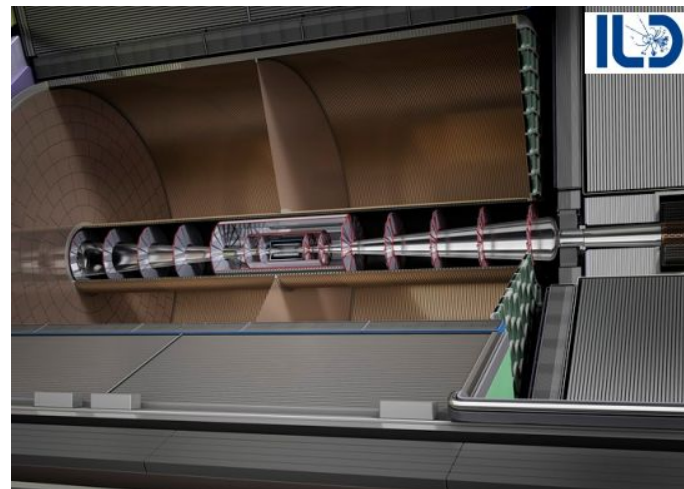
$$0.01 X_0 \text{ TPC gas}$$

$$0.01 X_0 \text{ inner cylinder}$$

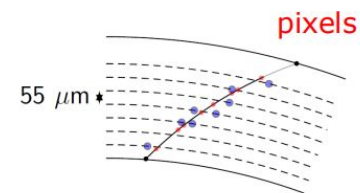
$$0.03 X_0 \text{ outer cylinder}$$

$$< 0.25 X_0 \text{ endplates (incl readout)}$$

Note the very low budget in the barrel region. Material budget can be respected by different technologies like **GEM, MicroMegas and Pixels**



22 electrons / hit
~ 200 hits / track



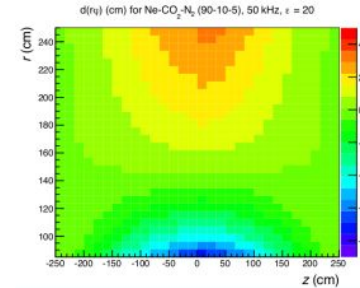
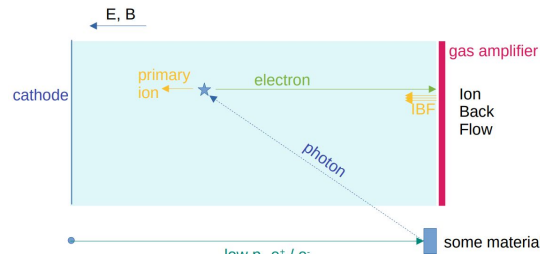
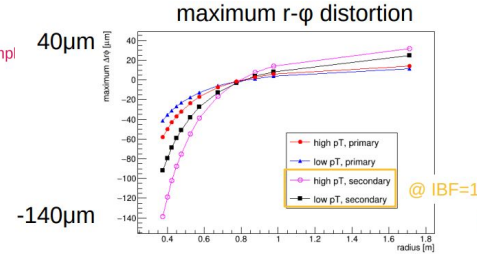
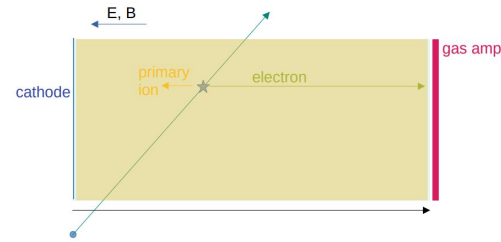
1 electron / hit
~ 10 000 hits / track

Large ionization at the Z peak produces a large ion density. As ions drift very slowly (0m/s) the TPC integrates a high ion space charge, which causes a transverse electric field, which in turn makes large distortions of the ionization electrons paths.

A distortions for the tracks at the Z pole, counting only the ionization from physics events is expected to be about $150\ \mu\text{m}$

Beamstrahlung background produced ~ 200 times more ionization than hadronic Z decays and a distortion of 1-10 cm is estimated.

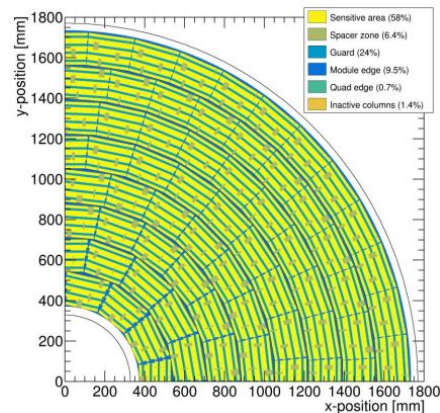
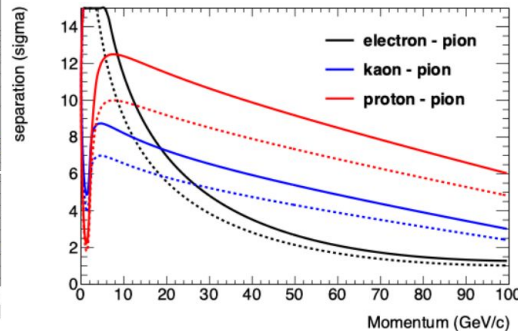
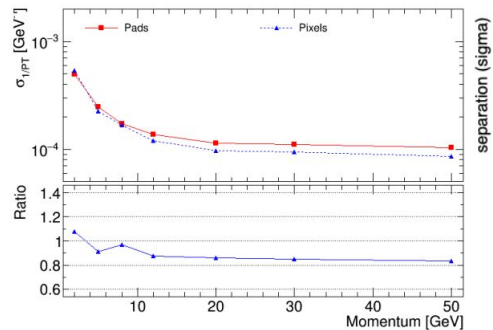
Correcting for this is necessary and is similar to what Alice attempts to do at the Pb-Pb run



To study the performance of a large pixelized TPC, the pixel readout was implemented in the full ILD DD4HEP simulation with a coverage 59%.

The expected pion-kaon separation for momenta in the range of 2.5-45 GeV/c at $\cos\theta = 0$ is more than $5.5(4.5)\sigma$

At a momentum of 100 GeV/c the separation is still $3.0(2.0)\sigma$.



Conclusion



An overview on the ongoing activities for tracking system are presented.

The current prototypes performance are close to the FCC-ee requirement but R&D is mandatory to match this performance in the final detector. Collaboration between institutes and **DRDs** is a key element to develop the detectors for future lepton colliders and to enlarge the scientific community.

Silicon detector can achieve a few μm spatial resolution and tens ps time resolution if an amplification layer is used.

Bended silicon detectors can reduce significantly the material budget and provide full r - ϕ coverage.

Gaseous detectors, the best choice for a low material budget, need to face-off the stringent mechanical requirement on such a large area detector.

Beam induced background has to be take into account for the occupancy and its impact on dN/dx in drift chamber and straw tubes, for the ion distortion in the TPC.



Thank You!