



# ITk pixel system test

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<u>Thanks to contributions from:</u> Marianna Testa, Matteo Beretta, Maurizio Gatta









# ≻Target:

#### System test scale up for EC integration.

- General DCS plans
- MOPS chips readout system
- Proposal of using MOPS board for HRs thermal test

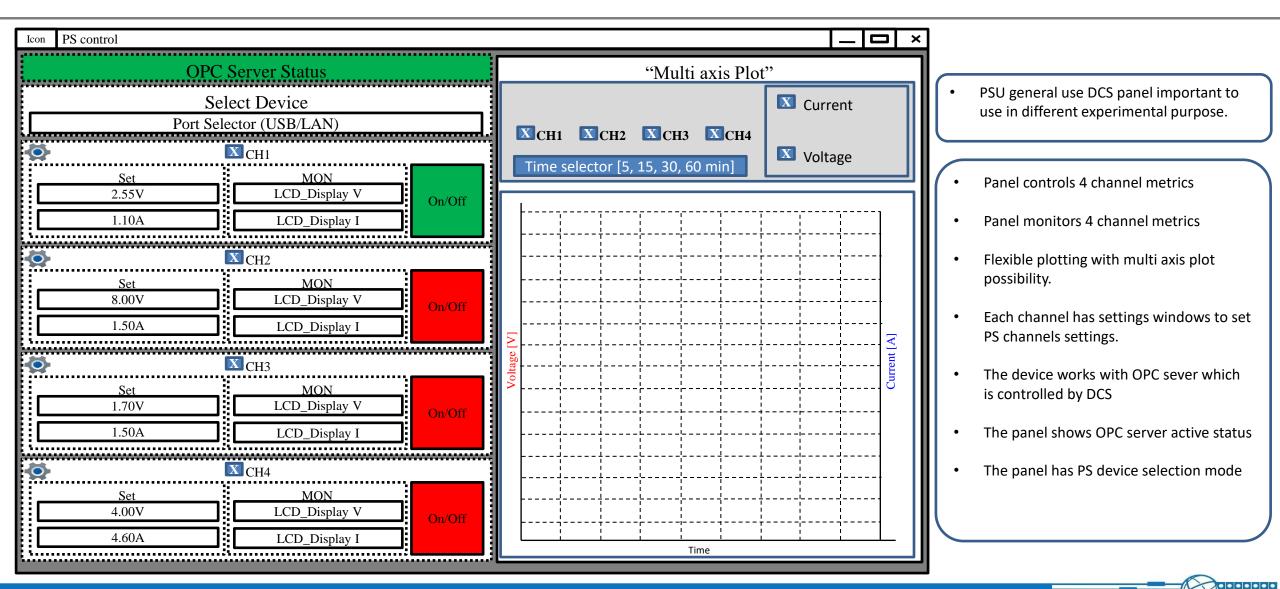


9<sup>th</sup> October 2024

**Status** 

## **DCS Power supply general use Panel**

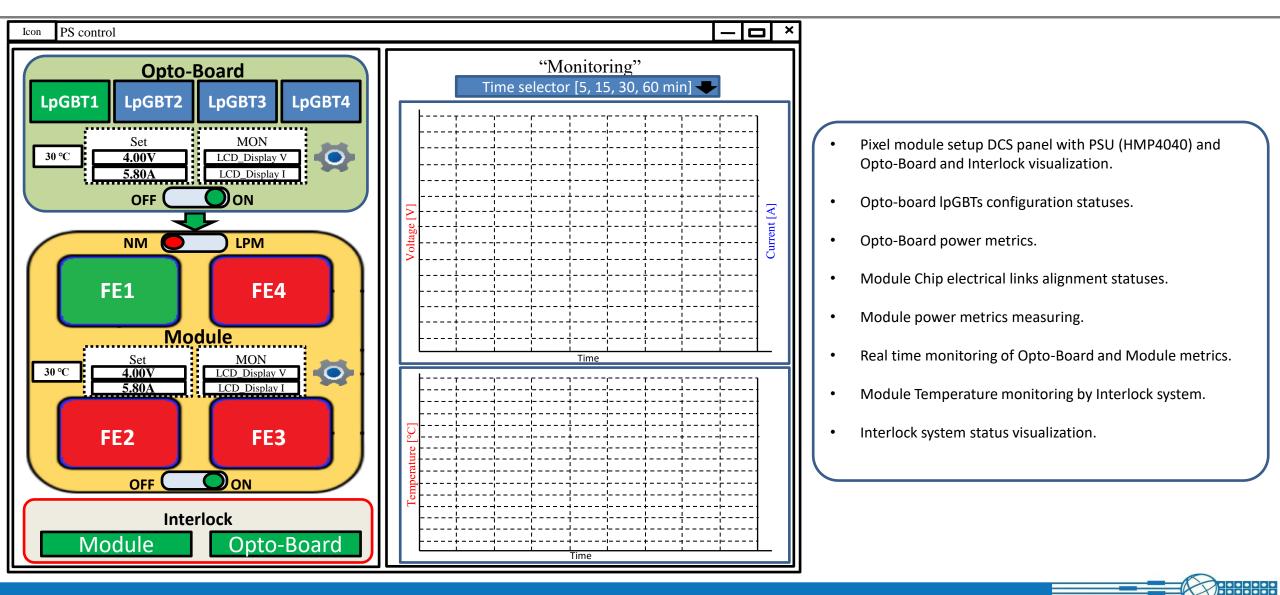




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### **DCS Panel of pixel module setup**

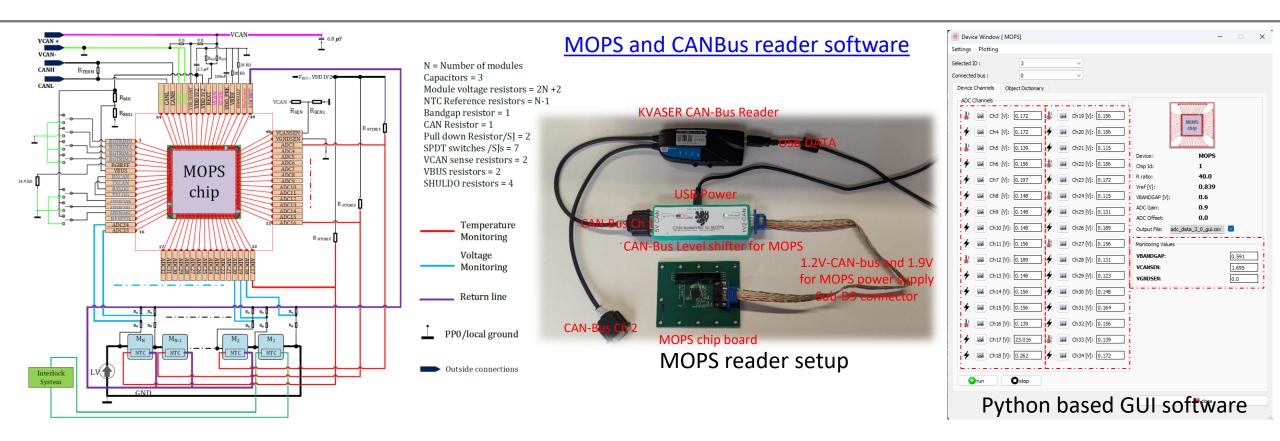




**Status** 

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#### **MOPS reader with Can-Bus**



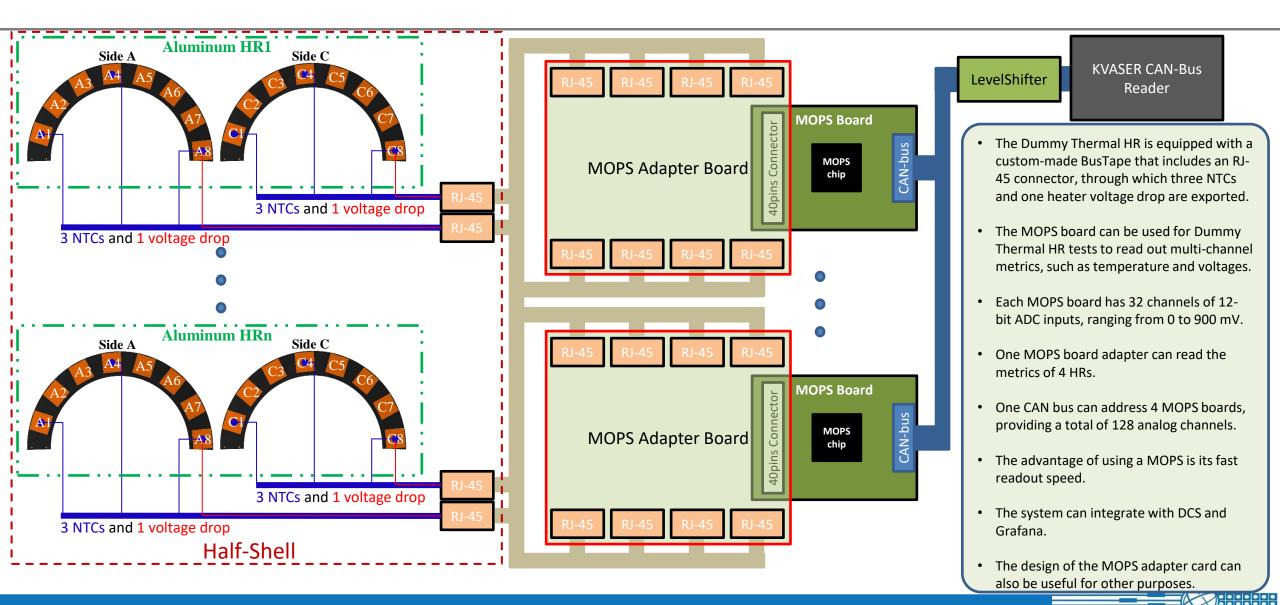
- The MOPS chip provides 40 analog ADC channels with 12-bit resolution and a voltage range of 0 to 900 mV. Of these 40 channels, 8 are dedicated to measuring the internal metrics of the MOPS chip, while the remaining 32 are available for general use.
- The CAN level shifter is a custom-made small circuit that translates and isolates the CAN bus signals between the MOPS chip and a standard CAN physical layer. It features a D-SUB9 connector on both sides.
- The purpose of the CAN level shifter circuit is to isolate and translate signals between two CAN physical layers operating at different voltage levels. While a standard CAN physical layer works at 3.3 or 5 V, the MOPS physical layer operates at 1.2 V.
- The MOPS board is controlled and monitored using a Python-based GUI software, which displays the readings from all channels.

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9<sup>th</sup> October 2024

ATLA

#### **Proposal of MOPS board using for full loaded dummy HRs in HS thermal tests**



**Status** 



# Thank You For Your Attention! <u>Any Questions?</u>



**ITk Week** 

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