

Session Program

Jan 20 - 24, 2025

ePIC Collaboration Meeting

Silicon Vertex Tracker Session 2

Villa Mondragone, Monte Porzio Catone (RM), Italy, Sala degli Svizzeri
Via Frascati, 51, 00040 Monte Porzio Catone (RM), Italy

Tue, January 21

2:00 PM

Silicon Vertex Tracker Session 2

Session

Location: Villa Mondragone, Monte Porzio Catone (RM), Italy, Sala degli Svizzeri, Via Frascati, 51, 00040 Monte
Co-organizers: (RM) Silberman, Laura Gonella

2:00 - 2:15 PM

Testing and characterization - next steps (continued)

2:20 - 2:40 PM

Sensor and ancillary ASIC power dissipation estimates

Speaker

Iain Sedgwick

2:45 - 2:55 PM

SVT power dissipation

Speaker

Georg Viehhauser

3:00 - 3:20 PM

SVT cooling

Speaker

Nicole Apadula

3:25 - 3:55 PM

Data rates

Speaker

Jo Schambach

4:00 - 4:30 PM

Afternoon break

4:35 - 5:35 PM

IB prototyping, FEA, and plans

Speakers

Domenico Elia, Maria Teresa Camerlingo, Rosario Turrisi

Subcontributions

Introduction and overview

Speaker

Domenico Elia

Bending and assembly of the L0/L1 layers

Speakers

Maria Teresa Camerlingo, M Teresa Camerlingo

Mechanics and FEA studies

Speaker

Rosario Turrisi

6:00 PM