



# NEST GPU Workshop 2024

## Wednesday, 23 October 2024

### MPI and simulation loop optimization (14:00 - 15:00)

time	[id] title	presenter
14:00	[30] Exploiting network topology in brain-scale multi-area model simulations	KUNKEL, Susanne
14:15	[31] Towards spatially organized networks with NEST GPU	VILLAMAR, Jose
14:30	[32] Studies for MPI communication optimizations	Dr SIMULA, Francesco PONTISSO, Luca
14:45	[33] Introduction to CINECA and ICSC HPC systems	DI BARI, Daniele