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## Upgrade of the CMS Level-1 Trigger

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Various parts of the CMS Level-1 hardware trigger will be upgraded to cope with increasing luminosity, using more selective trigger conditions at Level-1 and improving the reliability of the system. Many trigger subsystems use FPGAs (Field Programmable Gate Arrays) and will benefit from developments in this technology, allowing much more logic into a single FPGA chip, thus reducing the number of chips, electronic boards and interconnections and in this way improving reliability. A number of subsystems plan to switch from the old VME bus to the new microTCA crate standard. Using similar approaches, identical modules and common software wherever possible will reduce costs and manpower requirements and improve the serviceability of the whole trigger system.

### **for the collaboration**

CMS

**Primary author:** Prof. JEITLER, Manfred (HEPHY Vienna)

**Presenter:** Prof. JEITLER, Manfred (HEPHY Vienna)

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