

THE MICRO-COOLED LIGHT SUPPORT OF THE PIXEL MODULES FOR THE SUPERB EXPERIMENT

In HEP experiments the use of pixel detectors requires that high power density in the sensitive area should be carried away by efficient thermal systems.

Micro-channel cooling technology is featured by high efficient thermal exchange and it can profit by miniaturization technique applied on composite material (CFRP).

The key concept

In a thermal convective exchange the h film coefficient is:

Nu = Nusselt number

k = Conductive heat transfer coefficient of the liquid

D_h = Hydraulic Diameter of the cooling channel

$$h = \frac{Nu \cdot k}{D_h} \quad (1)$$

$$Q = h S (T_w - T_f) \quad (2)$$

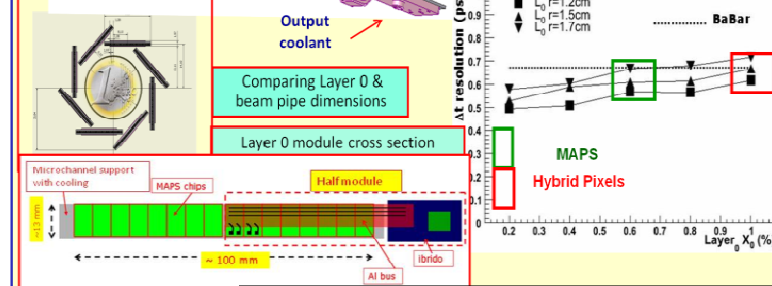
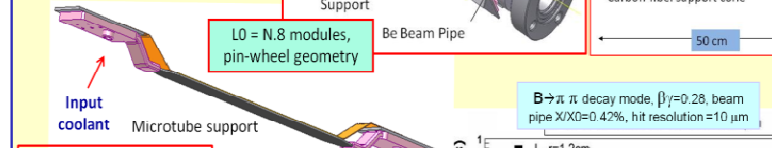
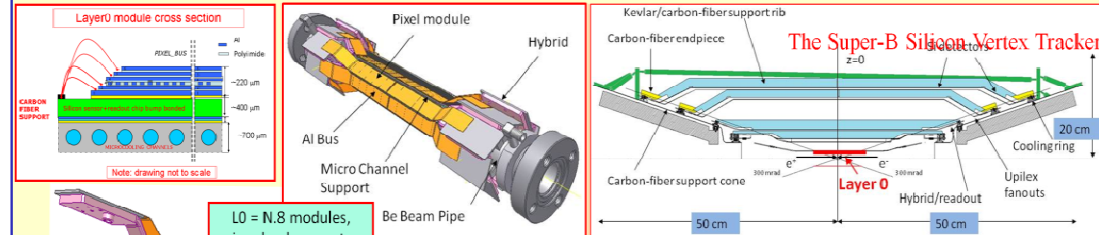
From formulas (1) and (2), for a Laminar flow fully developed, (Nu =constant), to maximize the thermal exchange $Q \rightarrow$ to maximize $h \rightarrow$ to minimize the hydraulic diameter: all these considerations brings to the **micro-channel technology**.

D_h minimization \rightarrow high pressure drop \rightarrow (needed a compromise between pressure drops and film coefficient value).

Super-B Silicon Vertex Tracker

The micro-channel mechanical support is designed to match the specifications for the planned pixel upgrade of the most internal layer (L0) of the Silicon Vertex Tracker of the Super-B experiment:

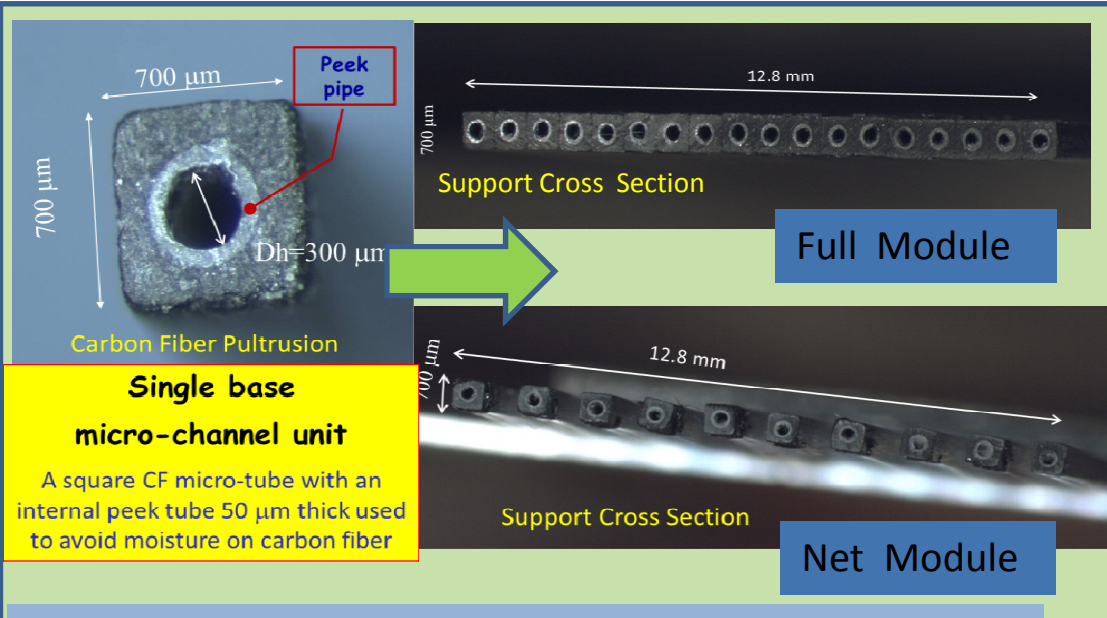
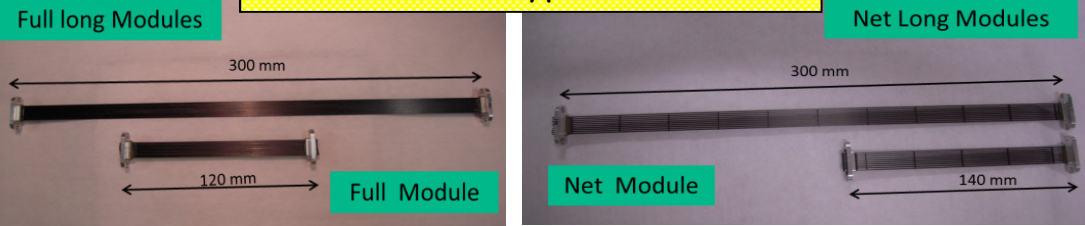
- To evacuate the heat dissipated by the electronics (specific power up to 2 W/cm²) and operating temperature of the sensors below 50°C
- Material budget of the pixel support structure and cooling(w/o cables/sensors) below 0.30% X_0 .



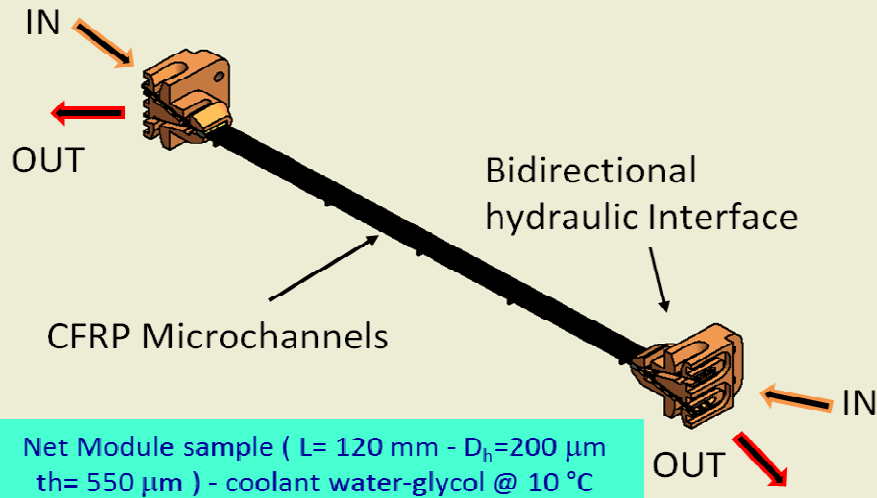
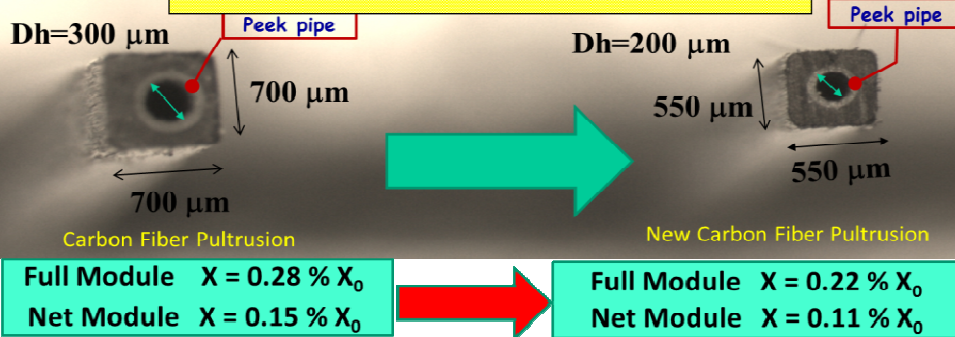
SuperB SVT based on Babar SVT design for $R > 3\text{cm}$. BUT:

- 1) reduced beam energy asymmetry ($7 \times 4 \text{ GeV}$ vs. $9 \times 3.1 \text{ GeV}$) requires an improved vertex resolution (\sim factor 2)
 - Layer0 very close to IP (@1.5 cm) with low material budget ($< 1\% X_0$) and fine granularity (50 μm pitch)
 - Layer0 area 100 cm²
- 2) bkg levels depend steeply on radius
 - Layer0 needs to be fast and rad hard ($> 20 \times 5 \text{ MHz/cm}^2$, $> 3 \times 5 \text{ Mrad/yr}$)

Module Prototype Production



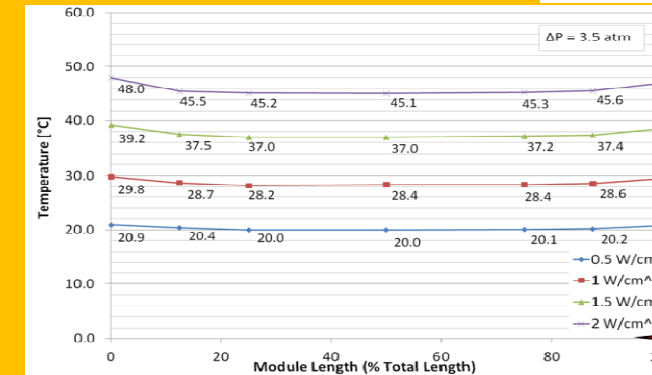
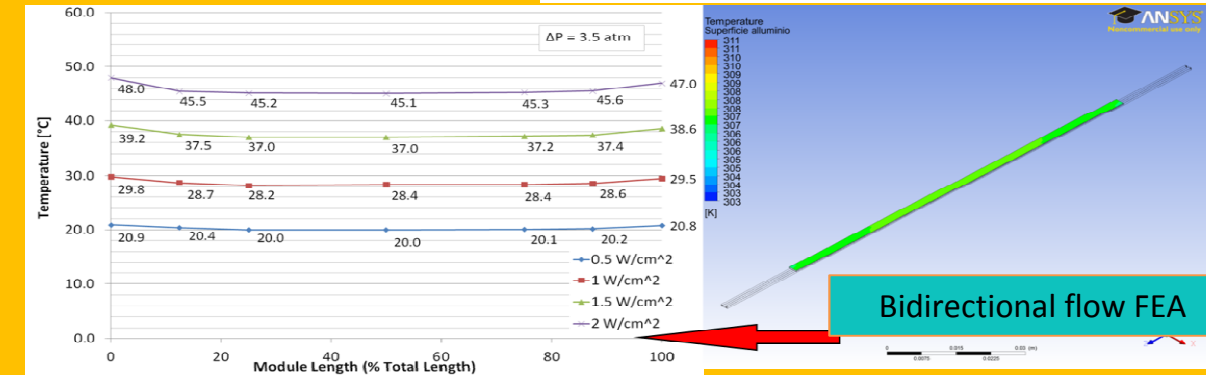
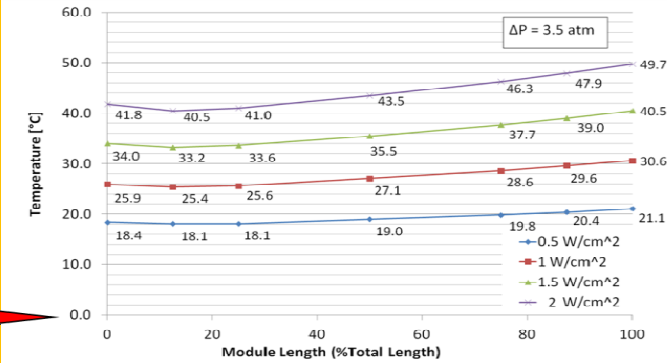
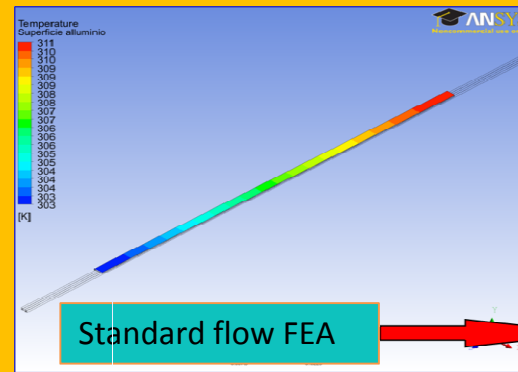
Further miniaturization



Breaking Test



Structural test was realized on Net Module and Full Module prototypes. The test has been performed up to 140 atm . At this pressure the flanges of hydraulic interface were deformed to extrude the seal and produced the failure.



The micro-channel CFRP prototypes match the Super-B Layer 0 pixel detector requirements on material thickness (X_0). An efficient heat evacuation has been achieved by micro-channel technology through liquid forced convection. The experimental results show that the Net Module is able to cool sensors with a power density up to 1.5 W/cm² with a X_0 value of 0.11 % and keep the sensor below 50 °C, as requested from specs. Moreover, with bidirectional coolant flow, it is possible to reduce the ΔT along the sensor below 2°C . Further optimization currently under development at the TFD Pisa laboratory: in progress the set-up for transition phase CO₂ cooling on CFRP micro-channels.