## FRONTIER DETECTORS FOR FRONTIER PHYSICS



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## Measurement Results of DIPIX Pixel Sensor Developed in SOI Technology

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The development of integration type pixel detectors presents interest for physics communities because it brings optimization of design, simplicity of production - which means smaller cost, and reduction of detector material budget. During the last decade a lot of research and development activities took place in the field of CMOS Silicon-On-Insulator (SOI) technology resulting in improvement in wafer size, wafer resistivity and MIM capacitance. Several ideas have been tested successfully and are gradually entering into the application phase. Some of the novel concepts exploring SOI technology are pursued at KEK; several prototypes of Dual mode Integration type PIXel (DIPIX) have been recently produced and described. This report presents initial test results of some of the prototype including tests obtained with the infrared laser beam and Americium (Am-241) source. The Equivalent Noise Charge (ENC) of 85e- has been measured. The measured performance demonstrates that SOI technology is a feasible choice for future applications.

**Primary author:** Mr AHMED, Mohammed Imran (AGH University of Science and Technology, Faculty of Physics and Applied Computer Science)

**Co-authors:** Prof. IDZIK, Marek (AGH University of Science and Technology, Faculty of Physics and Applied Computer Science); Prof. TURALA, Michal (Institute of Nuclear Physics Polish Academy of Sciences); Dr KAPUSTA, Piotr (Institute of Nuclear Physics Polish Academy of Sciences); Dr MIYOSHI, Toshinobu (Institute of Particle and Nuclear Studies, High Energy Accelerator Research Organization(KEK)); Prof. ARAI, Yasuo (Institute of Particle and Nuclear Studies, High Energy Accelerator Research Organization(KEK))

**Presenter:** Mr AHMED, Mohammed Imran (AGH University of Science and Technology, Faculty of Physics and Applied Computer Science)

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