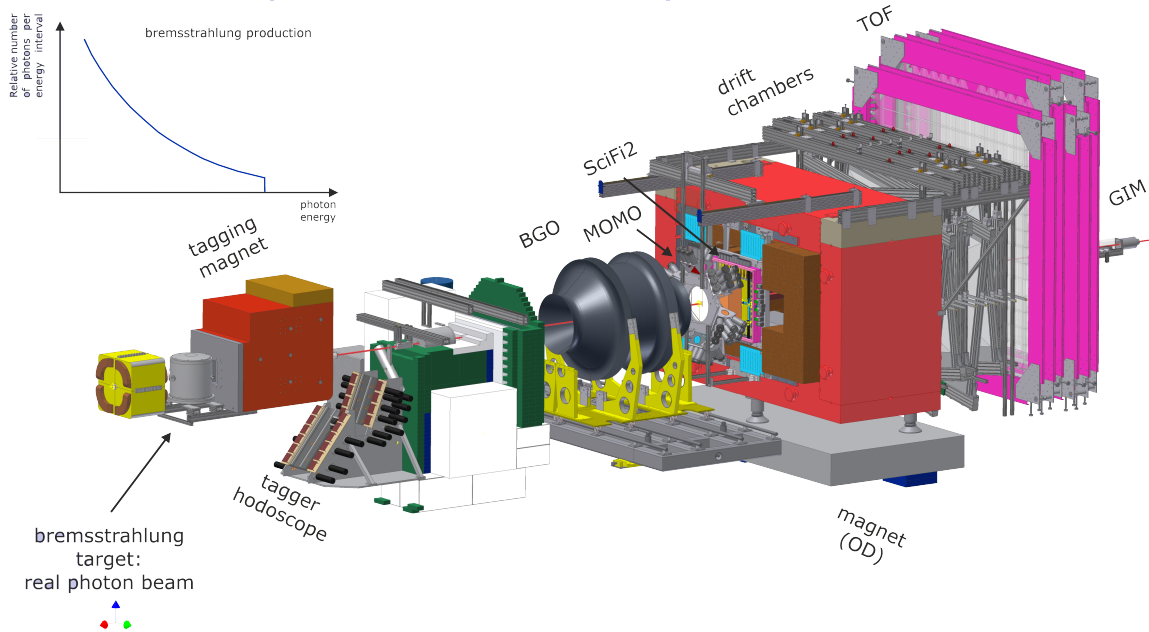


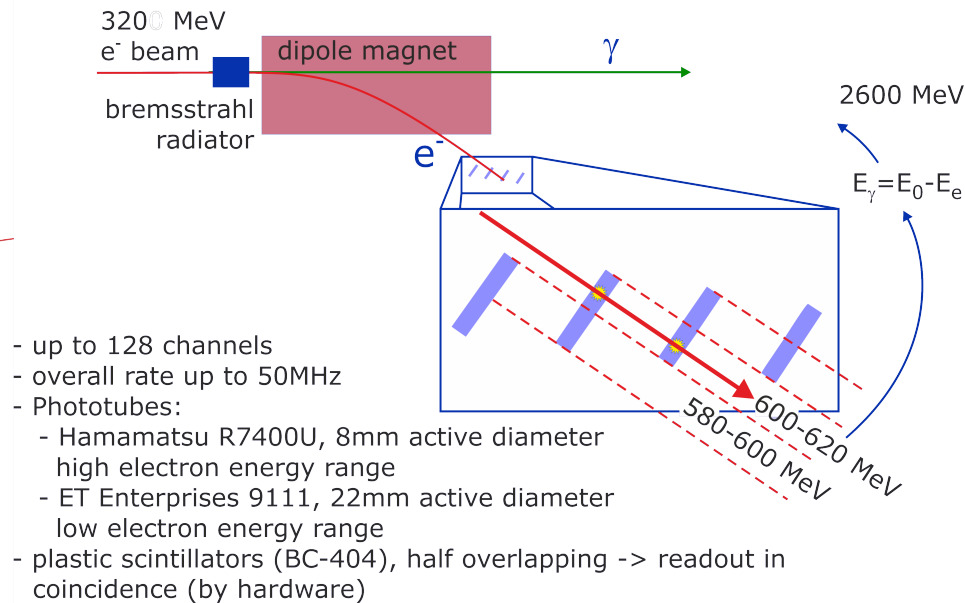
# Front-End electronics for the Tagger of the BGO-OD experiment

Supported by DFG - SFB/TR16

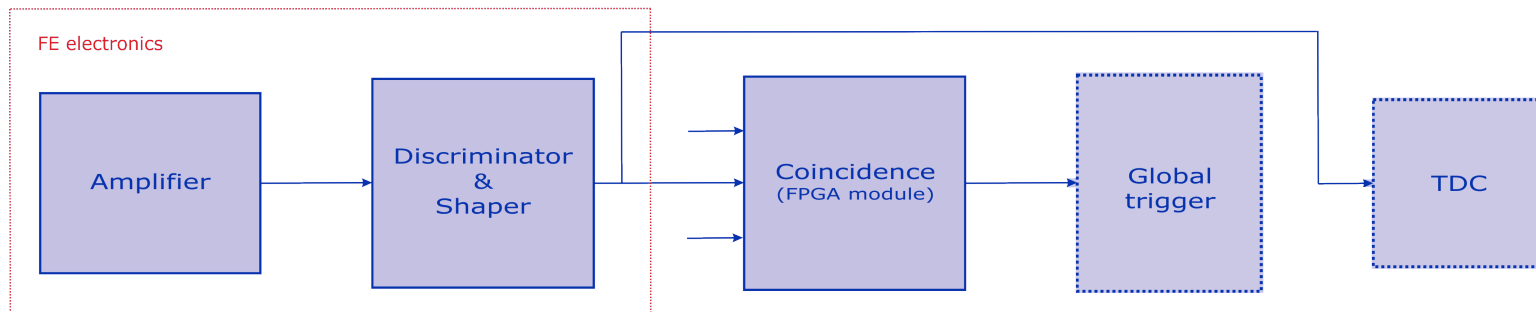
## Overall Experimental Setup



## Tagger concept:



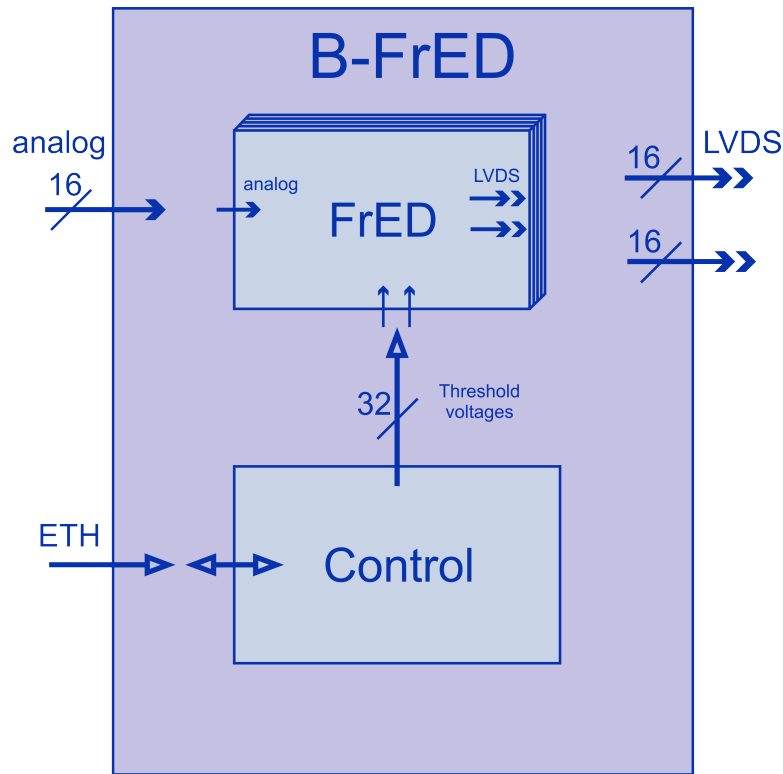
## Electronic chain:



# Front-End electronics for the Tagger of the BGO-OD experiment

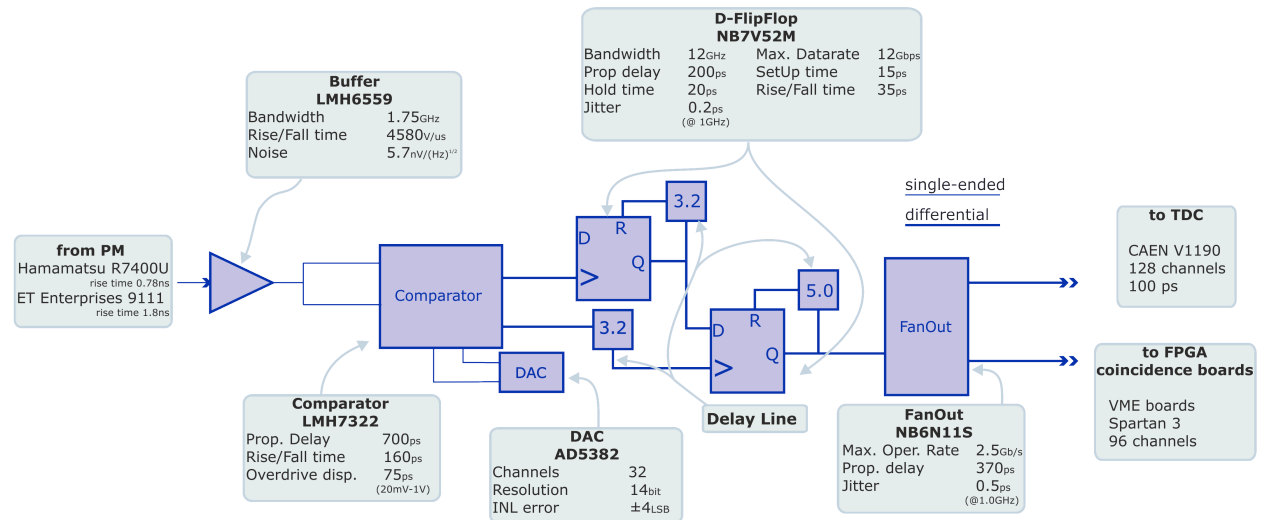
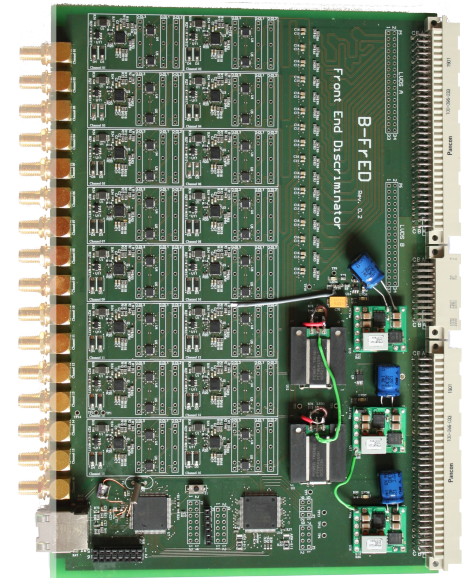
Supported by DFG - SFB/TR16

## Discriminator & Shaper



Status: prototype full tested,  
Final boards under test

- ) 16 analog inputs
- ) 2x16 LVDS outputs
- ) double threshold discriminator
- ) expected jitter < 8ps
- ) shaper by D-FlipFlop and delay lines
- ) daughter boards for the delay lines
- ) microcontroller on board
- ) ethernet connection for Slow-Control



Logical scheme and components used for the FrED channels